

A Case for Digital Beamforming at mmWave

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Abstract

Due to the heavy reliance of millimeter-wave (mmWave) wireless systems on directional links, beamforming (BF) with high-dimensional arrays is essential for cellular systems in these frequencies. How to perform the array processing in a power efficient manner is a fundamental challenge. Analog and hybrid BF require fewer analog-to-digital and digital-to-analog converters (ADCs and DACs), but can only communicate in a small number of directions at a time, limiting directional search, spatial multiplexing and control signaling. Digital BF enables flexible spatial processing, but must be operated at a low quantization resolution to stay within reasonable power levels. This decrease in quantizer resolution introduces noise in the received signal and degrades the quality of the transmitted signal. To assess the effect of low-resolution quantization on cellular system, we present a simple additive white Gaussian noise (AWGN) model for quantization noise. Simulations with this model reveal that at moderate resolutions (3-4 bits per ADC), there is negligible loss in downlink cellular capacity from quantization. In essence, the low-resolution ADCs limit the high SNR, where cellular systems typically do not operate. For the transmitter, it is shown that DACs with 4 or more bits of resolution do not violate the adjacent carrier leakage limit set by 3rd Generation Partnership Project (3GPP) New Radio (NR) standards for cellular operations. Further, this work studies the effect of low resolution quantization on the error vector magnitude (EVM) of the transmitted signal. In fact, our findings suggest that low-resolution fully digital BF architectures can be a power efficient alternative to analog or hybrid beamforming for both transmitters and receivers at millimeter wave.

Index Terms

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I. INTRODUCTION

The need for more bandwidth, driven by ever higher demand, has brought millimeter wave (mmWave) communication into the spotlight as an enabling technology for the 5th generation (5G) wireless communication systems. By offering large blocks of contiguous spectrum, mmWave presents a unique opportunity to overcome the bandwidth crunch problem in lower frequency bands [1]. At mmWave frequencies, high isotropic path loss necessitates the reliance on antenna arrays with large number of elements. These arrays overcome the path loss by high directional gains through beamforming (BF). Thus, a transmitter–receiver (Tx–Rx) pair uses large number of antennas to focus energy in a particular direction to meet a target link budget. A key challenge for large antenna arrays, and the motivation of our work, is to find an architecture capable of high-dimensional array processing in a power-efficient manner at mmWave frequencies.

Most current mmWave designs use *analog* [2] or *hybrid beamforming* [3]. In these cases, beamforming is performed in radio frequency (RF) or at an intermediate frequency (IF) through a bank of phase shifters (PSs) – one per antenna element as shown in Fig. 1. This architecture reduces the power consumption by using only one pair of analog to digital converters (ADC) and digital to analog converters (DAC) at the Rx and Tx, respectively, per digital stream. While analog and hybrid beamforming are power efficient, they are only capable of transmitting in one or a few directions at a given time [4]. This essentially limits their multiplexing capabilities. In contrast, in *fully digital architectures* [5]–[8], shown in Fig. 2, beamforming is performed in baseband. Each RF chain has a pair of ADCs at the Rx and DACs at the Tx enabling the transceiver to simultaneously direct beams in theoretically infinite directions at a given time. But, for wide-band systems high precision ADCs and DACs can be very power hungry. To be energy efficient, fully digital beamformers need to use converters with one or few bits of resolution [9].

A. Signal Processing with Low Resolution Quantizers

For communication systems, the degradation due to low resolution converters can be viewed as the introduction of *quantization noise* in the signal. Low resolution converters can be simply viewed as a noise source and the introduction of this additional noise has the effect of lowering the achievable link capacity. Studies on point-to-point links [7], [9], [10] have demonstrated that

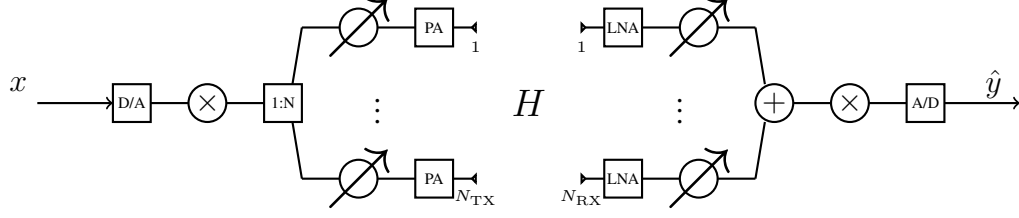


Fig. 1: Analog beamforming based transmitter (left) and receiver (right) use a bank of phase shifter to perform beamforming in the RF domain. This architecture uses just one pair of A/D or D/A at the baseband.

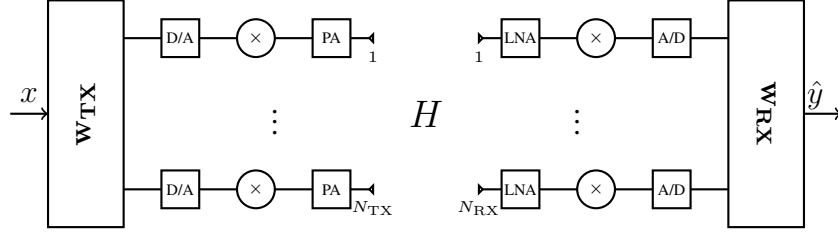


Fig. 2: Fully digital transmitter (left) and receiver (right) use a pair of DACs and ADCs per RF stream. Beamforming is performed in the baseband digital domain.

operations over wide band channels with low resolution ADCs can achieve sufficient spectral efficiency. Even with a single bit of precision, as shown in [7], [10], wide band multi-antenna systems can achieve considerably high spectral efficiency when perfect channel information is assumed. Further, MIMO channel estimation for wide band systems has been recently studied under the low resolution limit in [11], [12]. Moreover, the information theoretic work in [13] shows that for point-to-point systems, digital beamforming offers higher rates than analog for a given power budget. A comparison with hybrid beamforming in [14], similarly, shows that low resolution digital BF can achieve higher rates than hybrid beamforming while having similar or even lower power consumption.

B. Motivation and Contributions

The key objectives for 5G cellular systems are achieving higher rates, serving much denser networks, and ultra low latency. The abundant bandwidth in mmWave frequencies could achieve the first two objectives; the third objective, we argue, can be obtained as a consequence of utilizing digital beamforming. To make this argument we note that the fully digital architecture enables greatly enhanced spatial flexibility. The works in [15]–[17] showed how digital BF can

reduce control plane latency by a factor of 10 compared with the alternative, i.e., analog and hybrid BF. Additionally, frequency division multiple access (FDMA) scheduling is feasible with digital BF which enables very efficient transmission of short data and control packets [18]. However, to maintain similar power consumption levels as analog BF, fully digital arrays should operate at low quantization levels. Hence, there is a fundamental trade-off between directional search and spatial multiplexing on the one hand and quantization noise on the other.

The addition of quantization noise does not adversely effect control signaling as these are designed to operate in low SNRs where quantization noise is not dominant [18]. On the other hand, as observed from the theoretical works of [7], [9], the presence of quantization noise essentially limits the maximum achievable rate in the high SNR regime. Thus, in this work we study the effect of low resolution quantization on the *data plane* of mmWave cellular systems. More importantly, we answer the key question as to how many bits of resolution are required, both at the transmitter (DACs) and the receiver (ADCs), to operate in the mmWave bands. Our major contributions in this work are as follows:

- First, we provide a detailed assessment of various components of a mmWave front-end based on state-of-the-art circuits. This provides a first order model to analyze the power consumption of different beamforming architectures.
- Second, we propose an additive quantization noise model (AQNM) to model the signal degradation due to low resolution. We show that our proposed model accurately predicts system behavior both at low SNR and high SNR regimes.
- Third, we show that for most practical cellular operations the effect of low resolution quantization on the achieved rate is negligible. In fact, we show that at the Rx, 3 – 4 bits of resolution is sufficient for wide band mmWave applications.
- Finally, we study the effects of low resolution DACs on mmWave transmissions. We show that with no assumption on additional filtering, 4-bits of resolution is sufficient to guarantee 3GPP new radio (NR) compliant adjacent carrier leakage. Further, analyzing the error vector magnitude (EVM) we show that 4-bits of DAC resolution is sufficient both at BS and UE.

The rest of this paper is organized as follows. We model the power consumption of the mmWave front end circuit in Sec. II. In Sec. III, we describe the downlink (DL) system model for mmWave systems and the available multiple access schemes. We present the proposed AQNM in Sec. IV and discuss on the effects quantization noise under practical operating conditions.

Next, in Sec. V, we present the model of a DAC from a signal processing point of view and detail the effects of low resolution quantization on the transmitted signal quality. In Sec VI we validate the proposed AQNM and study its effect on system rate. Further, using extensive simulation we also determine the resolution needed for the DACs at the mmWave Tx. Sec. VII concludes the paper. This work was presented in part in [19].

II. POWER CONSUMPTION IN MMWAVE RADIO CIRCUITS

Power efficient fully digital beamformers will have to rely on low resolution converters DACs and ADCs. To better understand the effect of decreasing the precision of the converters, in this section we model the power consumption of transceiver front ends (FE) at mmWave frequencies for analog, hybrid and fully digital beamforming.

RF Front End: The RF front end (RFFE) refers to the circuitry between the antenna and the baseband data converters (DACs or ADCs). As shown in Fig.1 and Fig.2, this includes the power amplifiers (PAs) or low noise amplifiers (LNAs), mixers, PSs, combiners and splitters. At the Tx, consider that the total power delivered by the base-band circuit is $P_{\text{in}}^{\text{BB}}$. The mixers, splitters and PSs are considered to be passive devices which introduce insertion loss (IL) but do not draw any power. From Fig. 1 it is easy to see that for analog and K -stream hybrid beamformer, the power of input signal at the PA is given as

$$P_{\text{in,ana}}^{\text{PA}} = P_{\text{in,hyb}}^{\text{PA}} = P_{\text{in}}^{\text{BB}} - 10 \log_{10}(N_{\text{Tx}}) - IL_{\text{PS}} - IL_{\text{mix}} \text{ (dBm)}, \quad (1)$$

where IL_{PS} and IL_{mix} is the IL due to the PS and mixer respectively, and $10 \log_{10}(N_{\text{Tx}})$ is the loss in signal power due to the $1 : N_{\text{Tx}}$ power splitter. Similarly, from Fig. 2, for the fully digital beamformer, we can write,

$$P_{\text{in,dig}}^{\text{PA}} = P_{\text{in}}^{\text{BB}} - 10 \log_{10}(N_{\text{Tx}}) - IL_{\text{mix}} \text{ (dBm)}. \quad (2)$$

To transmit an output power $P_{\text{out}}^{\text{RF}}$ dBm, the D.C power drawn by the PA is

$$P_{\text{DC,BF}}^{\text{PA}} = \frac{1}{\eta_{\text{PAE}}} \left(10^{0.1 P_{\text{out}}^{\text{RF}}} - 10^{0.1 P_{\text{in,BF}}^{\text{PA}}} \right) \text{ mW}, \quad (3)$$

where η_{PAE} is the power added efficiency of the PA and $P_{\text{in,BF}}$ is the input power given a beamforming (BF) architecture. Note that for a given effective isotropic radiated power (EIRP), for an N_{Tx} antenna system, $P_{\text{out}}^{\text{RF}} = \text{EIRP} - 20 \log(N_{\text{Tx}})$. The total power drawn by the Tx RFFE can hence be given as

$$P_{\text{Tx}}^{\text{RFFE}} = N_{\text{Tx}} P_{\text{DC,ana}}^{\text{PA}} + N_s P_{\text{LO}}, \quad (4)$$

where N_s is the number of baseband streams; $N_s = 1$ for analog BF, $N_s = K$ for K -stream hybrid BF and $N_s = N_{\text{Tx}}$ for fully digital BF. Based on (4), the power consumption of the Tx RFFE is reported in Table I where the calculations are based considering $\text{EIRP} = 30$ dBm, $IL_{\text{PS}} = 10$ dB, $IL_{\text{mix}} = 6$ dB, $P_{\text{LO}} = 10$ dBm and $\eta_{\text{PAE}} = 20\%$.

At the Rx, LNAs are characterized by their figure of merit (FoM) which relates the gain (G_{LNA}) and the noise figure (N_{LNA}) to the D.C. power drawn ($P_{\text{dc}}^{\text{LNA}}$) as [20]

$$P_{\text{dc}}^{\text{LNA}} = \frac{G_{\text{LNA}}}{\text{FoM}(N_{\text{LNA}} - 1)}, \quad (5)$$

in linear scale. The total RFFE power consumption at the Rx is thus,

$$P_{\text{Rx}}^{\text{RFFE}} = N_{\text{Rx}} P_{\text{dc}}^{\text{LNA}} + N_s P_{\text{LO}}, \quad (6)$$

where N_s is the number of baseband streams at the Rx. Now, for the digital BF, if the LNA gain is selected as $G_{\text{LNA,dig}}$, then for analog/hybrid BF the required LNA gain required will be ($G_{\text{LNA,dig}} + IL_{\text{PS}}$) which compensates for the IL due to the RF PSs. In Table. I, we list the Rx RFFE power consumption given $G_{\text{LNA,dig}} = 10$ dB, $IL_{\text{PS}} = 10$ dB, $N_{\text{LNA}} = 3$ dB and a LNA $\text{FoM} = 6.5 \text{ mW}^{-1}$ for 90 nm CMOS as reported in [21].

Gain Control at the Rx: Given a fixed Tx EIRP, the power received at the Rx is $P_{\text{Rx}}(d) = \text{EIRP}_{\text{Tx}} - \text{PL}(d)$, where $\text{PL}(d)$ is the path loss for a Tx-Rx separation of d m. To maintain a constant baseband power of $P_{\text{BB}}^{\text{out}}$, the variable gain amplifier (VGA) at the input of the ADC needs a gain range from $0 - G_{\text{max}}^{\text{VGA}}$ dB. Noting that G_{LNA} is adjusted to compensate for IL_{PS} for analog/hybrid BF, to drive a total baseband power of $P_{\text{BB}}^{\text{out}}$, the VGA gain range required is

$$G_{\text{max}}^{\text{VGA}} = P_{\text{BB}}^{\text{out}} - 10 \log(N_{\text{Rx}}) + IL_{\text{mix}} - (G_{\text{LNA}} - IL_{\text{PS}}) - P_{\text{Rx}}(d = d_{\text{cell}}), \quad (7)$$

where d_{cell} is the radius of the cell. For a down-link (DL) transmission with $\text{EIRP}_{\text{Tx}} = 43$ dBm; at the cell edge, $d_{\text{cell}} = 100$ m, $P_{\text{Rx}}(d = d_{\text{cell}}) = -87$ dBm for a mmWave non-line of sight channel [22]. Assuming similar values of IL as on the Tx RFFE and considering $G_{\text{LNA,dig}} = (G_{\text{LNA}} - IL_{\text{PS}}) = 10$ dB, to maintain $P_{\text{BB}}^{\text{out}} = 10$ dBm we require a gain range of $G_{\text{max}}^{\text{VGA}} = 82$ dB.

The figure of merit of a VGA (FoM_{VGA}) is defined by [23] as

$$\text{FoM}_{\text{VGA}} = \frac{G_{\text{max}}^{\text{VGA}} \times f_{\text{BW}}}{P_{\text{dc}}^{\text{VGA}} \times A_{\text{chip}}}, \quad (8)$$

where $G_{\text{max}}^{\text{VGA}}$ is in dB, the bandwidth f_{BW} is in GHz, the D.C power draw $P_{\text{dc}}^{\text{VGA}}$ in mW and the VGA active area A_{chip} is in mm^2 . The FoM reported by [23] for a 90-nm CMOS process with an active area of 0.01 mm^2 is 5280. Considering the same active area, we report the power drawn by the VGA(s) for the three beamforming architectures in Table I.

DAC and ADC: For wide band wireless applications the data converters, DACs and the ADCs, are considered to be the most power hungry elements. The power consumed by an ADC or a DAC (P_{conv}) is a linear function of the sampling frequency (f_s) and grows exponentially with the number of bits of resolution (n) as

$$P_{\text{conv}} = \text{FoM} \times f_s \times 2^n, \quad (9)$$

where FoM is the figure of merit of the converter. As mmWave systems are envisioned for ultra wide-band applications, the sampling frequencies are in the order of 1 GHz. In analog or hybrid beamforming the use of one or a few pairs of converters limit the power consumption. For fully digital systems, a reduction of n is hence the only way to reduce the power consumption.

Contrary to the assumption made in [24], we observe that both DACs and the ADCs are equally power hungry. For instance the 4-bit Flash based ADC designed in [25] has a $\text{FoM} = 65 \text{ fJ/conv}$, while a state of the art DAC proposed in [26] has a $\text{FoM} = 67.6 \text{ fJ/conv}$. Thus, a pair of 8-bit ADC consumes 33.28 mW of power at $f_s = 1 \text{ GHz}$. At the same sampling rate, a pair of 8-bit DAC consumes 34.6 mW of power, nearly same as that of the ADC. Similar trends can be observed in more recent works [27], [28]. Hence, using low resolution DACs can considerably reduce the power consumption by fully digital Tx as shown in Table I.

Filtering at the Tx: The output of the DACs will require analog low pass filters (LPF) to reject spectral images, and maintain out of band emission limits as discussed in Sec. V. In this work we assume the use of active switched capacitor filters. For an m -th order active LPF with cutoff frequency at f_c , the power per pole per Hertz FoM is given as [29],

$$\text{FoM}_{\text{LPF}} = P_{\text{dc}}^{\text{LPF}} / (m \times f_c). \quad (10)$$

For wide band LPFs, based on [29], [30], we consider the $\text{FoM} = 1.3 \text{ mW/GHz}$. For mmWave beamformers, as discussed in Sec. VI-C, we can use a first order LPF with $f_c = 400 \text{ MHz}$, each of these filters will thus consume a power of $P_{\text{dc}}^{\text{LPF}} = 0.52 \text{ mW}$. Depending on the BF architecture, the total power drawn by the LPF is equal to $N_s P_{\text{dc}}^{\text{LPF}}$, with $N_s = 1$ for analog BF.

From Table I, we see that both at the Tx and the Rx, low resolution quantizers can considerably reduce the power consumption of the front-end circuitry. At the Rx, the use of low resolution quantizers for digital BF front ends leads to a power draw lower than even the analog BF. This reduction in power comes at the cost of increased quantization noise in the system. We will analyze the effect of coarse quantization in the sequel.

Rx Front End Power Consumption [mW]					
BF Arch.	RFFE	VGA	ADC (8 bits)	ADC (4 bits)	Total
Analog	257.3	1.55	33.3	–	292.15
Hybrid ($K = 2$)	267.3	3.11	66.6	–	337.01
Digital (High res.)	184.7	24.85	532.8	–	742.35
Digital (Low res.)	184.7	24.85	–	33.3	242.85

Tx Front End Power Consumption [mW]					
BF Arch.	RFFE	LPF	DAC (8 bits)	DAC (4 bits)	Total
Analog	321.2	0.52	34.4	–	356.12
Hybrid ($K = 2$)	331.2	1.04	69.2	–	401.44
Digital (High res.)	459.9	8.32	553.6	–	1021.82
Digital (Low res.)	459.9	8.32	–	34.4	502.62

TABLE I: Power consumption (in mW) for each component in the RF chain for various receiver and transmitter architectures with 16 Tx and Rx antennas.

III. MMWAVE DOWNLINK SYSTEM MODEL

In this section we detail the network in which mmWave cellular transceivers operate. To study the effect of low quantization converters, we characterize the signal to interference and noise ratio (SINR) under different multiple access strategies.

A. Network Model

We consider a wireless network with N base stations (BS) each with $N_{\text{BS}}^{\text{ant}}$ antennas. Each BS serves a multiplicity of UEs each with $N_{\text{UE}}^{\text{ant}}$ antennas. The BSs and UEs operate over mmWave frequencies i.e., $f_c > 10$ GHz, where f_c is the carrier frequency. Downlink (DL) and uplink (UL) transmissions use the same channel using time division duplexing (TDD). To mitigate the high isotropic path loss at mmWave frequencies, the BSs and UEs employ digital beamforming with low resolution DACs and ADCs both at the Tx and the Rx.

a) DL Transmission: In this system, a BS transmits single streams of data to $K = |\mathcal{K}(t)|$ associated UEs, where $K \leq \min(K_0, N_{\text{BS}}^{\text{beam}})$, $\mathcal{K}(t)$ is the set of scheduled users at time instant t , K_0 is the total number of UEs associated with the BS, and $N_{\text{BS}}^{\text{beam}}$ is the number of beams supported by the BS. Under a fixed power budget of P watts, the transmitted signal is given by

$$\mathbf{x}(t) = \sum_{k \in \mathcal{K}(t)} \rho \mathbf{v}_k s_k(t), \quad (11)$$

where $\rho = \sqrt{P/K}$, $\mathbf{v}_k \in \mathbb{C}^{N_{\text{BS}}^{\text{ant}} \times 1}$ is the transmit side long-term beamforming vector between user k and the BS. Throughout this work we assume long-term beamforming [22] where BF

vectors are computed based on the channel covariance matrices. Without loss of generality, we assume $P = 1$ and throughout this work we considered transmit power is split equally among all beams by the BS. At each time instant t the BS schedules $\mathcal{K}(t)$ UEs which can be multiplexed in time, frequency or spatially. We discuss the relevant multiple access schemes in Section III-B.

b) DL Reception: The signal received at the k -th UE, *before* digital beamforming is applied, is given as

$$\mathbf{y}_k(t) = \rho \mathbf{H}_k \mathbf{v}_k s_k(t) + \sum_{j \in \mathcal{K}(t), j \neq k} \rho \mathbf{H}_k \mathbf{v}_j s_j(t) + \mathbf{z}_k + \mathbf{n}_k, \quad (12)$$

where $\mathbf{H}_k \in \mathbb{C}^{N_{\text{UE}}^{\text{ant}} \times N_{\text{BS}}^{\text{ant}}}$ represents the channel matrix between user k and the BS, $\mathbf{z}_k \in \mathbb{C}^{N_{\text{UE}}^{\text{ant}} \times 1}$ represents the inter-cell interference and $\mathbf{n}_k \in \mathbb{C}^{N_{\text{UE}}^{\text{ant}} \times 1}$ represents the receiver noise. Note that the second term on the right hand side of (12) accounts for the *intra-cell interference* (ICI). The receiver noise is assumed to be zero mean i.i.d. Gaussian with covariance matrix given as $\sigma_n^2 I_{N_{\text{UE}}^{\text{ant}}}$. Similarly, the inter-cell interference $\mathbf{z}_k$ is also be assumed to be i.i.d Gaussian with the covariance matrix $\sigma_z^2 I_{N_{\text{UE}}^{\text{ant}}}$.

B. DL Multiple Access

MmWave BSs employing fully digital beamforming are capable of transmitting to multiple directions at the same time. Unlike conventional analog/hybrid architectures this allows the multiplexing of large number of UEs, spatially or in frequency, leading to potential increase in system throughput, and enabling low latency transmissions. The multiple access techniques available for fully digital BS transmissions are as follows.

a) TDMA: Time division multiple access (TDMA) based DL will have $|\mathcal{K}(t)| = 1$, i.e., only one UE can be scheduled for transmission at any given time instance and will have access to the entire bandwidth. As user allocations are orthogonal in time, the ICI is zero. Yet, TDMA can potentially lead to wastage of allocated bandwidth especially for UEs with bursty or low rate traffic. Sophisticated scheduling and frame design, such as [18], is required to intelligently exploit the large bandwidth available at mmWave.

b) OFDMA: A key attraction of digital beamforming at mmWave frequencies is that it enables orthogonal frequency division multiple access (OFDMA). In OFDMA, BS allocates chunks of total bandwidth i.e., physical resource blocks (PRBs) to multiple UEs at each scheduling instance. The scheduler allocates a variable portion of the bandwidth $W_k \geq 0$ to $k = 1, 2 \dots K_0$ UEs based on their data requirement, channel condition, and scheduling priority. Due to the

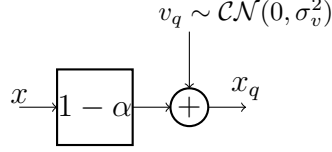


Fig. 3: Additive quantization noise model for low resolution quantizer. The parameter α is the inverse coding gain and models the quantizer resolution (e.g., $\alpha = 0$ implies infinite resolution).

use of orthogonal channels for transmission, OFDMA based DL signals do not encounter ICI. Moreover, unlike TDMA, multiple users can be scheduled at each time slot leading to higher utilization of mmWave bands and faster transmission of small packets. This is especially attractive for low latency communication.

c) SDMA: Fully digital beamforming has the potential to support space division multiple access (SDMA) as the BS Tx can transmit to multiple users on separate spatial streams¹. This has the potential of increasing the available degrees of freedom K folds, where K is the number of streams. In this case $|\mathcal{K}(t)| = K(t)$, the “optimal” number of streams that can be supported at time t . A key challenge for SDMA based transmission is mitigating the effect of the ICI. To minimize ICI, the BS scheduler must carefully select users, or beamforming patterns, or both, thus limiting the number of beams over which transmissions occur at any scheduling instance.

IV. LINK LAYER AQNM MODEL

A. Effective SINR

We first derive an analytical model for the effective SINR under the finite quantization limit for a multi-antenna receiver. For this purpose, we use a slightly modified version of the additive quantization noise model (AQNM) as presented in [31]. In our AQNM, shown in Fig. 3, the effect of finite uniform quantization of a scalar input y is represented as a constant gain plus an additive white Gaussian noise (AWGN). Furthermore, [31] showed that if an input complex sample y is modeled as a random variable, then the quantizer output y_q can be written as

$$y_q = Q(y) = (1 - \alpha)y + v_q, \quad \mathbb{E}|v_q|^2 = \alpha(1 - \alpha)\mathbb{E}|y|^2, \quad (13)$$

where $Q(\cdot)$ denotes the quantization operation and v_q represents quantization errors uncorrelated with y and approximated as a complex Gaussian. The parameter $\alpha \in [0, 1]$ is the *inverse coding*

¹Although the point-to-point mmWave link is low rank, additional degrees of freedom can be achieved by multi-user MIMO

gain of the quantizer, i.e., $\alpha = 0$ implies infinite quantizer resolution. The parameter α is assumed to depend on the resolution of the quantizer and is independent of the input distribution.

We now extend our model to a multi-antenna receiver model. For the received signal in (12), each component $y_k^i(t)$ of the received signal $\mathbf{y}_k(t)$ is independently quantized by an ADC before an appropriate receiver-side beamforming vector $\mathbf{w}_{\text{UE}}$ is applied. Thus, from (12) and (13), the quantized received vector is given as

$$\begin{aligned}\hat{\mathbf{y}}_k(t) &= \mathbf{Q}(\mathbf{y}_k(t)) \\ &= (1 - \alpha) \left(\rho \mathbf{H}_k \mathbf{v}_k s_k(t) + \sum_{j \in \mathcal{K}(t), j \neq k} \rho \mathbf{H}_k \mathbf{v}_j s_j(t) + \mathbf{z}_k + \mathbf{n}_k \right) + \mathbf{v}.\end{aligned}\quad (14)$$

The vector $\mathbf{v}$ denotes the additive quantization noise with covariance $\sigma_v^2 \mathbf{I}_{N_{\text{UE}}^{\text{ant}}}$. We assume that the quantization errors across antennas are uncorrelated. From (14), the average per component energy to the input $\mathbf{y}$ of the quantizer is

$$\frac{1}{N_{\text{UE}}} \mathbb{E} \|\mathbf{y}\|^2 = \rho^2 E_k + \rho^2 \sum_{j \in \mathcal{K}(t), j \neq k} E_j + \sigma_z^2 + \sigma_n^2, \quad (15)$$

where $E_j = (1/N_{\text{UE}}^{\text{ant}}) \mathbb{E} \|\mathbf{H}_k \mathbf{v}_j s_j\|^2 \forall j$ is the average received symbol energy per antenna for each stream $j \in \mathcal{K}$. From (13), the quantization noise variance is

$$\sigma_v^2 = \alpha(1 - \alpha) \left[\rho^2 E_k + \rho^2 \sum_{j \in \mathcal{K}(t), j \neq k} E_j + \sigma_z^2 + \sigma_n^2 \right]. \quad (16)$$

Note that for (16) we use the fact that at any given t the inter-cell interference is independent of the transmitted signal, i.e., $\mathbb{E} [z_k^H (\mathbf{H}_k \mathbf{v}_j s_j)] = 0, \forall j \in \mathcal{K}(t)$, and symbols transmitted to different users are independent, i.e. $\mathbb{E} [s_i^* s_j] = 0$, for point to point links, hence

$$\mathbb{E} \left[(\mathbf{H}_k \mathbf{v}_k s_k)^H \mathbf{H}_j \mathbf{v}_j s_j \right] = 0; \quad \forall j \in \mathcal{K}(t), j \neq k. \quad (17)$$

After applying a receiver-side beamforming vector $\mathbf{u}_k$, the channel between the UE and the BS is an effective SISO channel. Define the Rx side BF gain on signal in stream $j \in \mathcal{K}(t)$ as

$$G_j := \mathbb{E} |\mathbf{u}_k^* \mathbf{H}_k \mathbf{v}_j s_j|^2 / E_j, \quad (18)$$

which is the ratio of the signal energy after beamforming to the received signal energy per antenna. We note that in (18) if transmit beamforming vectors are chosen such that $\mathbf{H}_k \mathbf{v}_j = \mathbf{0}, \forall j \neq k, j \in \mathcal{K}(t)$, then the ICI at user k is zero, but this requires careful beam planning and scheduling and may not, except for channel conditions, be even possible. On the other

hand, to remove ICI one can set $s_j = 0, \forall j \neq k, j \in \mathcal{K}$, which is achieved by orthogonal transmissions schemes (TDMA and OFDMA). Although the latter approach for mitigating ICI simplifies scheduling, there is a loss in the available degrees of freedom.

Observe that, if there is no quantization error ($\alpha = 0$), the beamformed SINR of user k is

$$\gamma_k^{\text{BF}} := \frac{\rho^2 \mathbb{E} |\mathbf{u}_k^H \mathbf{H}_k \mathbf{v}_k s_k|^2}{\sigma_n^2 + \sigma_z^2 + \rho^2 \sum_{j \neq k} \mathbb{E} |\mathbf{u}_k^* \mathbf{H}_j \mathbf{v}_j s_j|^2} = \frac{\rho^2 G_k E_k}{\sigma_n^2 + \sigma_z^2 + \rho^2 \sum_{j \neq k} G_j E_j} = \frac{\gamma'_k}{1 + \sum_{j \neq k} \gamma'_j}, \quad (19)$$

where $\gamma'_j = \rho^2 G_j E_j / (\sigma_n^2 + \sigma_z^2), \forall j \in \mathcal{K}$. Now, with finite resolution quantization, using the AQNM, the signal after beamforming is given by

$$\begin{aligned} y_k^{\text{Q,BF}} &:= \mathbf{u}_k^H \hat{\mathbf{y}}_k = \rho(1 - \alpha) \mathbf{u}_k^H \mathbf{H}_k \mathbf{v}_k s_k \\ &\quad + \rho(1 - \alpha) \sum_{j \neq k} \mathbf{u}_k^H \mathbf{H}_j \mathbf{v}_j s_j + (1 - \alpha) \mathbf{u}_k^H \mathbf{z} + (1 - \alpha) \mathbf{u}_k^H \mathbf{n} + \mathbf{u}_k^H \mathbf{v}. \end{aligned} \quad (20)$$

Without loss of generality, assuming $\|\mathbf{u}_k\| = 1$ the mean beamformed received signal energy is

$$E_k^{\text{BF}} = (1 - \alpha)^2 \rho^2 \mathbb{E} |\mathbf{u}_k^H \mathbf{H}_k \mathbf{v}_k s_k|^2 = \rho^2 (1 - \alpha)^2 G_k E_k, \quad (21)$$

while the average noise plus interference energy is

$$W_k^{\text{BF}} = (1 - \alpha)^2 \left(\sigma_z^2 + \sigma_n^2 + \rho^2 \sum_{j \neq k} G_j E_j \right) + \sigma_v^2. \quad (22)$$

Finally, combining (16), (19), (21), and (22), we obtain an expression for the SINR after beamforming as

$$\gamma_k^{\text{Q,BF}} = \frac{E_k^{\text{BF}}}{W_k^{\text{BF}}} = \frac{(1 - \alpha) \gamma'_k}{1 + (1 - \alpha) \sum_{j \neq k} \gamma'_j + \alpha \left(\frac{\gamma'_k}{G_k} + \sum_{j \neq k} \frac{\gamma'_j}{G_j} \right)}. \quad (23)$$

B. Orthogonal Transmission

For orthogonal DL transmissions, i.e., TDMA or OFDMA, (23) simplifies as

$$\gamma_k^{\text{Q,BF}} = \frac{(1 - \alpha) \gamma_k^{\text{BF}}}{1 + (\alpha / G_k) \gamma_k^{\text{BF}}}, \quad (24)$$

where we use the fact that $\gamma_k^{\text{BF}} = \gamma'_k$ in the absence of ICI. Using (24), we can qualitatively understand the system-level effects of quantization by looking at the following two regimes:

a) Low SNR: In the low-SNR (or SNR) regime, γ_k^{BF} is small, hence

$$\gamma_k^{\text{Q,BF}} \approx (1 - \alpha) \gamma_k^{\text{BF}}, \quad (25)$$

i.e., the SINR is decreased only by a factor $1 - \alpha$. We show in Sec. VI that at moderate quantization levels this has very little impact on system performance.

b) *High SNR*: In this regime as $\gamma_k^{\text{BF}} \rightarrow \infty$

$$\gamma_k^{\text{Q,BF}} \rightarrow \frac{G_k(1-\alpha)}{\alpha}. \quad (26)$$

Thus, the effect of quantization is to saturate the maximum SINR i.e., the effect of finite quantization is critical only at high SNR (or SINR). In Sec. VI we show that even for 3–4 bits of resolution, the effects caused at the high SINR limit are not significant for cellular systems.

C. SDMA Transmission

Comparing (23) with (24) we note that for SDMA transmission the effect of quantization noise is further enhanced by the presence of ICI. In (23), we note that

$$G_j = \frac{\mathbb{E} \|\mathbf{u}_k^H \mathbf{H}_k \mathbf{v}_j s_j\|^2}{E_j} = N_{\text{UE}}^{\text{ant}} \frac{\mathbb{E} \|\mathbf{u}_k^H \mathbf{H}_k \mathbf{v}_j s_j\|^2}{\mathbb{E} \|\mathbf{H}_k \mathbf{v}_j s_j\|^2}. \quad (27)$$

Under the assumption that $\|s_j\|^2 = 1$, using the Kronecker model [32] we obtain

$$\mathbb{E} \|\mathbf{u}_k^* \mathbf{H}_k \mathbf{v}_j s_j\|^2 = \frac{1}{N_{\text{UE}}^{\text{ant}}} (\mathbf{u}_k^H \mathbf{Q}_k^{\text{rx}} \mathbf{u}_k) (\mathbf{v}_j^H \mathbf{Q}_k^{\text{tx}} \mathbf{v}_j),$$

where $\mathbf{Q}_k^{\text{rx}} = \mathbb{E}[\mathbf{H}_k \mathbf{H}_k^H]$ and $\mathbf{Q}_k^{\text{tx}} = \mathbb{E}[\mathbf{H}_k^H \mathbf{H}_k]$ are the receive and transmit covariance matrices respectively for the channel between the BS and user k . Similarly,

$$\mathbb{E} \|\mathbf{H}_k \mathbf{v}_j s_j\|^2 = (\mathbf{v}_j^H \mathbf{Q}_k^{\text{tx}} \mathbf{v}_j). \quad (28)$$

Thus we can rewrite (27) as

$$G_j = \mathbf{u}_k^H \mathbf{Q}_k^{\text{rx}} \mathbf{u}_k = G_k, \quad \forall j \in \mathcal{K}, \quad (29)$$

which points simply to the fact that for any signal received by the k -th UE from the associated BS will have the same receiver-side beamforming gain.

The presence of ICI in SDMA systems essentially limits the maximum achievable SINR. From (28), we note that although ICI can be eliminated by selecting transmit beamforming vectors such that $(\mathbf{v}_j^H \mathbf{Q}_k^{\text{tx}} \mathbf{v}_j) \rightarrow 0, \forall j \neq k$, but this can be a very hard problem in practice and in some cases a solution may not exist. Thus, in our analysis we will assume that ICI is always present for SDMA based systems.

For this, we simplify (19) by representing the ICI term as

$$\sum_{j \neq k} \gamma'_j = \psi \gamma'_k, \quad \psi \geq 0, \quad (30)$$

where $1/\psi$ is the signal to interference ratio (SIR), and as $\gamma'_k \rightarrow \infty, \gamma_k^{\text{BF}} \rightarrow (1/\psi)$. An interesting observation for SDMA systems is that even in the absence of quantization noise the SINR saturates depending on the severity of the ICI.

Effect of low resolution quantization: Using (29), and (30) in (23) we can express the SINR in the presence of quantization noise and ICI as

$$\gamma_k^{\text{Q,BF}} = \frac{(1 - \alpha)\gamma'_k}{1 + (1 - \alpha)\psi\gamma'_k + (\psi + 1)\frac{\alpha}{G_k}\gamma'_k}. \quad (31)$$

Furthermore, as in (24) we can rewrite (31) as

$$\gamma_k^{\text{Q,BF}} = (1 - \alpha\beta)\gamma_k^{\text{BF}}, \quad (32)$$

where

$$\beta = \frac{1 + (\psi + 1)\gamma'_k/G_k}{1 + (1 - \alpha)\psi\gamma'_k + \alpha(\psi + 1)\gamma'_k/G_k}. \quad (33)$$

Finally, from (33) we note that $\beta < \frac{1}{\alpha}$ is always satisfied under the AQN model (note that $\beta > \frac{1}{\alpha} \Leftrightarrow \alpha > 1$, which is not admissible). It follows from (33) that for $\beta < 1$ we must have $G_k > 1 + \frac{1}{\psi}$, and hence,

$$\gamma_k^{\text{Q,BF}} > (1 - \alpha)\gamma_k^{\text{BF}}, \text{ if } G_k > 1 + \frac{1}{\psi}. \quad (34)$$

We note that when ψ is *large*, i.e., when ICI dominates, it is easy to satisfy the inequality in (34). SINR degradation in this regime is dominated by the large ICI and *not* quantization noise. Alternatively, as $\psi \rightarrow 0$ the system converges to the orthogonal transmission case discussed in the previous subsection.

To summarize our analysis in this section, for multi-user communications with long term beamforming and low resolution front ends, we state the following:

- For orthogonal transmission (i.e. FDMA or TDMA) in low SNR/SINR regime there is very little or effectively no loss due to low resolution quantization.
- For orthogonal transmission in high SNR or SINR regimes there exists a saturation of the effective SNR or SINR due to quantizer resolution.
- For transmission schemes when orthogonality within the cell is not always guaranteed (e.g., SDMA), the degradation in SINR is dominated by the ICI.

In Sec. VI we will validate these claims using extensive simulations.

V. SIGNAL IMPAIRMENTS WITH LOW RESOLUTION DAC

The previous section analyzed quantization noise at the Rx. In this section, we analyze the Tx side. Low resolution quantization at the Tx can result in quantization noise being present both in band and out-of-band. For cellular communication systems, 3GPP specifies signal characteristics

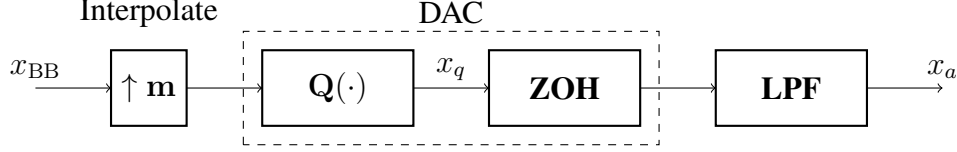


Fig. 4: High level model of a digital to analog converter at baseband. The DAC is clocked at f_s where $f_s = m f_{BB}$.

that must be adhered to by any transmitter. The key characteristics are (a) output power, (b) the adjacent carrier leakage ratio (ACLR), and (c) transmitted signal quality specified by the error vector magnitude (EVM). The output power is not affected by the addition of quantization noise but the EVM of the Tx increases due to the addition of this noise source. Moreover, the quantization noise is not band limited as opposed to the signal and contributes to an increase in the power leaked into the adjacent carrier. This out-of-band distortion is rather constraining for cellular communications systems and hence 3GPP specifies ACLR limits that all commercial transmitters must comply with. In this section we show the effects of low resolution DACs on the transmitted signal, and in the process determine the precision required by the DACs in the mmWave transmitter to meet 3GPP regulations.

A. Model of the DAC

To model the effect of low resolution on the transmit signal, we first describe a model for the DAC. A DAC comprises of a quantizer $Q(\cdot)$ and the zero order hold (ZOH) circuit. An output analog low pass filter (LPF), as shown in Fig. 4, is used to attenuate the spectral images of the signal located at an interval of f_s , where f_s is the sampling rate of the DAC. The sampling rate must be at least $f_s = f_{BW}$, where f_{BW} is the bandwidth of the input signal x_{BB} . In most designs, like [33], $f_s = m \times f_{BW}$, with $m > 1$ so that the spectral images formed at the output of the DAC are spaced sufficiently apart in frequency. Thus the base band signal is interpolated, i.e., upsampled by m and filtered, before it is converted to analog. The interpolator not only relaxes the design of the analog LPF but also minimizes the distortion caused by the ZOH on the in-band component. Additionally, upsampling the signal by a factor of m also reduces the power of the quantization noise by the same factor, e.g., a $m = 2$ interpolation of the baseband signal will lead to lowering the quantization noise by 3 dB. This gain comes at the cost of doubling the sampling rate f_s of the DAC which, from (9), doubles the power consumed. As pointed out

in [33], high over sampling is not practical for wide band systems due to the linear increase in power consumption.

B. Adjacent Carrier Leakage

From our analysis in Section IV, quantization noise for low resolution converters can be modeled as white Gaussian noise. This implies that the quantization noise has a flat spectrum while the signal of interest is band limited. This is problematic for practical systems as this noise causes unwanted interference in the adjacent bands. For interoperability, cellular transmissions need to limit the amount of power leaked into the adjacent bands. This restriction is quantified, by 3GPP, as the *adjacent carrier leakage ratio* (ACLR) defined as,

$$\text{ACLR} = 10 \log_{10} \left(\frac{P_{\text{in}}}{P_{\text{ac}}} \right), \quad (35)$$

where P_{in} is the total power in the transmission channel and P_{ac} is the total power accumulated over a given adjacent channel.

Well known effective techniques, like windowed overlap and add (WOLA) OFDM [34], are used to reduce the ACLR in LTE systems. These techniques focus on reducing the inter modulation products and operate on the signal in the digital domain. Quantization noise due to finite resolution DACs, however, is introduced in these signals after digital processing. Thus techniques like WOLA have no effect on the quantization noise. A classical method of dealing with quantization noise is the use of $\Delta\Sigma$ feedback structures. Recent works on Rx beamforming like [35] have considered such circuits for low resolution receivers to “clean up” the in band signal. Although attractive, as pointed out in [36], [37], such techniques rely on circuits that eliminate matching and timing errors, which increases the power consumption considerably. Moreover, $\Delta\Sigma$ modulators also require the DAC to operate with a high oversampling ratio, which further increase the power consumption.

Thus, the only practical option to control the ACL due to quantization noise is by imposing stricter restrictions on the analog LPF which, on the first glance, implies that higher order filters will be required when low resolution DACs are used at the transmitter. Thus the filters will either take more space on the chip (when they are passive) or consume higher power (for active CMOS filters). In Sec. VI-C we perform extensive simulations to determine the order of the LPF that meets the ACLR requirements at the Tx. More importantly, in the sequel we show that, for moderately low resolution DACs, no additional restrictions are imposed on the analog LPF.

C. Transmitted Signal Quality

As in [38], we quantify the transmitted signal quality by its EVM. Intuitively, the EVM captures the error in the modulated symbol produced due to Tx impairments. It is considered a key factor in determining the maximum modulation order a transmitter can faithfully support. The EVM, ϵ , for a Tx signal is given as

$$\epsilon^2 = \frac{\mathbb{E}_{t,f}|Z(t,f) - I(t,f)|^2}{\mathbb{E}_{t,f}|I(t,f)|^2}, \quad (36)$$

where $Z(t,f)$ and $I(t,f)$ are the received symbol and ideal modulation symbol respectively at time t and sub-carrier f . From (36) it is clear that lower the value of ϵ the cleaner is the transmitted signal and ϵ must be small in order to support high order modulations as they are more sensitive to distortions.

The signal impairments introduced by the mmWave RFFE, including the local oscillator (LO) phase noise, LO leakage, I-Q imbalance, etc. can be modeled as an AWGN noise source following the work in [39]. We represent the RF impairments as zero mean complex Gaussian random variable $n_{\text{RF}} \sim \mathcal{CN}(0, \sigma_{\text{RF}}^2)$. Based on the AQNM in (13), we can rewrite (36) as

$$\epsilon^2 = \alpha^2 + \frac{\sigma_{\text{RF}}^2 + \sigma_v^2}{\mathbb{E}|I(t,f)|^2}, \quad (37)$$

where σ_v^2 is the variance of the quantization noise. Thus the presence of quantization noise effectively limits the EVM from going to 0 even when $\sigma_{\text{RF}} \rightarrow 0$. Thus, low resolution quantizers essentially limit the maximum spectral efficiency that can be achieved by limiting the highest modulation order that can be supported by a transmitter. This is crucial for the utilization of the large bandwidths available at mmWave frequencies.

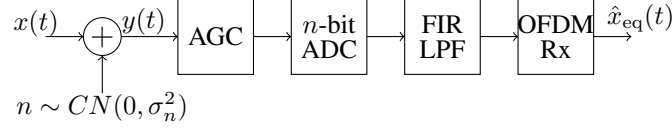
In Sec. VI-C, we perform extensive simulations to analyze the effect of low resolution DACs on transmitted signal quality. We show, in the sequel, that low resolution DACs can be used for mmWave transmitters under 3GPP specified limits on ACLR and EVM.

VI. SIMULATIONS RESULTS

In this section we present our results obtained through link level and cellular simulations. Firstly, we verify the AQNM presented in Section IV through a series of link layer OFDM simulations. Next, we use a multi-cell multi-user simulation at 28 GHz to study the effect of low resolution quantization and multiple access schemes on link quality and throughput. Finally, we investigate the effect of low resolution DACs on the transmitted signals.

Parameter	Value
Channel bandwidth (f_{BW}^{ch})	400 MHz
FFT size (N_{fft})	4096
Subcarrier spacing	120 kHz
OFDM chip rate (f_{chip})	491.52 MHz
Subcarriers per PRB	12
Max. PRBs used (N_{PRB}^{max})	275
Symbol duration	10.67 μs .

TABLE II: OFDM parameters for link level simulations.

Fig. 5: Simulation model for Rx front end with low resolution ADC modeled as a n -bit scalar quantizer.

A. Verification of the AQNM

To verify the proposed AQNM for low resolution converters, we use a link level OFDM simulator. The simulation parameters, given in Table II, are from the 3GPP NR standards [40]. We consider a wide band AWGN channel with a receiver structure shown in Fig. 5. The ADC is modeled as a finite resolution scalar quantizer. The automatic gain control (AGC) ensures that the input to the quantizer has a unit variance. A digital finite impulse response (FIR) LPF is used at the output of the ADC to remove out of band noise. Here, we note that practical transceivers will also employ analog LPFs before the ADC or the AGC to eliminate adjacent bands. As we do not model adjacent carrier blocking, in this simulation we omit the analog filtering.

Orthogonal Transmission: Fig. 6 compares the effective SNR predicted by the AQN model with the simulated post-equalization SNR, for varying quantization levels (n). The value of α is computed assuming an optimal uniform n -bit quantizer [13]. From Fig. (6a), we see that the AQNM very accurately predicts the signal degradation due to finite quantizer resolution. Here we consider that the Tx uses a $n + 2$ -bit DAC, and hence, quantization noise added by the Tx is 6 dB lower than that at the Rx. We observe that finite quantization has the effect of saturating the effective SNR in the high SNR regime. On the other hand, with $n > 3$, at SNRs below 15 dB, the effect of quantization noise on the system is negligible. In the case when both the DAC at the Tx and the ADC at the Rx have n -bits of resolution, the quantization noise power doubles.

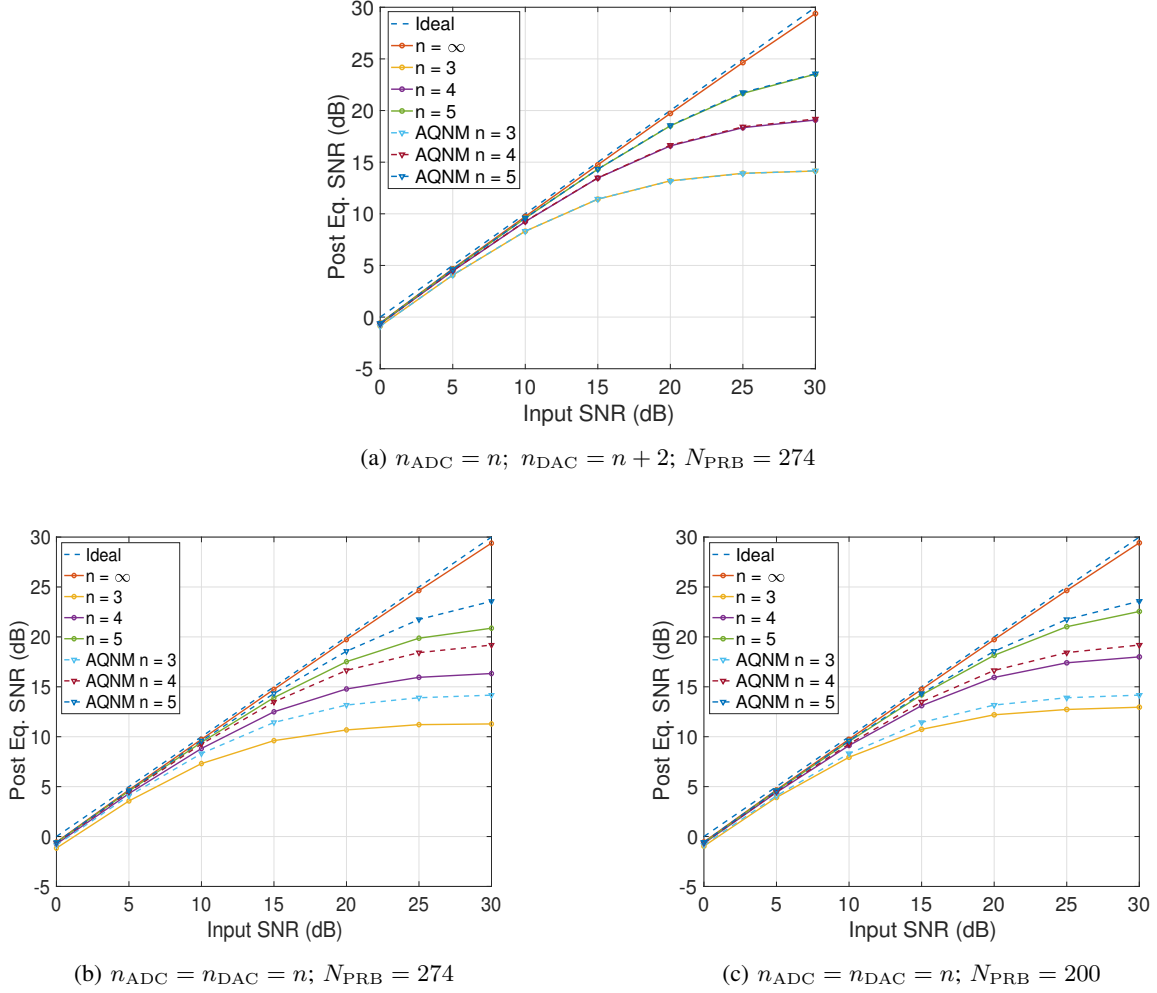


Fig. 6: Post-equalization SNR as a function of input SNR for varying quantization levels. Fig. (6a) shows the accuracy our proposed AQNM with link level OFDM simulation based on 3GPP NR specification under assumption of orthogonal transmission, i.e., no ICI. Fig. (6b) and (6c) show the effect of oversampling on quantization noise to highlight the trade-off between quantization noise and spectral efficiency.

This is observed in Fig. 6b, where in the high SNR regime, the simulated curve is nearly 3dB lower than that predicted by (24).

More interestingly, comparing Fig. 6b and Fig. 6c, we observe the effect of *oversampling* on quantization noise. OFDM systems generally have a OFDM chip rate slightly higher than the signal bandwidth. For instance in 3GPP NR, for a 400 MHz channel the OFDM chip rate is 491.52 MHz. In the high SNR regime, in the presence of quantization noise, for a system with

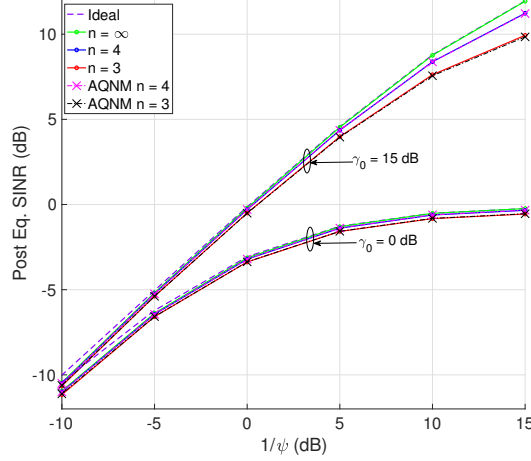


Fig. 7: Post-equalization SINR as a function SIR ($1/\psi$) with varying quantization levels. Results validate the proposed AQNM using link level OFDM simulation based on 3GPP NR specification in the presence of ICI.

n -bit quantizers at both Tx and Rx, we can express the SNR as,

$$\text{SNR (lin.)} = \frac{P_{\text{sig}}}{N_0 + \frac{2}{\text{OSR}} \sigma_q^2},$$

$$\text{SNR (dB)} \approx P_{\text{sig}}(\text{dB}) + 3 + 10 \log_{10}(\sigma_q^2) + 10 \log_{10}(N_{\text{fft}}/N_{\text{sc}}), \quad (38)$$

where $\text{OSR} = \frac{N_{\text{fft}}}{N_{\text{sc}}}$ is the oversampling ratio. The effect of oversampling is shown in Fig. 6b and Fig. 6c where by using only 200 PRBs, as opposed to 274, we increase the OSR from 0.95 dB to 2.32dB. This points to an interesting trade-off. When the system employs low resolution quantizers, under good channel conditions, oversampling by using a smaller part of the bandwidth can reduce the quantization noise at the cost of spectral efficiency, but without an increase in power consumption. In fact, in the high SNR regime, reducing the size of the bandwidth part can enable the use of high modulation and coding schemes boosting rates.

SDMA: Next, we turn our attention to the effect of quantization noise on systems with in-band ICI. Such scenarios are interesting especially when large arrays are available at the Tx and multiple users can be scheduled on multiple transmit beams simultaneously with all the available bandwidth (SDMA). In practical scenarios, transmitting on multiple orthogonal beams may not be possible due to the nature of the multi-user channel. In such cases, ICI becomes dominant in the system. In Fig. 7 we plot the post-equalization SNR versus the SIR ($1/\psi$). As predicted by (34), when the interference is low (high SIR), without any assumption on processing gains (G_k) at the receiver, there is effectively no degradation due to quantization noise. The effect of

Parameter	Value
Cell radius	100 m
Carrier frequency	28 GHz
Pathloss model	[22]
DL bandwidth (W_{tot})	1 GHz
DL Tx power	35 dBm
Rx noise figure	8 dB
Max. spectral efficiency	7.4063 b/s/Hz
BS antenna array	8×8 uniform planar
UE antenna array	4×4 uniform planar
BF mode	Digital long-term, single stream
Transmission time interval	$125\mu\text{s}$
Control overhead	20%
Traffic Model	Full buffer

TABLE III: Multi-cell simulation parameters.

quantization only becomes perceivable when SIR is high as discussed in Sec. IV-C. As shown in Fig. 7, in noise-limited scenarios ($\gamma_0 = 0$ dB), even at high SIR, the degradation due to quantization noise is less than 0.5 dB. Substantial loss in link quality is only observed when both SIR and SNR (γ_0) are high, as seen in Fig. 7 for $\gamma_0 = 15$ dB. For ADCs with 3-bits of resolution we observe approximately 2 dB of loss in the SINR due to finite quantization. For 4-bits of resolution, this loss is less than 1 dB.

B. Multi-cell Multi-user Simulations

We apply our link layer AQNM to understand the effect of low-resolution quantization on the DL system capacity. We simulate a 1Km by 1Km area covered by hexagonal cells of radius 100 m. Each cell is assumed to serve on average 10 UEs which are randomly “dropped”. We then compute a random path loss between the BS and the UEs based on the urban mmWave channel model presented in [22]. We simulate a DL transmission scenario where BSs transmit a single stream to every user. Both BSs and UEs are assumed to perform longterm digital beamforming [41] making use of the spatial second-order statistics of channel. For our simulations, we assume that the BSs always have data to send to every UE (full buffer assumption). The relevant parameters for our simulations are summarized in Table III.

OFDMA: For OFDMA based cellular systems, within one transmission time interval (TTI), each UE is assigned a non-overlapping part of the total bandwidth by the associated BS. Each link gets full beamforming gain but only uses a part of the total bandwidth. Orthogonalization in

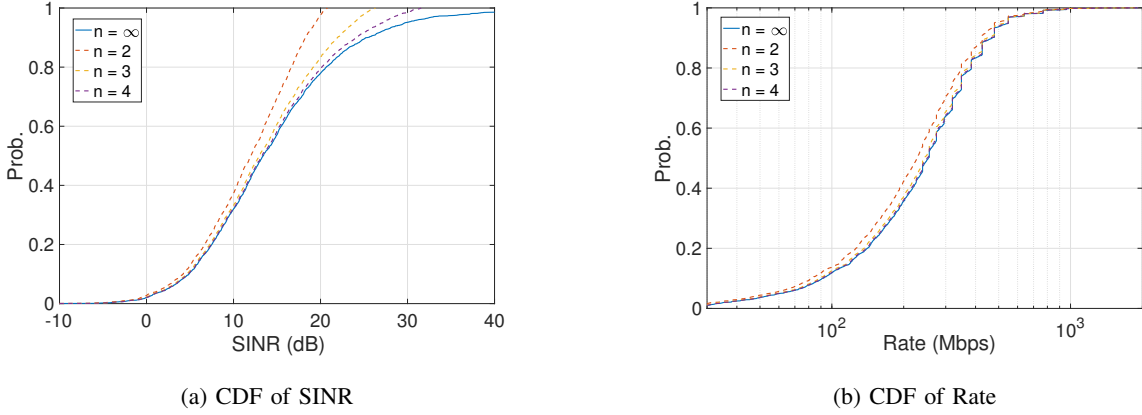


Fig. 8: Millimeter wave DL multi user simulations showing the effect of low resolution ADCs on the link quality and achieved rates using OFDMA as the multiple access scheme. For $n \geq 3$ the loss due to quantization becomes negligible since very few users operate at a sufficiently high SNR for quantization noise to have noticeable impact.

frequency eliminates ICI but also limits the maximum achievable rate. To study such a system in a practical setting, we employ a proportional fair scheduling algorithm for medium access control. At the T -th TTI, the k -th UE associated to BS j is assigned a weight,

$$w_{j,k}^T = \frac{\rho_{j,k}^T}{\sum_{t=0}^T r_t} \quad (39)$$

where r_t is the data that had been scheduled at the t -th TTI, and $\rho_{j,k}^T$ is the spectral efficiency of the link at the T -th TTI. Note that, for our simulations we assume that the BS does not have any information about the quantization noise at the Rx. The weights are normalized as $w_{j,k}^T = w_{j,k}^T / \sum_k w_{j,k}^T$, and each UE k associated with the BS j is assigned a bandwidth of $W_{j,k}^T = w_{j,k}^T \times W_{\text{tot}}$.

In Fig. 8a we plot the distribution function of the DL SINR for systems with $n = 2, 3$ and 4 bits of quantization at the Rx. For comparison, we also plot the case when infinite ADC resolution is available ($n = \infty$) at the Rx. We observe that at low SINR, the deviation from the $n = \infty$ curve is minimal if any. On the other hand, at high SINR regimes we observe a “clipping” of the maximum achievable SINR. More specifically, the SINR penalty for 2 bit quantization is nearly 10 dB for the 90-th percentile UE. For a 3-bit Rx, the 90-th percentile UEs have less than 5 dB of loss in SINR. In the 50-th percentile on the other hand, this difference goes down to about 2.5 and 1 dB for 2 and 3-bits of resolutions respectively.

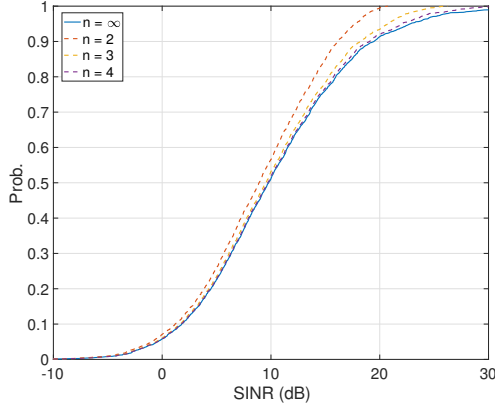
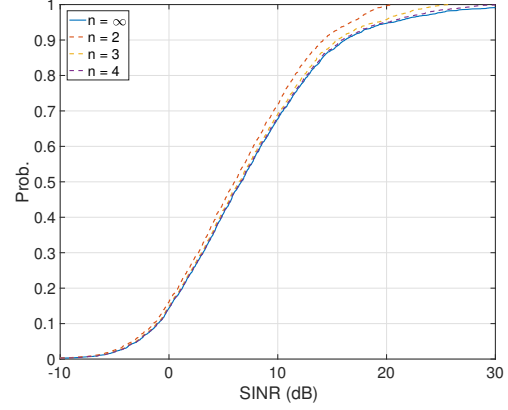
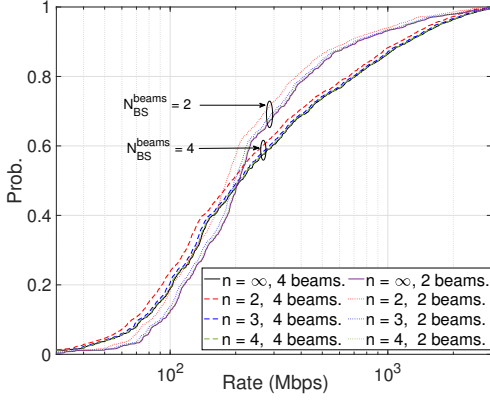
We next plot the achievable rates under various quantizer resolutions in Fig. 8b. Following the analysis in [22] and the link-layer model [42], we assume a 3 dB loss from Shannon capacity, and a 20% overhead. A maximum spectral efficiency of $\rho = 7.4063$ b/s/Hz is assumed based on the 256 QAM modulation scheme proposed in 3GPP NR standards [43]. The loss due to quantization is not noticeable for $n \geq 3$. This is because very few users in the system will operate at high SINR, thus the clipping of SINR as observed in Fig. 8b has little effect on the average rate. Moreover, as rate is a logarithmic function of the SINR, increasing the SINR beyond a certain point produces diminishing increase in the rate, more so with a limit on the maximum spectral efficiency. Further, we observe that under full buffer assumption, TDMA and OFDMA will achieve same rates under identical settings. But, OFDMA is only possible through digital BF, and is more efficient for low latency transmission of short mission critical data packets. Analog or hybrid BF based systems have to rely on TDMA where transmission of short packets can be either wasteful in terms of radio resource utilization or incur high latencies [18].

SDMA: Following our discussion in Sec. III-B, for SDMA systems, each BS assigns the entire bandwidth to k users at any given TTI. The number of users scheduled at each TTI depends on the maximum number of simultaneous beams supported by the system $N_{\text{beam}}^{\text{max}}$ and the multi-user channel condition. A simple scheduler based on proportional fair selection and sum rate maximization is used to demonstrate our results. At the T -th TTI, the j -th BS will select a group of UEs $\mathcal{K}(T)$ where $|\mathcal{K}(T)| \leq N_{\text{BS}}^{\text{beams}}$. The first UE k_1 is selected into the scheduled group $\mathcal{K}(T)$ such that,

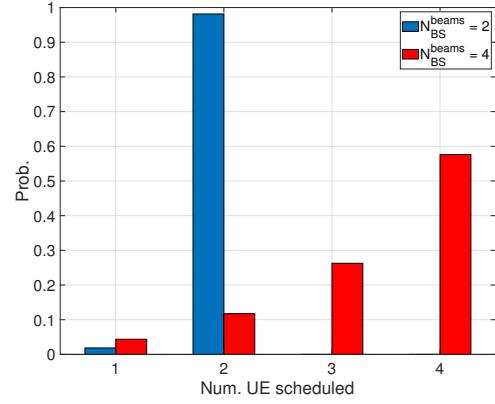
$$w_{j,k_1}^T = \max_k w_{j,k}^T, \quad (40)$$

where $w_{j,k}^T$ is computed using (39). Next, the j -th BS will admit users to the scheduled group if the achievable sum rate of the BS increases by that admission. The BS will stop admitting users to the scheduled group at a given TTI when either $N_{\text{BS}}^{\text{beams}}$ UEs are scheduled or given the associated UEs, no UE can be added to the group such that the sum-rate increases.

We present our results for a SDMA system with $N_{\text{BS}}^{\text{beams}} = 2$ and 4 in Fig. 9. From Fig. 9 we observe the SINR clipping due to quantization noise. We also notice the effect of ICI on the system by comparing Fig. 9a with Fig. 8a. We observe that with 2-stream SDMA around 5% of the users have a SINR less than 0 dB; with OFDMA less than 1% of the UEs have SINR < 0 dB. Also, the median SINR in the SDMA case is 10 dB for $N_{\text{BS}}^{\text{beams}} = 2$ and 7 dB for $N_{\text{BS}}^{\text{beams}} = 4$; a 3 and 6 dB additional loss compared to OFDMA with infinite resolution ADCs.

(a) CDF of SINR for $N_{BS}^{\text{beams}} = 2$ (b) CDF of SINR for $N_{BS}^{\text{beams}} = 4$ 

(c) CDF of rate



(d) Distribution of the number of UEs scheduled by each BS per TTI

Fig. 9: Millimeter wave DL multi user simulations showing the effect of low resolution ADCs on the link quality and achieved rates for SDMA with $N_{BS}^{\text{beams}} = \{2, 4\}$ spatial streams.

In spite of the presence of ICI, like OFDMA, the effects of low resolution quantization is only noticeable in the high SINR regimes as evident from Figs. 9a and 9b. We note that due to ICI, the beamformed DL SINR rarely exceeds 30 dB hence, with $n = 4$ or more bits of resolution, there is no noticeable loss in link quality. Moreover, with multi-stream SDMA, rates greater than 1 Gbps can be achieved for the top 7% and 14% users with $N_{BS}^{\text{beams}} = 2$ and 4 respectively as shown in Fig. 9c. This is a considerable improvement over OFDMA where less than the top 1% of the users achieved rates higher than 1 Gbps. More importantly, we notice that there is very little penalty in the achieved rates due to quantization noise. Especially with $n = 3$ or more bits

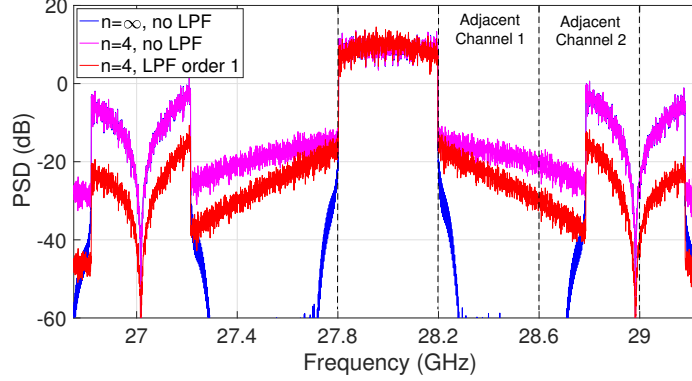


Fig. 10: Transmit p.s.d of a 400 MHz OFDM signal using a DAC sampled at $f_s = 983.04$ MHz centered at $f_c = 28$ GHz. Observe the effects of quantization noise on the adjacent channels centered at 28.4 and 28.8 GHz where a LPF of order 1 has considerable impact on leakage attenuation.

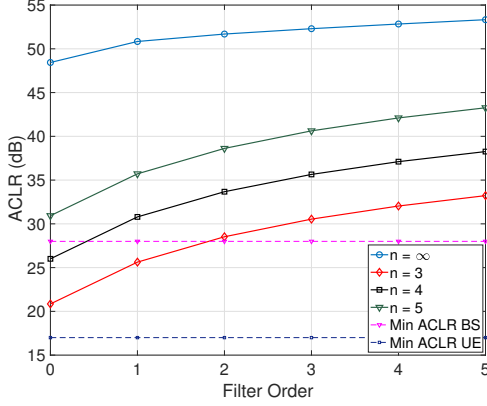
of resolution, the effect of quantization noise at the receiver on the average rate is negligible.

Interestingly, in Fig. 9d we observe that with $N_{BS}^{\text{beams}} = 2$, in more than 95% of the scheduling instances, the maximum possible number of beams are used. On the other hand when $N_{BS}^{\text{beams}} = 4$, we see that less than 60% of the time are all the beams are used. Based on our results, we draw the following conclusions. To fully utilize the available spatial degrees of freedom offered by fully digital beamforming for data transmissions, sophisticated scheduling algorithms will be necessary. Yet, even when large number of users are scheduled simultaneously, the effect of low resolution quantization is negligible in the case of SDMA transmissions.

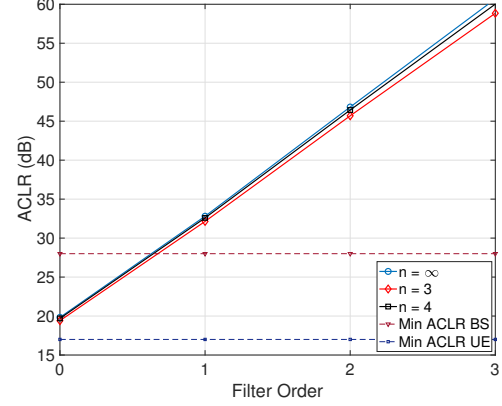
C. Transmitter Characteristics

Finally, we look into the effects of a low resolution DAC on the transmitted signal quality. As discussed in Sec. V, the quantization noise not only corrupts the transmitted signal but also increases the leakage into the adjacent channels. This is evident from the power spectral density (p.s.d) of the transmitted signal plotted in Fig. 10. The signal is transmitted over a $f_{BW}^{\text{ch}} = 400$ MHz channel around $f_c = 28$ GHz. The adjacent channels are f_{BW}^{ch} wide and located around $f = f_c \pm n f_{BW}^{\text{ch}}$, $n = 1, 2, \dots$. The DAC operates at a sampling frequency of $f_s = 2 \times f_{\text{chip}} = 983.04$ MHz. The ACLR is measured over the maximum occupied bandwidth of $f_{BW}^{\text{meas}} = 396$ MHz.

In Fig. 10 we can note two important points. Firstly, the quantization noise considerably increases the leakage in the adjacent channels. For instance, the leakage is nearly 40 dB higher



(a) ACLR on adjacent channel 1.



(b) ACLR on adjacent channel 2.

Fig. 11: ACLR versus filter order for Butterworth LPF measured over adjacent channels 1 and 2. A filter order of 0 implies the absence of a LPF at the DAC output. Also shown in dashed lines are the ACLR requirements at the BS and the UE specified by 3GPP NR specifications at mmWave frequencies (frequency range 2).

for $n = 4$ compared to the $n = \infty$. Secondly, the LPF serves two crucial purposes. Not only does it attenuate the out-of-band quantization noise, it also removes the spectral images introduced by the DAC. In fact, with $n = \infty$, adjacent channel 2 in Fig. 10, has a very low ACLR due to the presence of the spectral image. In Fig. 11 we plot the ACLR on adjacent channel 1 and 2 versus the LPF order when the LPF is modeled as a Butterworth filter. For 5G mmWave systems, the 3GPP standards [38], [44] specify the ACLR to be 28 dB and 17 dB for BSs and UEs respectively. From Fig. 11a we see that for $n \geq 4$, an order-1 Butterworth filter is sufficient to meet the ACLR requirements at the BS. Moreover, from Fig. 11b, we note that to achieve acceptable ACLR over adjacent channel 2, i.e., have sufficient image rejection, the BS Tx requires at least an order-1 Butterworth LPF.

Interestingly, we observe that at the BS, in order to knock out spectral images, a Butterworth LPF of order 1 is necessary regardless of the DAC resolution. Further, this LPF is also sufficient to attenuate the out of band quantization noise below the level specified in [38] when $n \geq 4$ bits of quantization are used. Thus, for a BS Tx, the out of band emissions due to finite quantization can be sufficiently attenuated without an increase in the hardware complexity when compared to the infinite resolution case. Moreover, as shown in Sec. II, low order active analog filters consume very little power in the current state of the art. For UEs, the ACLR requirements [44] are met for $n \geq 3$ without any assumption on filtering for both adjacent channels 1 and 2. Hence,

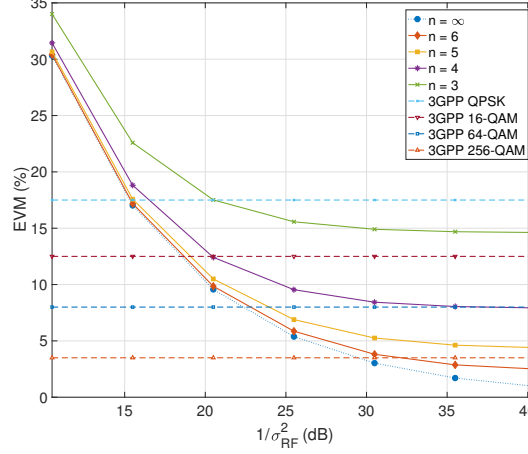


Fig. 12: EVM vs. RF impairment comparing the performance of finite resolution DACs with a first order Butterworth LPF at the output. Dashed lines represent the minimum EVM requirements for different modulation orders.

low resolution DACs can be used on UE FEs possibly without any analog LPF. For fully digital systems, this implies a saving in power or chip area for UEs.

To conclude our study on low resolution DACs, in Fig. 12 we plot the EVM versus $1/\sigma_{RF}^2$ representing the Tx signal to RF impairments ratio as discussed in Sec. V-C when the Tx power is normalized to unity. From Fig. 12 we note that 4-bits of resolution are sufficient to support modulation orders up to 64-QAM which, as specified in [43], is the highest modulation order that UEs need to support. Thus, 4 bits of resolution is sufficient for DACs used in UEs. For the BS transmitter, using $n \leq 5$ implies that 256-QAM cannot be supported due to quantization noise. This does not violate specification, as 256-QAM is an optional feature in current NR standards, but limits the maximum achievable spectral efficiency on the DL. Thus, to fully exploit the large bandwidth at mmWave, the BS transmitter will need DACs with at least 6-bits of resolution. This is a feasible design choice for BSs as they operate with higher power budgets than UEs.

VII. CONCLUSION

Fully digital beamforming at mmWave requires the use of low resolution converters to keep the power consumption of the front ends reasonable. The gain in spatial multiplexing offered by the fully digital architecture is, thus, achieved at a cost of signal degradation due to coarse quantization. In this paper we have determined how many bits of resolution is required for efficient communications over wide band mmWave channels. We show that at the Rx, the loss

due 3 – 4 bits of ADC resolution is negligible for practical cellular deployment scenarios. More interestingly, we show that mmWave receivers with 3 – 4 bits of ADCs precision can achieve multi-Gbps rates when SDMA scheduling is used for medium access. For transmitters, low resolution DACs, with 4 or more bits of precision, meet the 3GPP transmission regulations on ACL without any additional hardware costs both at the BS and the UE. Further, we show that the EVM required for the transmission of 64-QAM is met by 4-bit DACs while 6-bits of DAC resolution is required to support 256-QAM. This implies that 4-bits of resolution is sufficient at the UE Tx while 6-bits may be required for BS DACs when the 256-QAM is to be supported. Thus, low resolution fully digital beamforming can be used both at the receiver and the transmitter of wide band mmWave cellular equipments.

A major concern for fully digital beamformers at mmWave will be the cost and power draw by the baseband processor. This requires the design and analysis of digital beamformers at mmWave, similar to [45], with low resolution converters. Moreover, the flexibility offered by digital beamforming cannot be exploited without efficient communication protocols. Current mmWave cellular systems are designed on the assumption of analog or hybrid beamforming. To enable ultra low latency communications using fully digital BF, the design of control and data channels need to be revisited at mmWave frequencies. In fact, future research and standardizations efforts need to consider the practicality and potentials of fully digital beamforming.

REFERENCES

- [1] S. Rangan, T. S. Rappaport, and E. Erkip, "Millimeter-wave cellular wireless networks: Potentials and challenges," *Proc. IEEE*, vol. 102, no. 3, pp. 366–385, Mar. 2014.
- [2] B. Sadhu, Y. Tousi, J. Hallin, S. Sahl, S. Reynolds, O. Renström, K. Sjögren, O. Haapalahti, N. Mazar, B. Bokinge, G. Weibull, H. Bengtsson, A. Carlinger, E. Westesson, J. Thillberg, L. Rexberg, M. Yeck, X. Gu, D. Friedman, and A. Valdes-Garcia, "A 28GHz 32-element phased-array transceiver IC with concurrent dual polarized beams and 1.4 degree beam-steering resolution for 5G communication," in *Proc. ISSCC*, Feb. 2017, pp. 128–129.
- [3] W. Roh, J. Seol, J. Park, B. Lee, J. Lee, Y. Kim, J. Cho, K. Cheun, and F. Aryanfar, "Millimeter-wave beamforming as an enabling technology for 5G cellular communications: theoretical feasibility and prototype results," *IEEE Commun. Mag.*, vol. 52, no. 2, pp. 106 – 113, Feb. 2014.
- [4] F. Khan and Z. Pi, "An introduction to millimeter-wave mobile broadband systems," *IEEE Commun. Mag.*, vol. 49, no. 6, pp. 101 – 107, Jun. 2011.
- [5] X. Zhang, A. Molisch, and S.-Y. Kung, "Variable-phase-shift-based RF-baseband codesign for MIMO antenna selection," *IEEE Trans. Signal Process.*, vol. 53, no. 11, pp. 4091–4103, Nov. 2005.
- [6] J. H. C. van den Heuvel, J. M. G. Linnartz, P. G. M. Baltus, and D. Cabric, "Full MIMO spatial filtering approach for dynamic range reduction in wideband cognitive radios," *IEEE Trans. Circuits and Syst. I: Reg. Papers*, vol. 59, no. 11, pp. 2761–2773, Nov. 2012.

- [7] J. Mo and R. W. Heath, "Capacity analysis of one-bit quantized MIMO systems with transmitter channel state information," *IEEE Trans. Signal Process.*, vol. 63, no. 20, pp. 5498–5512, Oct. 2015.
- [8] S. Jacobsson, G. Durisi, M. Coldrey, U. Gustavsson, and C. Studer, "Throughput analysis of massive MIMO uplink with low-resolution ADCs," *IEEE Trans. on Wireless Commun.*, vol. 16, no. 6, pp. 4038–4051, Jun. 2017.
- [9] J. Singh, O. Dabeer, and U. Madhow, "On the limits of communication with low-precision analog-to-digital conversion at the receiver," *IEEE Trans. Commun.*, vol. 57, no. 12, pp. 3629–3639, Dec. 2009.
- [10] C. Mollen, J. Choi, E. G. Larsson, and R. W. Heath, "Uplink performance of wideband massive mimo with one-bit ADCs," *IEEE Trans. Wireless Commun.*, vol. 16, no. 1, pp. 87–100, Jan. 2017.
- [11] J. Mo, P. Schniter, and R. W. Heath, "Channel estimation in broadband millimeter wave MIMO systems with few-bit ADCs," *IEEE Trans. Signal Process.*, vol. 66, no. 5, pp. 1141–1154, Mar. 2018.
- [12] A. Mezghani and A. L. Swindlehurst, "Blind estimation of sparse broadband massive MIMO channels with ideal and one-bit ADCs," *IEEE Trans. Signal Process.*, vol. 66, no. 11, pp. 2972–2983, Jun. 2018.
- [13] O. Orhan, E. Erkip, and S. Rangan, "Low power analog-to-digital conversion in millimeter wave systems: Impact of resolution and bandwidth on performance," in *Proc. ITA Wkshp.*, Feb. 2015, pp. 191–198.
- [14] J. Mo, A. Alkhateeb, S. Abu-Surra, and R. W. Heath, "Hybrid architectures with few-bit ADC receivers: Achievable rates and energy-rate tradeoffs," *IEEE Trans. Wireless Commun.*, vol. 16, no. 4, pp. 2274–2287, Apr. 2017.
- [15] C. Barati Nt., S. Hosseini, S. Rangan, P. Liu, T. Korakis, S. Panwar, and T. S. Rappaport, "Directional cell discovery in millimeter wave cellular networks," *IEEE Trans. Wireless Commun.*, vol. 14, no. 12, pp. 6664 – 6678, Nov. 2015.
- [16] C. N. Barati, S. A. Hosseini, M. Mezzavilla, T. Korakis, S. S. Panwar, S. Rangan, and M. Zorzi, "Initial access in millimeter wave cellular systems," *IEEE Trans. Wireless Commun.*, vol. 15, no. 12, pp. 7926–7940, Dec. 2016.
- [17] M. Giordani, M. Polese, A. Roy, D. Castor, and M. Zorzi, "A tutorial on beam management for 3GPP NR at mmWave frequencies," *arXiv preprint arXiv:1804.01908*, 2018.
- [18] S. Dutta, M. Mezzavilla, R. Ford, M. Zhang, S. Rangan, and M. Zorzi, "Frame structure design and analysis for millimeter wave cellular systems," *IEEE Trans. Wireless Commun.*, vol. 16, no. 3, pp. 1508–1522, Mar. 2017.
- [19] S. Dutta, C. N. Barati, A. Dhananjay, and S. Rangan, "5G millimeter wave cellular system capacity with fully digital beamforming," in *Proc. Asilomar Conf. on S, S & C*, Oct. 2017, pp. 1224–1228.
- [20] I. Song, J. Jeon, H. Jhon, J. Kim, B. Park, J. D. Lee, and H. Shin, "A simple figure of merit of RF MOSFET for low-noise amplifier design," *IEEE Electron Device Lett.*, vol. 29, no. 12, pp. 1380–1382, Dec. 2008.
- [21] E. Adabi, B. Heydari, M. Bohsali, and A. M. Niknejad, "30 GHz CMOS low noise amplifier," in *Proc. IEEE RFIC Symp.*, Jun. 2007, pp. 625–628.
- [22] M. Akdeniz, Y. Liu, M. Samimi, S. Sun, S. Rangan, T. Rappaport, and E. Erkip, "Millimeter wave channel modeling and cellular capacity evaluation," *IEEE J. Sel. Areas Commun.*, vol. 32, no. 6, pp. 1164–1179, Jun. 2014.
- [23] Y. Wang, B. Afshar, L. Ye, V. C. Gaudet, and A. M. Niknejad, "Design of a low power, inductorless wideband variable-gain amplifier for high-speed receiver systems," *IEEE Trans. Circuits and Syst. I: Reg Papers*, vol. 59, no. 4, pp. 696–707, Apr. 2012.
- [24] A. Alkhateeb, J. Mo, N. González-Prelcic, and R. W. Heath, "MIMO precoding and combining solutions for millimeter-wave systems," *IEEE Commun. Mag.*, vol. 52, no. 12, pp. 122–131, Dec. 2014.
- [25] B. Nasri, S. P. Sebastian, K. D. You, R. RanjithKumar, and D. Shahrjerdi, "A 700 uW 1GS/s 4-bit folding-flash ADC in 65nm CMOS for wideband wireless communications," in *Proc. ISCAS*, May 2017, pp. 1–4.
- [26] E. Olieman, A. J. Annema, and B. Nauta, "An interleaved full nyquist high-speed DAC technique," *IEEE J. Solid-State Circuits*, vol. 50, no. 3, pp. 704–713, Mar. 2015.

- [27] Juanda, W. Shu, and J. S. Chang, "A calibration-free/DEM-free 8-bit 2.4-GS/s single-core digital-to-analog converter with a distributed biasing scheme," *IEEE Trans. Very Large Scale Integr. (VLSI) Syst.*, vol. 26, no. 11, pp. 2299–2309, Nov. 2018.
- [28] S. Kim, M. Kim, B. Sung, H. Kang, M. Cho, and S. Ryu, "A SUC-based full-binary 6-bit 3.1-GS/s 17.7-mW current-steering DAC in 0.038 mm²," *IEEE Trans. Very Large Scale Integr. (VLSI) Syst.*, vol. 24, no. 2, pp. 794–798, Feb. 2016.
- [29] F. Houfay, M. Egot, A. Kaiser, A. Cathelin, and B. Nauta, "A 65nm CMOS 1-to-10GHz tunable continuous-time low-pass filter for high-data-rate communications," in *Proc. IEEE ISSCC*, Feb. 2012, pp. 362–364.
- [30] Y. Chen, P. Mak, S. D'Amico, L. Zhang, H. Qian, and Y. Wang, "A single-branch third-order pole-zero low-pass filter with 0.014-mm² die size and 0.8-kHz (1.25-nW) to 0.94-GHz (3.99-mW) bandwidth-power scalability," *IEEE Trans. Circuits and Syst., II: Exp. Briefs*, vol. 60, no. 11, pp. 761–765, Nov. 2013.
- [31] A. K. Fletcher, S. Rangan, V. K. Goyal, and K. Ramchandran, "Robust predictive quantization: Analysis and design via convex optimization," *IEEE J. Sel. Topics Signal Process.*, vol. 1, no. 4, pp. 618–632, Dec. 2007.
- [32] J. P. Kermoal, L. Schumacher, K. I. Pedersen, P. E. Mogensen, and F. Frederiksen, "A stochastic MIMO radio channel model with experimental validation," *IEEE J. Sel. Areas Commun.*, vol. 20, no. 6, pp. 1211–1226, Aug. 2002.
- [33] M. S. Alavi, R. B. Staszewski, L. C. N. de Vreede, and J. R. Long, "A wideband 2×13-bit all-digital I/Q RF-DAC," *IEEE Trans. Microw. Theory Tech.*, vol. 62, no. 4, pp. 732–752, Apr. 2014.
- [34] Qualcomm Incorporated, "Waveform candidates," R1-162199, 2016.
- [35] D. S. Palguna, D. J. Love, T. A. Thomas, and A. Ghosh, "Millimeter wave receiver design using low precision quantization and parallel $\Delta\Sigma$ architecture," *IEEE Trans. Wireless Commun.*, vol. 15, no. 10, pp. 6556–6569, Oct. 2016.
- [36] M. Mehrpoo, M. Hashemi, Y. Shen, L. C. N. de Vreede, and M. S. Alavi, "A wideband linear I/Q-interleaving DDRM," *IEEE J. of Solid-State Circuits*, vol. 53, no. 5, pp. 1361–1373, May 2018.
- [37] E. Roverato, M. Kosunen, K. Cornelissens, S. Vatti, P. Stynen, K. Bertrand, T. Korhonen, H. Samsom, P. Vandenameele, and J. Ryynanen, "All-digital RF transmitter in 28nm CMOS with programmable RX-band noise shaping," in *Proc. IEEE ISSCC*, Feb. 2017, pp. 222–223.
- [38] 3GPP, "Base station (BS) radio transmission and reception," TS 38.104 (release 15), 2018.
- [39] A. K. Gupta and J. F. Buckwalter, "Linearity considerations for low-EVM, millimeter-wave direct-conversion modulators," *IEEE Trans. Microw. Theory Tech.*, vol. 60, no. 10, pp. 3272–3285, Oct. 2012.
- [40] 3GPP, "Physical channels and modulation," TS 38.211 (release 15), 2018.
- [41] A. Lozano, "Long-term transmit beamforming for wireless multicasting," in *Proc. ICASSP*, vol. 3, Apr. 2007, pp. III–417–III–420.
- [42] P. Mogensen, W. Na, I. Z. Kovács, F. Frederiksen, A. Pokhariyal, K. I. Pedersen, T. Kolding, K. Hugl, and M. Kuusela, "LTE capacity compared to the Shannon bound," in *Proc. IEEE VTC*, Apr. 2007, pp. 1234–1238.
- [43] 3GPP, "Physical layer procedure for data," TS 38.214 (release 15), 2018.
- [44] —, "User equipment (UE) radio transmission and reception: Part 2: Range 2 standalone," TS 38.101-2 (release 15), 2018.
- [45] B. Yang, Z. Yu, J. Lan, R. Zhang, J. Zhou, and W. Hong, "Digital beamforming-based massive MIMO transceiver for 5G millimeter-wave communications," *IEEE Trans. Microw. Theory Tech.*, vol. 66, no. 7, pp. 3403–3418, Jul. 2018.