
Dynamic Tensor Product Regression

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Abstract

In this work, we initiate the study of *Dynamic Tensor Product Regression*. One has matrices $A_1 \in \mathbb{R}^{n_1 \times d_1}, \dots, A_q \in \mathbb{R}^{n_q \times d_q}$ and a label vector $b \in \mathbb{R}^{n_1 \dots n_q}$, and the goal is to solve the regression problem with the design matrix A being the tensor product of the matrices A_1, A_2, \dots, A_q i.e. $\min_{x \in \mathbb{R}^{d_1 \dots d_q}} \|(A_1 \otimes \dots \otimes A_q)x - b\|_2$. At each time step, one matrix A_i receives a sparse change, and the goal is to maintain a sketch of the tensor product $A_1 \otimes \dots \otimes A_q$ so that the regression solution can be updated quickly. Recomputing the solution from scratch for each round is very slow and so it is important to develop algorithms which can quickly update the solution with the new design matrix. Our main result is a dynamic tree data structure where any update to a single matrix can be propagated quickly throughout the tree. We show that our data structure can be used to solve dynamic versions of not only Tensor Product Regression, but also Tensor Product Spline regression (which is a generalization of ridge regression) and for maintaining Low Rank Approximations for the tensor product.

1 Introduction

The task of fitting data points to a line is well-known as the least-squares regression problem [Sti81], which has a wide range of applications in signal processing [RGIY78], convex optimization [Bub15], network routing [Mad13, Mad16], and training neural networks [BPSW21, SZZ21]. In this work, we study a generalized version of least-squares regression where the design matrix A is a tensor product of q smaller matrices $A_1 \otimes A_2 \otimes \dots \otimes A_q$.

Tensor products have been extensively studied in mathematics and the physical sciences since their introduction more than a century ago by Whitehead and Russell in their *Principia Mathematica* [WR12]. They have been shown to have a humongous number of applications in several areas of mathematics like applied linear algebra and statistics [VL00]. In particular, machine learning applications include image processing [NK06], multivariate data fitting [GVL13], and natural language processing [PSA21]. Furthermore, regression problems involving tensor products arise in surface fitting and multidimensional density smoothing [EM06], and structured blind deconvolution problems [OY05], among several other applications, as discussed in [DSSW18, DJS⁺19].

Solving tensor product regression in ℓ_2 norm [DJS⁺19, FFG22] to a $(1 \pm \varepsilon)$ precision takes time $\tilde{O}(\sum_{i=1}^q \text{nnz}(A_i) + \text{poly}(dq/(\delta\varepsilon)))$, where each matrix $A_i \in \mathbb{R}^{n_i \times d_i}$ and $d = d_1 d_2 \dots d_q$, and $\text{nnz}(A)$ denotes the number of nonzero entries of A . However, these algorithms are inherently *static*, meaning that even if there is a sparse (or low-rank) update to a *single* design matrix A_i , it needs to completely recompute the new solution from scratch. In modern machine learning applications, such static algorithms are not practical due to the fact that data points are always evolving and recomputing the solution from scratch every time is computationally too expensive. Hence, it is important to develop efficient *dynamic algorithms* that can adaptively update the solution. For example, graphs for real-world network data are modeled as the tensor product of a large number of

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smaller graphs [LCK⁺10]. Many important problems on these graphs can be solved by regression of the adjacency and Laplacian matrices of these graphs [ST04]. Real-world network data is always time-evolving and so it is crucial to develop dynamic algorithms for solving regression problems where the design matrix is a tensor product of a large number of smaller matrices. Hence, we ask the following question:

Can we design a dynamic data structure for tensor product regression that can handle updates to the design matrix and quickly updates the solution?

We provide a positive answer to the above question.

At the heart of our data structure is a binary tree that can maintain a succinct sketch of $A_1 \otimes \dots \otimes A_q$ and supports an update to one of the matrices quickly. Such tree structures have been useful in designing efficient sketching algorithms for tensor products of vectors [AKK⁺20, SWYZ21]. However, the goal of these works has been in reducing the sketching dimension from an exponential dependence on q to a polynomial dependence on q . Their results can be directly generalized to solving tensor product regression *statically* but not dynamically. In this work, we build upon the tree structure to solve the Dynamic Tensor Product Regression problem and other related dynamic problems like Dynamic Tensor Spline Regression and Dynamic Tensor Low Rank Approximation. Our key observation is that updating the entire tree is efficient when a single leaf of the tree gets an update: specifically, we only need to update the nodes which fall on the path from the leaf to the root.

Technical Contributions.

- We design a dynamic tree data structure **DYNAMICTENSOR TREE** that maintains a succinct representation of the tensor product $A_1 \otimes \dots \otimes A_q$ and supports efficient updates to any of the A_i ’s.
- Consequently, we develop a dynamic algorithm for solving Tensor Product Regression, when one of the matrices A_i ’s is updated and we need to output an estimate of the new solution to the regression problem quickly.
- We also show that we can use our **DYNAMICTENSOR TREE** data-structure in a fast dynamic algorithm to solve the Tensor Product Spline Regression problem, which is a generalization of the classic Ridge Regression problem.
- We also initiate the study of *Dynamic Tensor Low Rank Approximation*, where the goal is to maintain a low rank approximation (LRA) of the tensor product with dynamic updates, and show how we can use our **DYNAMICTENSOR TREE** data structure to solve this problem.

Roadmap. In section 2, we first discuss some related work. Then, we provide notation, some background for our work, and the main problem formulation in section 3. In section 4, we provide a technical overview of our paper. Following that, we provide our dynamic tree data structure in section 5. We then show how it can be used for Dynamic Tensor Product Regression, Dynamic Tensor Spline Regression, and Dynamic Tensor Low Rank Approximation in section 6. We end with our conclusion and discuss some future directions in section 7.

2 Related Work

Sketching. Sketching techniques have many applications in numerical linear algebra, such as linear regression, low-rank approximation [CW13, NN13, MM13, BW14, RSW16, SWZ17, HLW17, ALS⁺18, BBB⁺19, IWW19, SWZ19a, SWZ19b, MW21, WY22, CSTZ22], distributed problems [WZ16, BWZ16], reinforcement learning [WZD⁺20, SSX21], projected gradient descent [XSS21], training over-parameterized neural networks [SYZ21, SZZ21], tensor decomposition [SWZ19c], clustering [EMZ21], cutting plane method [JLSW20], generative adversarial networks [XZZ18], recommender systems [RRS⁺22], and linear programming [LSZ19, JSWZ21, SY21].

Dynamic Least-squares Regression. A dynamic version of the ordinary least-squares regression problem (without a tensor product design matrix) was recently studied in [JPW22]. In their model, at each iteration, a new row is prepended to the design matrix A and a new coordinate is prepended

to the vector b . To efficiently maintain the necessary parts of the design matrix and the solution, they make use of a fast leverage score maintenance data structure by [CMP20] and achieve a running time of $\tilde{O}(\text{nnz}(A^{(T)}) + \text{poly}(d, \varepsilon^{-1}))$ where T is the number of time steps.

Tensor/Kronecker Product Problems. Many machine learning problems involve tensor/Kronecker product computations such as, regression [HLW17, DSSW18, DJS⁺19], low-rank approximation [SWZ19c], fast kernel computation [SWYZ21], semi-definite programming [JKL⁺20, HJS⁺21], and training over-parameterized neural networks [BPSW21, SZZ21]. Apart from solving problems which directly involve tensor/Kronecker products, another line of research focuses on constructing polynomial kernels efficiently so that the computation of various kernel problems can be improved [AKK⁺20, SWYZ21].

3 Preliminaries

3.1 Notation

For any natural number n , we use $[n]$ to denote the set $\{1, 2, \dots, n\}$. We use $\mathbb{E}[\cdot]$ to denote expectation of a random variable if it exists. We use $\mathbf{1}[\cdot]$ and $\Pr[\cdot]$ to denote the indicator function and probability of an event respectively. For any natural numbers a, b, c , we use $\mathcal{T}_{\text{mat}}(a, b, c)$ to denote the time it takes to multiply two matrices of sizes $a \times b$ and $b \times c$. For a vector x , we use $\|x\|_2$ to denote its ℓ_2 norm. For a matrix A , we use $\|A\|_F$ to denote its Frobenius norm. For a matrix A , we use A^\top to denote the transpose of A . Given two matrices $A \in \mathbb{R}^{n_1 \times d_1}$ and $B \in \mathbb{R}^{n_2 \times d_2}$, we use $A \otimes B$ to denote their tensor product (which is also referred to as Kronecker product), i.e., $(A \otimes B)_{i_1+(i_2-1) \cdot n_1, j_1+(j_2-1) \cdot d_1} = A_{i_1, j_1} \cdot B_{i_2, j_2}$. For example, for 2×2 matrices A and B ,

$$\begin{bmatrix} a_{11} & a_{12} \\ a_{21} & a_{22} \end{bmatrix} \otimes B = \begin{bmatrix} a_{11}B & a_{12}B \\ a_{21}B & a_{22}B \end{bmatrix} = \begin{bmatrix} a_{11}b_{11} & a_{11}b_{12} & a_{12}b_{11} & a_{12}b_{12} \\ a_{11}b_{21} & a_{11}b_{22} & a_{12}b_{21} & a_{12}b_{22} \\ a_{21}b_{11} & a_{21}b_{12} & a_{22}b_{11} & a_{22}b_{12} \\ a_{21}b_{21} & a_{21}b_{22} & a_{22}b_{21} & a_{22}b_{22} \end{bmatrix}$$

We use \bigotimes to denote tensor product of more than two matrices, i.e., $\bigotimes_{i=1}^q A_i = A_1 \otimes A_2 \otimes \dots \otimes A_q$. For non-negative real numbers a and b , we use $a = (1 \pm \varepsilon)b$ to denote $(1 - \varepsilon) \cdot b \leq a \leq (1 + \varepsilon) \cdot b$. For a real matrix A , we use $\sigma_i(A)$ to denote its i -th largest singular value. For any function f , we use $\tilde{O}(f)$ to denote $O(f \text{poly}(\log f))$.

3.2 Problem Formulation

In this section, we define our task. We start with the *static version* of tensor product regression.

Definition 3.1 (Static version). In the ℓ_2 Tensor Product Regression problem, we are given matrices A_1, A_2, \dots, A_q where $A_i \in \mathbb{R}^{n_i \times d_i}$ and $b \in \mathbb{R}^{n_1 n_2 \dots n_q}$. Let us define $n := n_1 n_2 \dots n_q$ and $d := d_1 d_2 \dots d_q$. The goal is to output $x \in \mathbb{R}^d$ such that we minimize the objective:

$$\|(A_1 \otimes A_2 \otimes \dots \otimes A_q)x - b\|_2$$

Our dynamic version of ℓ_2 Tensor Product Regression involves updating one of the matrices A_i with an update matrix B . We formally define the problem as follows:

Definition 3.2 (Dynamic version). In the Dynamic ℓ_2 Tensor Product Regression problem, we are given matrices A_1, A_2, \dots, A_q where $A_i \in \mathbb{R}^{n_i \times d_i}$, a sequence $\{(i_1, B_1), (i_2, B_2), \dots, (i_T, B_T)\}$ with $i_t \in [q]$, $B_t \in \mathbb{R}^{n_{i_t} \times d_{i_t}}$ for all $t \in [T]$, and the goal is to design a data structure with the following procedures:

- **INITIALIZE:** The data structure initializes A_1, \dots, A_q and maintains an approximation to $\bigotimes_{i=1}^q A_i$.
- **UPDATE:** At time step t , given an index i_t and an update matrix B_t , the data-structure needs to update $A_{i_t} \leftarrow A_{i_t} + B_t$, and maintain an approximation to $A_1 \otimes \dots \otimes (A_{i_t} + B_t) \otimes \dots \otimes A_q$.

- **QUERY:** The data structure outputs a vector $\hat{x} \in \mathbb{R}^d$ such that

$$\|(\bigotimes_{i=1}^q A_i)\hat{x} - b\|_2 = (1 \pm \varepsilon) \min_{x \in \mathbb{R}^d} \|(\bigotimes_{i=1}^q A_i)x - b\|_2.$$

Note that although we don't discuss updates to the vector b in our model, they are easy to implement in our data-structure. In addition to the standard regression problem, we also consider the spline regression (which is a generalization of ridge regression) and low rank approximation problems. We will provide the definitions for these problems in their respective sections.

4 Technical Overview

The two main design considerations for our data structure are as follows: 1). We want a data structure that stores a *sketch* of the tensor product, this has the advantage of having a smaller storage space and a faster time to form the tensor product. 2). The update time to one of the matrices A_i should be fast, ideally we want to remove the linear dependence on q . With these two goals in mind, we introduce a dynamic tree data structure that satisfies both of them simultaneously, building on the sketch of [AKK⁺20]. Specifically, we use each leaf of the tree to represent $S_i A_i$, a sketch of the base matrix that only has m rows instead of n rows, where m is proportional to the small dimension d . Each internal node represents a sketch of the Tensor product of its two children. To speed up computation, we adapt tensor-typed sketching matrices to avoid the need to explicitly form the Tensor product of the two children. The root of the tree will store a sketch for the tensor product $\bigotimes_{i=1}^q A_i$.

We point out that in order for this tree to work, we need to apply an *independent* sketching matrix to each tree node. Since there are $2q - 1$ nodes in total, we will still have a dependence on q in the initialization phase (which will be dominated by the d term). However, during the update phase, our tree has a clear advantage: if one of the leaves has been updated, we only need to propagate the change along the path to root. Since the height of the tree is at most $O(\log q)$, we remove the linear dependence on q in the update phase. This is the key insight for obtaining a faster update time using our dynamic data structure. Moreover, the correctness follows naturally from the guarantee of the tree: as shown in [AKK⁺20], the tree itself is an Oblivious Subspace Embedding (OSE) for matrices of proper size, hence, it also preserves the subspace after updating one of the leaves. Figure 1 is an example tree for tensor product of 4 matrices.

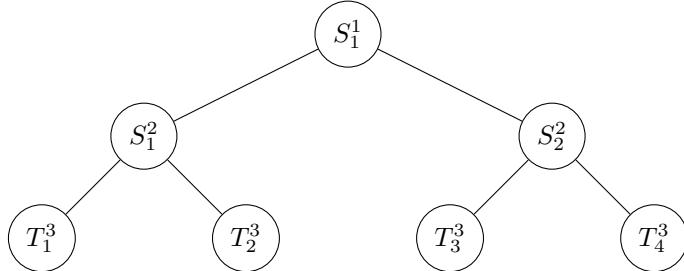


Figure 1: A tree for sketching the tensor product of 4 matrices A_1, A_2, A_3 and A_4 . The leaves use sketches of type T_{base} and internal nodes use sketches of type S_{base} . The algorithm starts to apply T_i to each matrix A_i , and then propagates them up the tree.

With this data structure, we provide efficient algorithms for dynamic Tensor product regression, dynamic Tensor Spline regression, and dynamic Tensor low rank approximation. For all these problems, we do not explicitly maintain the solutions, rather, we use our tree to quickly update a sketch of the design matrix. In the query phase (when we need to actually solve the regression problems), we use this sketch of updated design matrix to quickly compute the solution.

5 Dynamic Tree Data Structure

In this section, we introduce our data structure, which contains **INITIALIZE** and **UPDATE** procedures. We'll define task-specific **QUERY** procedures later. We defer proofs in this section to Appendix B. We start with an outline of Algorithm 1.

Algorithm 1 Our dynamic tree data structure

```

1: data structure DYNAMICTENSORTREE
2:
3: members
4:    $J_{k,\ell} \in \mathbb{R}^{m \times d^{2^\ell}}$  for  $0 \leq \ell \leq \log q$  and  $0 \leq k \leq q/2^\ell - 1$             $\triangleright \ell$  is the level of the tree.
5: end members
6:
7: procedure INITIALIZE( $A_1 \in \mathbb{R}^{n_1 \times d_1}, \dots, A_q \in \mathbb{R}^{n_q \times d_q}, m \in \mathbb{N}_+, C_{\text{base}}, T_{\text{base}}$ )
8:   for  $\ell = 0 \rightarrow \log q$  do
9:     for  $k = 0 \rightarrow q/2^\ell - 1$  do
10:      if  $\ell = 0$  then
11:        Choose a  $C_k \in \mathbb{R}^{m \times n_k}$  from  $C_{\text{base}}$ .  $\triangleright C_{\text{base}}$  can be COUNTSKETCH, SRHT or
12:          OSNAP.
13:         $J_{k,\ell} \leftarrow C_k A_k$ 
14:      else
15:        Choose a  $T_{k,\ell} \in \mathbb{R}^{m \times m^2}$  from  $T_{\text{base}}$ .  $\triangleright T_{\text{base}}$  can be TENSORSKETCH or
16:          TENSORSRHT.
17:         $J_{k,\ell} \leftarrow T_{k,\ell}(J_{2k,\ell-1} \otimes J_{2k+1,\ell-1})$             $\triangleright$  Sketch of tensor product of its children
18:      end if
19:    end for
20:  end for
21: end procedure
22: procedure UPDATE( $i \in [q], B \in \mathbb{R}^{n_i \times d_i}$ )
23:   for  $\ell = 0 \rightarrow \log(q)$  do
24:     if  $\ell = 0$  then
25:        $J_{i,0}^{\text{new}} \leftarrow C_i B$ 
26:        $J_{i,0} \leftarrow J_{i,0} + J_{i,0}^{\text{new}}$ 
27:     else
28:        $J_{\lceil i/2^\ell \rceil, \ell}^{\text{new}} \leftarrow T_{k,\ell}(J_{\lceil i/2^{\ell-1} \rceil, \ell-1}^{\text{new}} \otimes J_{\lceil i/2^{\ell-1} \rceil+1, \ell-1})$             $\triangleright$  Sibling is to the left also
29:       sometimes
30:        $J_{\lceil i/2^\ell \rceil, \ell} \leftarrow J_{\lceil i/2^\ell \rceil, \ell} + J_{\lceil i/2^\ell \rceil, \ell}^{\text{new}}$             $\triangleright$  Sketch of tensor product of its children
31:     end if
32:   end for
33: end procedure

```

Outline of Tree: For simplicity, let us assume that the number of matrices q is a power of 2. Also, let us assume that all the matrices are of the same dimension $n_1 \times d_1$. The output is a matrix of dimension $m \times d$ where $m = \text{poly}(q, d, \varepsilon^{-2})$. First, we apply q independent base sketch matrices (such as COUNTSKETCH (Def. A.6), SRHT (Def. A.8)) $\{T_i \in \mathbb{R}^{m \times n_1}, i \in [q]\}$ to each of the matrices A_1, A_2, \dots, A_q . After this step, all the leaf nodes are of size $m \times d_1$. Then, we have a tree of height $\log q$. Let us use $\ell = 0 \rightarrow \log q$ to represent the level of the tree nodes where $\ell = 0$ represents the leaves and $\ell = \log q$ represents the root node. Let us use $J_{k,\ell} \in \mathbb{R}^{n \times d^{2^\ell}}$ to denote the matrix stored at the node k, ℓ . For leaf nodes, we choose base sketches $T_k \in \mathbb{R}^{m \times n_1}$ and compute $J_{k,0} = T_k A_k$. At each internal node, we use a sketch for tensor-typed inputs (such as TENSORSKETCH (Def. A.7) or TENSORSRHT (Def. A.5)), apply it to its two children. The sketching matrix $S_i \in \mathbb{R}^{m \times m^2}$, and it can be applied to its two inputs fast, obtaining a running time of $\tilde{O}(m)$ instead of $O(m^2)$. We inductively compute the tree all the way up to the root for initialization. Note that at the root, we end up with a matrix of size $m \times d$, which is significantly smaller than the tensor product $n \times d$.

The running time guarantees of Algorithm 1, which we will prove in Appendix B, are as follows:

Theorem 5.1 (Running time of our main result. Informal version of Theorem B.1). *There is an algorithm (Algorithm 1) that has the following procedures:*

- **INITIALIZE**($A_1 \in \mathbb{R}^{n_1 \times d_1}, A_2 \in \mathbb{R}^{n_2 \times d_2}, \dots, A_q \in \mathbb{R}^{n_q \times d_q}, m \in \mathbb{N}_+, C_{\text{base}}, T_{\text{base}}$):
Given matrices $A_1 \in \mathbb{R}^{n_1 \times d_1}, A_2 \in \mathbb{R}^{n_2 \times d_2}, \dots, A_q \in \mathbb{R}^{n_q \times d_q}$, a sketching dimension m , families of base sketches C_{base} and T_{base} , the data-structure pre-processes in time $\tilde{O}(\sum_{i=1}^q \text{nnz}(A_i) + qmd)$.
- **UPDATE**($i \in [q], B \in \mathbb{R}^{n_i \times d_i}$): Given a matrix B and an index $i \in [q]$, the data structure updates the approximation to $A_1 \otimes \dots \otimes (A_i + B) \otimes \dots \otimes A_q$ in time $\tilde{O}(\text{nnz}(B) + md)$.

Remark 5.2. In our data structure, we only pay the linear q term in **INITIALIZATION** (which will be dominated by the term d), since we are maintaining a tree with $2q - 1$ nodes, each node corresponds to a matrix of m rows. In the **UPDATE** phase, we shave off the linear dependence on q . We also want to point out that the sketching dimension m is typically at least linear on q , however, it only depends on q , the small dimension d and the precision parameter ε , hence it is far more efficient to use this sketch representation compared to the original tensor product. Also, note that though we only design a data structure for q being a power of 2, it is not hard to generalize to q being an arbitrary positive integer via the technique introduced in [SWYZ21]. We only incur a $\log q$ factor by doing so.

The approximation guarantee for our dynamic tree, which follows from [AKK⁺20] and for which we provide a brief sketch in Appendix B are as follows:

Theorem 5.3 (Approximation guarantee of our dynamic tree. Informal version of Theorem B.1). *Let Π^q denote the sketching matrix generated by the dynamic tree, we then have that:*

$$\|\Pi^q(\bigotimes_{i=1}^q A_i)x\|_2 = (1 \pm \varepsilon)\|(\bigotimes_{i=1}^q A_i)x\|_2.$$

Choices of parameter m : An important parameter to be chosen is the sketching dimension m . Intuitively, it should depend polynomially on ε^{-1}, d, q . We list them in the following table.

C_{base}	T_{base}	$m(\text{dim})$	Init time
COUNTSKETCH	TENSORSKETCH	$\varepsilon^{-2}q \cdot \text{dim}^2 \cdot (1/\delta)$	$\sum_{i=1}^q \text{nnz}(A_i)$
OSNAP	TENSORSRHT	$\varepsilon^{-2}q^4 \cdot \text{dim} \cdot \log(1/\delta)$	$\sum_{i=1}^q \text{nnz}(A_i)$
OSNAP	TENSORSRHT	$\varepsilon^{-2}q \cdot \text{dim}^2 \cdot \log(1/\delta)$	$\sum_{i=1}^q \text{nnz}(A_i)$

Table 1: How different choices of C_{base} and T_{base} affect the sketching dimension $m(\text{dim})$ and initialization time that depends on the choice of sketch. We note that the sketching dimension m is a function of dim , the fundamental dimension of the problem. For example, $\text{dim} = d$ for tensor product regression and $\text{dim} = k$ for k -low rank approximation. The sketching dimension corresponds to the problem of computing an $(\varepsilon, \delta, \text{dim}, d, n)$ -OSE.

Robustness against an Adaptive Adversary: An often encountered issue in dynamic algorithms is the presence of an *adaptive adversary* [BKM⁺22]. Consider a sequence of update pairs $\{(i, B_i)\}_{i=1}^T$ in which the adversary can choose the pair (i, B_i) based on the output of our data structure on $(i-1, B_{i-1})$. Such an adversary model naturally captures many applications, in which the data structure is used in an *iterative process*, and the input to the data structure depends on the output from last iteration.

We will now describe how our data structure can be extended to handle an adaptive adversary. We store all initial factor matrices A_1, \dots, A_q . During an update, suppose we are given a pair (i, B_i) . Instead of using the sketching matrices correspond to the i -th leaf during initialization, we instead choose fresh sketching matrices along the path from the leaf to the root. Since the only randomness being used is the sketching matrices along the path, by using fresh randomness, our data structure works against adaptive adversary.

Such modification does not affect the update time too much: during each update, we need to apply the newly-generated base sketch C_i^{new} to both A_i and B , incurring a time of $\tilde{O}(\text{nnz}(A_i) + \text{nnz}(B))$. Along the path, the data structure repeatedly apply $T_{k,\ell}^{\text{new}}$ to its two children, which takes $\tilde{O}(md)$ time. The overall time is thus

$$\tilde{O}(\text{nnz}(A_i) + \text{nnz}(B) + md).$$

Thus, in later applications of our data structure, we present the runtime without this modification.

6 Faster Dynamic Tensor Product Algorithms with Dynamic Tree

In this section, we instantiate the dynamic tree data structure introduced in section 5 for three applications: dynamic tensor product regression, dynamic tensor spline regression, and dynamic tensor low rank approximation.

6.1 Dynamic Tensor Product Regression

We present an algorithm (Algorithm 2) for solving the dynamic Tensor product regression problem. Our algorithm uses the dynamic tree data structure to maintain a sketch of the design matrix and updates it efficiently. Additionally, we maintain a sketch of the label vector b via $\Pi^q b$. During the query phase, we output the solution using the sketch of the tensor product design matrix and the sketch of the label vector. We have the following guarantees, the proof of which we defer to Appendix C:

Theorem 6.1 (Informal version of Theorem C.1). *There is an algorithm (Algorithm 2) for dynamic Tensor product regression problem with the following procedures:*

- **INITIALIZE**($A_1 \in \mathbb{R}^{n_1 \times d_1}, A_2 \in \mathbb{R}^{n_2 \times d_2}, \dots, A_q \in \mathbb{R}^{n_q \times d_q}, m \in \mathbb{N}_+, C_{\text{base}}, T_{\text{base}}, b \in \mathbb{R}^{n_1 \dots n_q}$): Given matrices $A_1 \in \mathbb{R}^{n_1 \times d_1}, A_2 \in \mathbb{R}^{n_2 \times d_2}, \dots, A_q \in \mathbb{R}^{n_q \times d_q}$, a sketching dimension m , families of base sketches $C_{\text{base}}, T_{\text{base}}$ and a label vector $b \in \mathbb{R}^{n_1 \dots n_q}$, the data-structure pre-processes in time $\tilde{O}(\sum_{i=1}^q \text{nnz}(A_i) + qmd + m \cdot \text{nnz}(b))$.
- **UPDATE**($i \in [q], B \in \mathbb{R}^{n_i \times d_i}$): Given a matrix B and an index $i \in [q]$, the data structure updates the approximation to $A_1 \otimes \dots \otimes (A_i + B) \otimes \dots \otimes A_q$ in time $\tilde{O}(\text{nnz}(B) + md)$.
- **QUERY**: Query outputs an approximate solution \hat{x} to the Tensor product regression problem where $\|\bigotimes_{i=1}^q A_i \hat{x} - b\|_2 = (1 \pm \varepsilon) \|\bigotimes_{i=1}^q A_i x^* - b\|_2$ with probability at least $1 - \delta$ in time $O(md^{\omega-1} + d^\omega)$ where x^* is an optimal solution to the Tensor product regression problem i.e. $x^* = \arg \min_{x \in \mathbb{R}^d} \|\bigotimes_{i=1}^q A_i x - b\|_2$.

To realize the input sparsity time initialize and update time, we can use the combination of OSNAP and TENSORSRHT, which gives a sketching dimension

$$m = \varepsilon^{-2} q d^2 \log(1/\delta).$$

Hence, the update time is $\tilde{O}(\text{nnz}(B) + \varepsilon^{-2} q d^3 \log(1/\delta))$. Compared to the static algorithm [DJS⁺19], we note that their algorithm is suitable to be modified into dynamic setting, with a running time of $\tilde{O}(\text{nnz}(A_i) + \text{nnz}(B) + \varepsilon^{-2}(q+d)d/\delta)$, their algorithm cannot accommodate general dynamic setting due to the $1/\delta$ dependence in running time. Suppose the length dynamic sequence is at least d , then one requires the failure probability to be at most $\frac{1}{d}$ for union bound. In such scenario, the algorithm of [DJS⁺19] is strictly worse. [FFG22] improves the dependence on d , yielding a subquadratic update time in the form of $\tilde{O}((\text{nnz}(A_i) + \text{nnz}(B) + \varepsilon^{-2} q^2 d_i^\omega + \varepsilon^{-1} d^{2-\theta}) \log(1/\delta))$. While their algorithm is more efficient in solving the regression problem, their approach does not scale with the statistical dimension of the problem, and is geared only towards regression, whereas our data structure also handles low rank approximation.

6.2 Dynamic Tensor Spline Regression

Spline regression models are a well studied class of regression models [MC01]. In particular, Spline regression problems involving tensor products arise in the context of B-Splines and P-Splines [EM96]. For a very brief but relevant introduction to Spline regression, we refer the reader to [DSSW18]. In this section, we first define the Spline regression problem as it pertains to our sketching application. Algorithm 3, which shows how we can use our DYNAMICTENSORTREE data structure to solve a dynamic version of the Spline regression problem is provided in Appendix D.

Definition 6.2 (Spline Regression). In the Spline regression problem, given a design matrix $A \in \mathbb{R}^{m \times n}$, a target vector $x \in \mathbb{R}^n$, a regularization matrix $L \in \mathbb{R}^{p \times d}$, and a non-negative penalty coefficient $\lambda \in \mathbb{R}$, the goal is to optimize

$$\min_{x \in \mathbb{R}^n} \|Ax - b\|_2^2 + \lambda \cdot \|Lx\|_2^2 \quad (1)$$

Notice that the classic ridge regression problem is a special case of spline regression (when $L = I$). In the TENSOR SPLINE REGRESSION problem, we want to solve the Spline regression problem where the design matrix A can be decomposed as the tensor product of multiple smaller matrices i.e. $A = A_1 \otimes \dots \otimes A_q$. Our DYNAMIC TENSOR TREE data structure can be used to solve a dynamic version of TENSOR SPLINE REGRESSION. In our dynamic model, at each time step, the update is of the form $i \in [q], B \in \mathbb{R}^{n_i \times d_i}$, and the data structure has to update $A_i \leftarrow A_i + B$.

Before introducing our main result, we define an important metric to measure the rank of a Spline regression:

Definition 6.3 (Statistical dimension of Splines). For matrix $A \in \mathbb{R}^{n \times d}$ and $L \in \mathbb{R}^{p \times d}$ with $\text{rank}(L) = p$ and $\text{rank} \left(\begin{bmatrix} A \\ L \end{bmatrix} \right) = d$. Given the generalized singular value decomposition [GVL13] of (A, L) such that $A = U \begin{bmatrix} \Sigma & 0_{p \times (n-p)} \\ 0_{(n-p) \times p} & I_{d-p} \end{bmatrix} R Q^\top, L = V [\Omega \ 0_{p \times (n-p)}] R Q^\top$, where $\Sigma = \text{diag}(\sigma_1, \dots, \sigma_p), \Omega = \text{diag}(\mu_1, \dots, \mu_p) \in \mathbb{R}^{p \times p}$ are diagonal matrices. Finally, let $\gamma_i = \sigma_i / \mu_i$ for $i \in [p]$.

The statistical dimension of the Spline is defined as $\text{sd}_\lambda(A, L) = \sum_{i=1}^p 1/(1 + \lambda/\gamma_i^2) + d - p$.

The statistical dimension of the Spline can be much smaller than d , and we will show that the sketching dimension depends on the statistical dimension instead of d .

Let x^* denote an optimal solution of the Spline regression problem, i.e.

$$x^* = \arg \min_{x \in \mathbb{R}^n} \|Ax - b\|_2^2 + \lambda \cdot \|Lx\|_2^2$$

and let $\text{OPT} := \|Ax^* - b\|_2^2 + \lambda \cdot \|Lx^*\|_2^2$. We can compute x^* given A, b, L, λ by $x^* = (A^\top A + \lambda L^\top L)^{-1} A^\top b$.

Theorem 6.4 (Guarantees for DTSREGRESSION. Informal version of Theorem D.5). *There exists an algorithm (Algorithm 3) with the following procedures:*

- **INITIALIZE**($A_1 \in \mathbb{R}^{n_1 \times d_1}, \dots, A_q \in \mathbb{R}^{n_q \times d_q}$): Given matrices $A_1 \in \mathbb{R}^{n_1 \times d_1}, \dots, A_q \in \mathbb{R}^{n_q \times d_q}$, the data structure pre-processes in time

$$\tilde{O} \left(\sum_{i=1}^q \text{nnz}(A_i) + qmd + m \cdot \text{nnz}(b) \right).$$

- **UPDATE**($i \in [q], B \in \mathbb{R}^{n_i \times d_i}$): Given an index $i \in [q]$ and an update matrix $B \in \mathbb{R}^{n \times d}$, the data structure updates in time $\tilde{O}(\text{nnz}(B) + md)$.
- **QUERY**: Let A denote the tensor product $\bigotimes_{i=1}^q A_i$. Query outputs a solution $\tilde{x} \in \mathbb{R}^n$ with constant probability such the \tilde{x} is an ε approximate to the Tensor Spline Regression problem i.e.

$$\|A\tilde{x} - b\|_2^2 + \lambda \cdot \|L\tilde{x}\|_2^2 \leq (1 + \varepsilon) \cdot \text{OPT}$$

where $\text{OPT} = \min_{x \in \mathbb{R}^n} \|Ax - b\|_2^2 + \lambda \cdot \|Lx\|_2^2$. Query takes time $md^{(\omega-1)} + pd^{(\omega-1)} + d^\omega$.

We defer the proof of the above theorem to Appendix D.

The sketching dimension m here depends on the statistical dimension of the Spline, which is at most d . To realize the above runtime, one can pick OSNAP and TENSORSHRT, which gives $m = \varepsilon^{-1} q \text{sd}_\lambda^2(A, L) \log(1/\delta)$ and an update time $\tilde{O}(\text{nnz}(B) + \varepsilon^{-1} q \text{sd}_\lambda^2(A, L) d \log(1/\delta))$. The improved dependence on ε^{-1} comes from using approximate matrix product directly, instead of OSE.

6.3 Dynamic Tensor Low Rank Approximation

In this section, we consider a problem studied in [DJS⁺19]: given matrices A_1, \dots, A_q , the goal is to compute a low rank approximation for the tensor product matrix $A_1 \otimes \dots \otimes A_q \in \mathbb{R}^{n \times d}$, specifically, a rank- k approximation $B \in \mathbb{R}^{n \times d}$ such that

$$\|B - A\|_F \leq (1 + \varepsilon) \text{OPT}_k,$$

where $\text{OPT}_k = \min_{\text{rank-}k A'} \|A' - A\|_F$ and $A = \bigotimes_{i=1}^q A_i$. The [DJS⁺19] algorithm works as follows: they pick q independent COUNTSKETCH matrices [CCFC02] with $O(\varepsilon^{-2}qk^2)$ rows, then apply each sketch matrix S_i to A_i . After that, they form the tensor product $M = \bigotimes_{i=1}^q S_i A_i$ and run SVD on $M = U\Sigma V^\top$. Finally, they output $B = AU_k^\top U_k$ in factored form (as matrices A_1, \dots, A_q, U_k), where $U_k \in \mathbb{R}^{k \times d}$ denotes the top k right singular vectors. By doing so, they achieve an overall running time of

$$O\left(\sum_{i=1}^q \text{nnz}(A_i) + \varepsilon^{-2}qk^2d^{\omega-1}\right)$$

Two central issues around their algorithm are 1). exponential dependence on parameters ε^{-1}, q, k and 2). the algorithm is inherently static, so a natural way to make it dynamic is to apply S_i to the update B , reforming the tensor product of sketches and compute the SVD.

We address these two issues simultaneously by using our tree data structure. For the sketching dimension, we observe it is enough to design a sketch that is $(\varepsilon, \delta, k, d, n)$ -OSE and $(\varepsilon/k, \delta)$ -approximate matrix product, therefore, we can set sketching dimension $m = \varepsilon^{-2}qk^2 \log(1/\delta)$. As we will show later, this gives a much improved running time. By using the dynamic tree, we also provide a dynamic algorithm.

Theorem 6.5 (Guarantees for LowRankMaintenance. Informal version of Theorem E.3). *There exists an algorithm (Algorithm 4) that has the following procedures*

- **INITIALIZE**($A_1 \in \mathbb{R}^{n_1 \times d_1}, \dots, A_q \in \mathbb{R}^{n_q \times d_q}$): *Given matrices $A_1 \in \mathbb{R}^{n_1 \times d}, \dots, A_q \in \mathbb{R}^{n_q \times d_q}$, the data structure processes in time*

$$\tilde{O}\left(\sum_{i=1}^q \text{nnz}(A_i) + qmd\right).$$

- **UPDATE**($i \in [q], B \in \mathbb{R}^{n_i \times d_i}$): *Given an index $i \in [q]$ and an update matrix $B \in \mathbb{R}^{n_i \times d_i}$, the data structure updates the approximation for $A_1 \otimes \dots \otimes (A_i + B) \otimes \dots \otimes A_q$ in time*

$$\tilde{O}(\text{nnz}(B) + md).$$

- **QUERY**: *Let A denote the tensor product $\bigotimes_{i=1}^q A_i$. The data structure outputs a rank- k approximation C such that*

$$\|C - A\|_F \leq (1 + \varepsilon) \min_{\text{rank-}k A'} \|A' - A\|_F.$$

The time to output C is $\tilde{O}(md^{\omega-1})$.

By choosing $m = \varepsilon^{-2}qk^2 \log(1/\delta)$, we can provide good guarantees for low rank approximation. This gives

- Initialization time in $\tilde{O}(\sum_{i=1}^q \text{nnz}(A_i) + \varepsilon^{-2}qdk^2 \log(1/\delta))$;
- Update time in $\tilde{O}(\text{nnz}(B) + \varepsilon^{-2}qdk^2 \log(1/\delta))$;
- Query time in $\tilde{O}(\varepsilon^{-2}qd^{\omega-1}k^2 \log(1/\delta))$.

Without using fast matrix multiplication, our algorithm improves upon the prior state-of-the-art [DJS⁺19] on all parameters even in the static setting.

7 Conclusion & Future Directions

In this work, we initiate the study of the Dynamic Tensor Regression, Dynamic Tensor Spline Regression, and Dynamic Tensor Low-Rank Approximation problems, and design a tree data structure DYNAMICTENSORTREE which can be used to provide fast dynamic algorithms for these problems. The problems we study are very interesting and there are a lot of exciting future directions, some of which we discuss below. First, it is unclear whether the algorithms we proposed are optimal and so having lower bounds for the update times would be an important direction. Second, we have focused exclusively on regression problems with the ℓ_2 norm whereas solving Dynamic Tensor Regression and Dynamic Tensor Spline Regression with respect to the more robust ℓ_1 -norm as well as for more general p -norms are also very appealing.

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References

- [ACW17] Haim Avron, Kenneth L. Clarkson, and David P. Woodruff. Sharper bounds for regularized data fitting. In *Approximation, Randomization, and Combinatorial Optimization. Algorithms and Techniques - 20th International Workshop, APPROX 2017 and 21st International Workshop, RANDOM 2017*, August 2017.
- [AKK⁺20] Thomas D Ahle, Michael Kapralov, Jakob BT Knudsen, Rasmus Pagh, Ameya Velingker, David P Woodruff, and Amir Zandieh. Oblivious sketching of high-degree polynomial kernels. In *Proceedings of the Fourteenth Annual ACM-SIAM Symposium on Discrete Algorithms (SODA)*, pages 141–160, 2020.
- [ALS⁺18] Alexandr Andoni, Chengyu Lin, Ying Sheng, Peilin Zhong, and Ruiqi Zhong. Subspace embedding and linear regression with orlicz norm. In *International Conference on Machine Learning (ICML)*, pages 224–233. PMLR, 2018.
- [BBB⁺19] Frank Ban, Vijay Bhattiprolu, Karl Bringmann, Pavel Kolev, Euiwoong Lee, and David P. Woodruff. A ptas for ℓ_p -low rank approximation. In *Proceedings of the Thirtieth Annual ACM-SIAM Symposium on Discrete Algorithms*, SODA ’19, page 747–766, 2019.
- [BKM⁺22] Amos Beimel, Haim Kaplan, Yishay Mansour, Kobbi Nissim, Thatchaphol Saranurak, and Uri Stemmer. Dynamic algorithms against an adaptive adversary: Generic constructions and lower bounds. In *Proceedings of the 54th Annual ACM SIGACT Symposium on Theory of Computing*, pages 1671–1684, 2022.
- [BPSW21] Jan van den Brand, Binghui Peng, Zhao Song, and Omri Weinstein. Training (over-parametrized) neural networks in near-linear time. In *ITCS*, 2021.
- [Bub15] Sébastien Bubeck. Convex optimization: Algorithms and complexity. *Foundations and Trends® in Machine Learning*, 8(3-4):231–357, 2015.
- [BW14] Christos Boutsidis and David P Woodruff. Optimal cur matrix decompositions. In *Proceedings of the 46th Annual ACM Symposium on Theory of Computing (STOC)*, 2014.
- [BWZ16] Christos Boutsidis, David P Woodruff, and Peilin Zhong. Optimal principal component analysis in distributed and streaming models. In *Proceedings of the forty-eighth annual ACM symposium on Theory of Computing (STOC)*, pages 236–249, 2016.
- [CCFC02] Moses Charikar, Kevin Chen, and Martin Farach-Colton. Finding frequent items in data streams. In *International Colloquium on Automata, Languages, and Programming*, pages 693–703. Springer, 2002.
- [CEM⁺15] Michael B Cohen, Sam Elder, Cameron Musco, Christopher Musco, and Madalina Persu. Dimensionality reduction for k-means clustering and low rank approximation. In *Proceedings of the forty-seventh annual ACM symposium on Theory of computing*, pages 163–172, 2015.
- [CMP20] Michael B. Cohen, Cameron Musco, and Jakub Pachocki. Online row sampling. *Theory Comput.*, 16:Paper No. 15, 25, 2020.
- [CSTZ22] Sitan Chen, Zhao Song, Runzhou Tao, and Ruizhe Zhang. Symmetric sparse boolean matrix factorization and applications. In *13th Innovations in Theoretical Computer Science Conference (ITCS 2022)*. Schloss Dagstuhl-Leibniz-Zentrum für Informatik, 2022.

[CW13] Kenneth L. Clarkson and David P. Woodruff. Low rank approximation and regression in input sparsity time. In *Proceedings of the 45th Annual ACM Symposium on Theory of Computing (STOC)*, 2013.

[DJS⁺19] Huaian Diao, Rajesh Jayaram, Zhao Song, Wen Sun, and David Woodruff. Optimal sketching for kronecker product regression and low rank approximation. *Advances in neural information processing systems*, 32:4737–4748, 2019.

[DSSW18] Huaian Diao, Zhao Song, Wen Sun, and David Woodruff. Sketching for kronecker product regression and p-splines. In *International Conference on Artificial Intelligence and Statistics*, pages 1299–1308. PMLR, 2018.

[EM96] Paul HC Eilers and Brian D Marx. Flexible smoothing with b-splines and penalties. *Statistical science*, 11(2):89–121, 1996.

[EM06] Paul HC Eilers and Brian D Marx. Multidimensional density smoothing with p-splines. In *Proceedings of the 21st international workshop on statistical modelling*, 2006.

[EMZ21] Hossein Esfandiari, Vahab Mirrokni, and Peilin Zhong. Almost linear time density level set estimation via dbscan. In *AAAI*, 2021.

[FFG22] Matthew Fahrbach, Thomas Fu, and Mehrdad Ghadiri. Subquadratic kronecker regression with applications to tensor decomposition. *arXiv preprint arXiv:2209.04876*, 2022.

[GVL13] Gene H. Golub and Charles F. Van Loan. *Matrix computations*. Johns Hopkins Studies in the Mathematical Sciences. Johns Hopkins University Press, Baltimore, MD, 2013.

[HJS⁺21] Baihe Huang, Shunhua Jiang, Zhao Song, Runzhou Tao, and Ruizhe Zhang. Solving sdp faster: A robust ipm framework and efficient implementation, 2021.

[HLW17] Jarvis Haupt, Xingguo Li, and David P Woodruff. Near optimal sketching of low-rank tensor regression. In *ICML*, 2017.

[IVWW19] Pitor Indyk, Ali Vakilian, Tal Wagner, and David P Woodruff. Sample-optimal low-rank approximation of distance matrices. In *Conference on Learning Theory*, pages 1723–1751. PMLR, 2019.

[JKL⁺20] Haotian Jiang, Tarun Kathuria, Yin Tat Lee, Swati Padmanabhan, and Zhao Song. A faster interior point method for semidefinite programming. In *FOCS*, 2020.

[JLSW20] Haotian Jiang, Yin Tat Lee, Zhao Song, and Sam Chiu-wai Wong. An improved cutting plane method for convex optimization, convex-concave games and its applications. In *STOC*, 2020.

[JPW22] Shunhua Jiang, Binghui Peng, and Omri Weinstein. Dynamic least-squares regression. *arXiv preprint arXiv:2201.00228*, 2022.

[JSWZ21] Shunhua Jiang, Zhao Song, Omri Weinstein, and Hengjie Zhang. Faster dynamic matrix inverse for faster lps. In *STOC*. arXiv preprint arXiv:2004.07470, 2021.

[LCK⁺10] Jure Leskovec, Deepayan Chakrabarti, Jon Kleinberg, Christos Faloutsos, and Zoubin Ghahramani. Kronecker graphs: an approach to modeling networks. *Journal of Machine Learning Research*, 11(2), 2010.

[LDFU13] Yichao Lu, Paramveer Dhillon, Dean P Foster, and Lyle Ungar. Faster ridge regression via the subsampled randomized hadamard transform. In *Advances in neural information processing systems*, pages 369–377, 2013.

[LSZ19] Yin Tat Lee, Zhao Song, and Qiuyi Zhang. Solving empirical risk minimization in the current matrix multiplication time. In *COLT*, 2019.

[Mad13] Aleksander Madry. Navigating central path with electrical flows: From flows to matchings, and back. In *2013 IEEE 54th Annual Symposium on Foundations of Computer Science (FOCS)*, pages 253–262. IEEE, 2013.

[Mad16] Aleksander Madry. Computing maximum flow with augmenting electrical flows. In *2016 IEEE 57th Annual Symposium on Foundations of Computer Science (FOCS)*, pages 593–602. IEEE, 2016.

[MC01] L.C. Marsh and D.R. Cormier. *Spline Regression Models*. Number 137 in Quantitative Applications in the Social Sciences. SAGE Publications, 2001.

[MM13] Xiangrui Meng and Michael W Mahoney. Low-distortion subspace embeddings in input-sparsity time and applications to robust linear regression. In *Proceedings of the forty-fifth annual ACM symposium on Theory of computing (STOC)*, pages 91–100, 2013.

[MW21] Arvind V. Mahankali and David P. Woodruff. Optimal ℓ_1 column subset selection and a fast ptas for low rank approximation. In *Proceedings of the Thirty-Second Annual ACM-SIAM Symposium on Discrete Algorithms*, SODA ’21, page 560–578, 2021.

[NK06] James G Nagy and Misha Elena Kilmer. Kronecker product approximation for preconditioning in three-dimensional imaging applications. *IEEE Transactions on Image Processing*, 15(3):604–613, 2006.

[NN13] Jelani Nelson and Huy L Nguyễn. Osnap: Faster numerical linear algebra algorithms via sparser subspace embeddings. In *Proceedings of the 54th Annual IEEE Symposium on Foundations of Computer Science (FOCS)*, 2013.

[OY05] S. Oh, S. Kwon and J. Yun. A method for structured linear total least norm on blind deconvolution problem. *Applied Mathematics and Computing*, 19:151–164, 2005.

[Pag13] Rasmus Pagh. Compressed matrix multiplication. *ACM Trans. Comput. Theory*, 5(3), aug 2013.

[PP13] Ninh Pham and Rasmus Pagh. Fast and scalable polynomial kernels via explicit feature maps. In *Proceedings of the 19th ACM SIGKDD international conference on Knowledge discovery and data mining*, pages 239–247, 2013.

[PSA21] Aliakbar Panahi, Seyran Saeedi, and Tom Arodz. Shapeshifter: a parameter-efficient transformer using factorized reshaped matrices. *Advances in Neural Information Processing Systems*, 34, 2021.

[RGIY78] L. R. Rabiner, B. Go ld, and C. K. Yuen. Theory and application of digital signal processing. *IEEE Transactions on Systems, Man, and Cybernetics*, 8(2):146–146, 1978.

[RRS⁺22] Aravind Reddy, Ryan A Rossi, Zhao Song, Anup Rao, Tung Mai, Nedim Lipka, Gang Wu, Eunyee Koh, and Nesreen Ahmed. One-pass algorithms for map inference of nonsymmetric determinantal point processes. In *International Conference on Machine Learning*, pages 18463–18482. PMLR, 2022.

[RSW16] Ilya Razenshteyn, Zhao Song, and David P. Woodruff. Weighted low rank approximations with provable guarantees. In *Proceedings of the Forty-Eighth Annual ACM Symposium on Theory of Computing*, STOC ’16, page 250–263, 2016.

[Sar06] Tamás Sarlós. Improved approximation algorithms for large matrices via random projections. In *Proceedings of 47th Annual IEEE Symposium on Foundations of Computer Science (FOCS)*, 2006.

[SSX21] Anshumali Shrivastava, Zhao Song, and Zhaozhuo Xu. Sublinear least-squares value iteration via locality sensitive hashing. *arXiv preprint arXiv:2105.08285*, 2021.

[ST04] Daniel A Spielman and Shang-Hua Teng. Nearly-linear time algorithms for graph partitioning, graph sparsification, and solving linear systems. In *Proceedings of the thirty-sixth annual ACM symposium on Theory of computing*, pages 81–90, 2004.

[Sti81] Stephen M. Stigler. Gauss and the Invention of Least Squares. *The Annals of Statistics*, 9(3):465 – 474, 1981.

[SWYZ21] Zhao Song, David P. Woodruff, Zheng Yu, and Lichen Zhang. Fast sketching of polynomial kernels of polynomial degree. In *ICML*, 2021.

[SWZ17] Zhao Song, David P. Woodruff, and Peilin Zhong. Low rank approximation with entrywise ℓ_1 -norm error. In *Proceedings of the 49th Annual Symposium on the Theory of Computing (STOC)*, 2017.

[SWZ19a] Zhao Song, David Woodruff, and Peilin Zhong. Average case column subset selection for entrywise ℓ_1 -norm loss. *Advances in Neural Information Processing Systems (NeurIPS)*, 32:10111–10121, 2019.

[SWZ19b] Zhao Song, David Woodruff, and Peilin Zhong. Towards a zero-one law for column subset selection. *Advances in Neural Information Processing Systems*, 32:6123–6134, 2019.

[SWZ19c] Zhao Song, David P. Woodruff, and Peilin Zhong. Relative error tensor low rank approximation. In *SODA*. arXiv preprint arXiv:1704.08246, 2019.

[SY21] Zhao Song and Zheng Yu. Oblivious sketching-based central path method for solving linear programming problems. In *38th International Conference on Machine Learning (ICML)*, 2021.

[SYZ21] Zhao Song, Shuo Yang, and Ruizhe Zhang. Does preprocessing help training over-parameterized neural networks? *Advances in Neural Information Processing Systems (NeurIPS)*, 34, 2021.

[SZZ21] Zhao Song, Lichen Zhang, and Ruizhe Zhang. Training multi-layer over-parametrized neural network in subquadratic time. *arXiv preprint arXiv:2112.07628*, 2021.

[VL00] Charles F. Van Loan. The ubiquitous kronecker product. *Journal of computational and applied mathematics*, 123(1-2):85–100, 2000.

[WR12] Alfred North Whitehead and Bertrand Russell. *Principia mathematica*, volume 2. Cambridge University Press, 1912.

[WY22] David P. Woodruff and Taisuke Yasuda. Improved algorithms for low rank approximation from sparsity. In *Proceedings of the 2022 Annual ACM-SIAM Symposium on Discrete Algorithms (SODA)*, pages 2358–2403. SIAM, 2022.

[WZ16] David P. Woodruff and Peilin Zhong. Distributed low rank approximation of implicit functions of a matrix. In *2016 IEEE 32nd International Conference on Data Engineering (ICDE)*, pages 847–858. IEEE, 2016.

[WZD⁺20] Ruosong Wang, Peilin Zhong, Simon S. Du, Russ R. Salakhutdinov, and Lin F. Yang. Planning with general objective functions: Going beyond total rewards. In *Annual Conference on Neural Information Processing Systems (NeurIPS)*, 2020.

[XSS21] Zhaozhuo Xu, Zhao Song, and Anshumali Shrivastava. Breaking the linear iteration cost barrier for some well-known conditional gradient methods using maxip data-structures. *Advances in Neural Information Processing Systems (NeurIPS)*, 34, 2021.

[XZZ18] Chang Xiao, Peilin Zhong, and Changxi Zheng. Bourgan: generative networks with metric embeddings. In *Proceedings of the 32nd International Conference on Neural Information Processing Systems (NeurIPS)*, pages 2275–2286, 2018.

Checklist

1. For all authors...
 - (a) Do the main claims made in the abstract and introduction accurately reflect the paper’s contributions and scope? **[Yes]**
 - (b) Did you describe the limitations of your work? **[Yes]** Please refer to Section 7.

(c) Did you discuss any potential negative societal impacts of your work? [Yes] Our work is theoretical and it provides a dynamic algorithm for tensor product-typed problems. We analyze the runtime complexity of our algorithm, which relates to energy consumption in practice. We do not foresee potential negative societal impact of our work.

(d) Have you read the ethics review guidelines and ensured that your paper conforms to them? [Yes]

2. If you are including theoretical results...

- (a) Did you state the full set of assumptions of all theoretical results? [Yes] Please refer to Section 3.2.
- (b) Did you include complete proofs of all theoretical results? [Yes] Please refer to Section B, C, D, and E in the supplementary materials.

3. If you ran experiments...

- (a) Did you include the code, data, and instructions needed to reproduce the main experimental results (either in the supplemental material or as a URL)? [N/A]
- (b) Did you specify all the training details (e.g., data splits, hyperparameters, how they were chosen)? [N/A]
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- (a) Did you include the full text of instructions given to participants and screenshots, if applicable? [N/A]
- (b) Did you describe any potential participant risks, with links to Institutional Review Board (IRB) approvals, if applicable? [N/A]
- (c) Did you include the estimated hourly wage paid to participants and the total amount spent on participant compensation? [N/A]