# **CodeArt: Better Code Models by Attention Regularization When Symbols Are Lacking**

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Transformer based code models have impressive performance in many software engineering tasks. However, their effectiveness degrades when symbols are missing or not informative. The reason is that the model may not learn to pay attention to the right correlations/contexts without the help of symbols. We propose a new method to pre-train general code models when symbols are lacking. We observe that in such cases, programs degenerate to something written in a very primitive language. We hence propose to use program analysis to extract contexts a priori (instead of relying on symbols and masked language modeling as in vanilla models). We then leverage a novel attention masking method to only allow the model attending to these contexts, e.g., bi-directional program dependence transitive closures and token co-occurrences. In the meantime, the inherent self-attention mechanism is utilized to learn which of the allowed attentions are more important compared to others. To realize the idea, we enhance the vanilla tokenization and model architecture of a BERT model, construct and utilize attention masks, and introduce a new pre-training algorithm. We pre-train this BERT-like model from scratch, using a dataset of 26 million stripped binary functions with explicit program dependence information extracted by our tool. We apply the model in three downstream tasks: binary similarity, type inference, and malware family classification. Our pre-trained model can improve the SOTAs in these tasks from 53% to 64%, 49% to 60%, and 74% to 94%, respectively. It also substantially outperforms other general pre-training techniques of code understanding models.

# CCS Concepts: • Computing methodologies → Neural networks; Unsupervised learning.

Additional Key Words and Phrases: Code Language Models, Attention Regularization, Self-supervised Learning

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## 1 INTRODUCTION

Transformer models [2, 8, 19, 62] have substantially advanced the state-of-the-art of Natural Language Processing (NLP) applications. They are also the technique behind Large Language Models (LLMs) which have demonstrated unprecedented generalization and reasoning ability. These models feature the self-attention mechanism [73] that allows them to learn correlations/contexts distant in the input space and self-supervised pre-training methods, such as Masked Language Modeling (MLM) that can produce high quality embeddings by masking parts of inputs and forcing the models to predict the masked parts from their contexts. Although Transformer models were initially introduced to improve NLP applications, recent research has shown that they can be used in many software engineering tasks, such as automated program repair [11, 25, 59, 78], software testing [26], vulnerability detection [54, 69] and so on [27, 36, 45, 80], outperforming traditional methods. An underlying reason of their impressive performance is that software has rich natural language artifacts (also called *symbols* in this paper for terminology simplicity), such as comments, documents, variable and function names. These artifacts make programs "understandable" to Transformer models, just like to human developers. For example, the correlation between a statement that defines a variable and another statement that uses the variable can be naturally captured by the attention mechanism due to the common variable name, just like how developers infer dataflow by variable names.

However, when symbols are not available, such as in stripped binary executables, or not informative, such as in obfuscated software, programs become extremely difficult for models to understand, just like they are hard for developers. In particular, they are in a very primitive language in which tokens no longer have rich semantics. For example, in x86 executables, variables are denoted by registers and memory locations dereferenced through registers or constants; a same register may be allocated to multiple variables. As such, the definition of a register and the use of the register may not suggest dependence; neighboring tokens may belong to completely independent contexts/computations. Such context interleavings are difficult to unfold without the help of symbols. As shown by our experiments, code models based on vanilla Transformer architectures and using the vanilla MLM pre-training have degraded performance when symbols are lacking.

To mitigate the problem, researchers have proposed various methods. Trex [57] used microexecutions to acquire input and output operand values of an x86 instruction and then leveraged such values to train a Transformer model to precisely represent instruction level semantics. [Trans [75] used jump target prediction together with MLM to pre-train a Transformer model and used contrastive learning to force the model to learn embeddings that can distinguish similar and dissimilar binary functions. DiEmph [82] further improved JTrans by removing biases (e.g., undesirable code pattern distributions) introduced by compilers. GraphCodeBERT [35] aimed to enhance Transformer code model pre-training leveraging dataflow information. It expanded the raw input sequence with additional tokens denoting variables and introduced extra training losses to force the model to learn data-flow between these tokens. While these proposals demonstrated great improvements over vanilla models, most of them focused on single downstream tasks, instead of general pre-trained models. Some (e.g., GraphCodeBERT) required symbols. Note that there were also a body of works that treated programs as graphs (e.g., control flow graphs and dependence graphs) and leveraged Graph Neural Networks (GNNs) for software engineering tasks [23, 31, 41, 71, 84]. However, their performance also degrades when symbols are lacking, e.g., due to their need of labeled data in supervised learning and difficulties in capturing correlations that are multiple edges away in graphs [79]. More can be found in Section 2.

In this paper, we aim to develop a new technique for pre-trained general code models that targets programs without meaningful symbols, binary executables in particular. It does not fine-tune an

existing pre-trained model. Instead, it pre-trains a model from scratch using a new method that regularizes attention. It produces high quality embeddings that encode program dependences and disentangle interleaving contexts and hence enables better performance in downstream tasks. Its intuition is the following. Without meaningful symbols, a programming language degenerates to a very primitive one like an arithmetic language [60], in which the meaning of a variable/statement can only be derived from the direct and transitive computations that produce and use the value of the variable/statement. For example, in a function where all statements in the function body cohesively compute a final return value, the embedding of the return value should reflect the computation of the whole function; a function computing multiple orthogonal output values shall have embeddings reflecting such orthogonality in spite of interleavings of the sub-computations. The language is dissimilar to a natural language, and humans rarely speak in such a primitive fashion. As such, the vanilla MLM method can hardly help the model produce the right attention during pre-training without the help of symbols because MLM mainly leverages the correlations between individual tokens and their left and right contexts (shown in Section 2).

To address the challenge, we propose to use program analysis to derive possible dependences between instructions and then construct attention masks from such dependences. The masks enable self-attention between instructions that have dependences and preclude attention among those that are independent. This aligns well with the aforementioned primitive language. The pre-training then helps the model determine which dependences are more important than others and hence deserve more attention. Besides the dependence masks, additional masks are created to explicitly regularize self-attention to correlations other than program dependences, such as token co-occurrences. We also enhance the MLM pre-training by masking part of program dependences and introducing spurious dependences, and then forcing the model to correctly predict/classify such dependences. During inference, CodeArt takes the subject binary and generates the corresponding attention masks and feeds both the input tokens and the masks to produce output embeddings. Note that the dependence analysis and mask construction are deterministic and transparent to users. Our contributions are summarized in the following.

- We propose a new method to pre-train Transformer code models when symbols are lacking. Inspired by existing works that utilize program analysis to enhance Deep Learning models [9, 10, 12, 13, 15, 20–22, 32, 43, 51, 64, 65, 67, 68, 74], our method analyzes program dependences and use them to help self-attention. Different from many existing techniques that focus on improving performance of individual downstream tasks, our pre-trained models are general, serving a large number of applications, and use masks to regulate attention.
- We address a number of technical challenges, including enhancements of tokenization, model architecture, a new pre-training method that masks dependences, transforming transitive dependences to connectivity relations to avoid undesirable decay, and new training objectives. Built on top of the BinaryCorps dataset [75], we construct a large-scale training dataset with 26 million stripped binary functions containing explicit dependence information.
- We develop a prototype CodeArt (Better CODE models by Attention regulaRizaTion when symbols are lacking) and use it to pre-train a BERT-like general model from scratch. The pre-training converges in four days with an 8×A100 GPU cluster. To demonstrate the generalization of the pre-trained model, we use it in three downstream tasks: binary similarity analysis, malware classification, and binary type inference. We have improved the SOTAs of these tasks from 53% to 64% (Recall@1 with a pool size of 500), from 49% to 60% (LRAP), and from 74% to 94% (F-1 score averaged over different optimizations), respectively. We empirically compare with other general code pre-training approaches like GraphCodeBERT

```
1 struct ret *stats(int *data, int len) {
2  int mean = 0;
                                                              1 struct ret *stats(int *data, int len) {
2  int mean = 0;
                                                                                                                        1 struct ret *stats(int *data, int len) {
2  int mean = 0;
      int var = 0:
                                                                   int var = 0:
                                                                                                                              int var = 0:
      int *percentile :
                                                                                                                              int *percentile :
                                                                   int *percentile
                  malloc(sizeof(int) * 20):
                                                                               malloc(sizeof(int) * 20):
                                                                                                                                          malloc(sizeof(int) * 20);
 6
7
                                                              6
7
                                                                                                                         6
7
      for (i = 0; i < len; i++) {
  mean += data[i];</pre>
                                                                   for (i = 0; i < len; i++) {
                                                                                                                              for (i = 0; i < len; i++) {
                                                                                                                        8
                                                              8
                                                                     mean += data[i];
                                                                                                                                mean += data[i]:
10
     }
mean /= len;
for (i = 0; i < len; i++) {
    var += (data[i] - mean)
    * (data[i] - mean);</pre>
                                                            10
                                                                                                                       10
                                                                  for (i = 0; i < 20; i++) {
  percentile[i] = data[i * len / 20];</pre>
                                                                                                                              mean *= len;
                                                                                                                       12
                                                                                                                              for (i = 0; i < len; i++) {
  var += (data[i] - mean)</pre>
12
                                                            12
                                                             13
                                                                  mean /= len;
for (i = 0; i < len; i++) {
                                                                                                                                           * (data[i] - mean);
14
                                                            14
                                                                                                                       14
                                                                     var += (data[i] - mean)
* (data[i] - mean);
16
17
     var /= len;
for (i = 0;
                                                                                                                       16
17
                                                                                                                              var /= len;
for (i = 0; i < 20; i++) {
   percentile[i] = data[i * len / 20];
                                                            16
        or (i = 0; i < 20; i++) {
    percentile[i] = data[i * len / 20];
                                                             17
18
                                                            18
                                                                                                                       18
                                                                   var /= len:
      return (struct ret *){mean.
                                                                   return (struct ret *){mean.
                                                                                                                              return (struct ret *){mean.
20
                                                             20
                                                                                                                       20
21 22 }
                                 var, percentile};
                                                            21 22 }
                                                                                             var, percentile};
                                                                                                                                                        var, percentile};
                                                                                                                        22 }
      (a) A program calculating statistics
                                                                          (b) An equivalent version
                                                                                                                                        (c) A buggy version
 code for the mean value
                                                    code for the variance
                                                                                               code for the percentiles
                                                                                                                                      bug
```

Fig. 1. Code examples calculating statistics: (a) and (b) are equivalent but have different statement orders; (c) is buggy at line 11 with the wrong operation.

on binary code and show that our model is much more effective. We also conduct an ablation study to justify our design choices.  $^{\rm 1}$ 

## 2 MOTIVATION

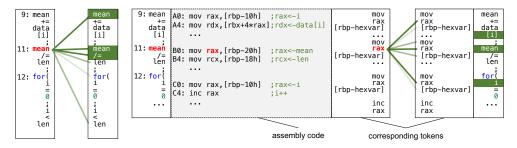
We use an example to discuss the limitations of existing techniques and illustrate our technique. For better readability, we present the example mostly in its source code form. Our technique works on stripped binaries without any symbol information.

**Example.** Fig. 1(a) shows a code snippet computing mean, variance, and percentile for an array of sorted data. The code blocks in different colors denote the three sub-computations. Fig. 1(b) shows a version equivalent to (a) with statements reordered. Fig. 1(c) is highly similar to (a) except that its line 11 has a buggy operation. We mix (b) and (c) with a set of 5 random functions to form a pool of candidate functions and use (a) to query the most similar function from the pool. We first use a similarity analysis built on the CodeT5 model using source code. The analysis easily identifies that (a) is similar to (b) and not to (c). However, when we use a binary similarity analysis JTrans that operates on stripped binaries (without symbols), JTrans mistakenly considers (a) is similar to (c) instead of (b). In contrast, a similarity analysis built on our model reports the correct similarity result even without symbols. In the following, we explain these different results.

Transformer Models. Recent research has shown that Transformer based code models, including Large Language Models (LLMs), deliver superior performance in many software engineering tasks [29, 48, 63, 77]. Transformer was initially introduced for NLP applications [19, 61, 62, 73], and the rich natural language artifacts in software make it suitable for these tasks. However, in tasks where symbols are precluded (e.g., binary analysis) or not informative (e.g., in obfuscated software), code becomes dissimilar to natural language products, causing the model to have sub-optimal attention and hence performance degradation.

Fig. 2(a) shows part of the attention map of lines 9–12 in the code in Fig. 1(a), by the CodeT5 model that works on source code. Observe that with the help of variable name mean, the model correctly correlates statements with program dependences, e.g., the strong attention between *mean* at line 11 and *mean* at line 9. In contrast, Fig. 2(b) shows the attention map of the corresponding binary code by the JTrans model [75]. Here, rax@A0 denotes variable i, rax@B0 denotes mean

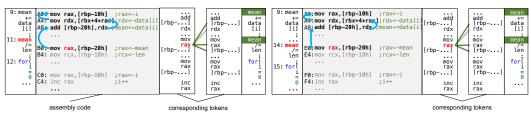
<sup>&</sup>lt;sup>1</sup>Code and data are available at https://github.com/ziansu/codeart. Reproduction package at [1].



(a) Attention map on source code

(b) Attention map on binary code

Fig. 2. Attention maps of variable mean at line 11 of Fig. 1(a) (in red), by a source code model (a) and by a binary code model (b). In (a), we show attention between source code tokens, corresponding to lines 9–11 in Fig. 1(a). In (b), we show attention between binary code tokens. For readability, we also include the corresponding source code and part of the assembly. The variable *mean* is stored in the register rax at line B0 (in red). A line with a darker color denotes a larger attention value. In an instruction, the first operand is destination and the second the source, and comment "rax<-i" means that register rax stores variable i.



(a) Attention map of CODEART

(b) Attention map of CODEART on the equivalent version

Fig. 3. Attention maps by CodeArt for variable mean in the code snippets in Fig. 1 (a) and (b). The instructions in bold are in the dependence context of the variable and the blue arrows denote dependences.

and rax@C0 denotes i. Observe that the model has undesirable (strong) attention among the rax's. Moreover, as the binaries for the programs in Fig. 1(a) and (c) are syntactically very similar. The JTrans model produced highly similar (but problematic) attention maps for the two, causing its misclassification of the two as similar functions. In Section 4.1, our results show that CODEART outperforms the JTrans model on the binary similarity task by over 30% in zero-shot settings.

GNN Models. GNNs are also widely used in software engineering tasks [4, 7, 17, 37, 44]. The basic idea is that programs have explicit graph structures, such as control flow graph (CFG), abstract syntax tree (AST), and program dependence graph (PDG), which can be leveraged by GNNs. In GNNs, the embedding of a node (e.g., a basic block in CFG) can be derived from the embeddings of neighboring nodes and the content of the node itself. They are hence a plausible solution when symbols are missing. However, as pointed out in [75, 79], standard GNN based code models struggle to capture long-range dependences [86]. Although scaling GNNs in depth and width may mitigate the problem, it may in the meantime cause optimization instabilities and representation oversmoothing [6, 14, 42]. Our experiments in Section 4.1 show that CodeART outperforms GNN based code models on the binary similarity task by over 20%.

**Our Method.** Our key observation is that when symbols are lost or not informative, code becomes dissimilar to natural language products, and exhibits its own characteristics. First, *the semantics of a statement is the aggregation of all the statements that directly or transitively contribute to it and those that it contributes to.* For example, the meaning of variable mean at line 11 in Fig. 1(a) is determined

by lines 8-11 and lines 13-14, which form a context for mean. In an extreme case where all statements in a function are devoted to computing a final return value, the embedding of the value shall reflect the meaning of the whole function. This is quite different from how humans use natural languages. Second, absolute code positions shall be de-emphasized when meaningful symbol information is not present to help disentangle interleaving contexts. Program code tends to have interleaving contexts, which may not be a problem when symbols are present to help disentanglement. For example in Fig. 1(b), the context of mean interleaves with the context for computing percentile, namely, lines 11-13. Although it may not be a problem when variable names are present to disentangle the contexts, it becomes a lot more challenging when symbols are lost.

Based on the observations, we propose a new technique that is based on Transformer, and enhanced with a new pre-training method to explicitly regulate self-attention using masks. In particular, it uses program analysis to determine possible dependences between instructions, including both data and control dependences. The masks ensure that a token can only pay attention to its dependence context, which includes the instructions that it directly or transitively depends on and those that are directly or transitively dependent on the token. As we will show in Section 3.4, additional masks are also derived to regulate attention in other types of contexts such as instruction local contexts that only allow an token to pay attention to other tokens within the same instruction and global contexts that model token co-occurrences (within and across instructions). The pretraining further helps the model learn which of these allowed attentions ought to be strong. As shown in Fig. 3(a), the attention map by CodeArt for the binary version of Fig. 1(a) closely resembles that when the source code is used (Fig. 2(a)). Moreover, the attention map by CODEART for the binary version of Fig. 1(b) (i.e., the reordered but equivalent version) is also highly similar (see Fig. 3(b)), despite their syntactic differences. In fact, CODEART produces similar attention maps for all three code snippets in Fig. 1. However, the different operations at line 11 of Fig. 1 (a) and (c) cause different final embeddings, allowing a binary similarity analysis built on our model to correctly recognize the similar functions.

Comparison with GraphCodeBERT. GraphCodeBERT [35] is a source code representation model based on BERT architecture that leverages data-flow information in pre-training. By augmenting (appending) the [comment, source code] input with additional variable tokens, and forcing the variable tokens to align with their corresponding tokens in the source code part of the input, and to have the intended data-flow, GraphCodeBERT aims to have better embeddings. Although GraphCodeBERT has demonstrated advantages over the vanilla CodeBERT [29], porting it to handling binaries without symbols is challenging. In particular, its variable token alignment largely relies on the variable name equivalence. At the binary level, the multiple occurrences of a variable may have completely different register/memory tokens, rendering the alignment much more difficult. Without proper alignment, the data-flow training is infeasible. This is supported by our evaluation results of a binary version of GraphCodeBERT in Section 4.5.

#### 3 DESIGN

**Overview.** Fig. 4 shows the pipeline of CodeArt encoder. Given a binary executable, CodeArt first disassembles the code (step (a)). It then tokenizes the disassembly (step (b)) and performs dependence analysis to derive both data and control dependences (step (c)). In (d), the dependence transitive closures are computed for individual instructions, by traversing dependence edges in both forward and backward directions. The closures are further transformed to connectivity graphs in which an edge is introduced between two nodes if one is reachable from the other. In *Mask Builder* (e), the connectivity graph and the instruction tokens are leveraged to construct an attention mask and a relative distance matrix that measures the dependence distance between two connected

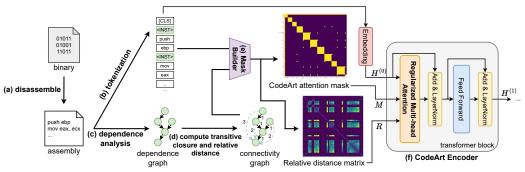


Fig. 4. CodeArt encoder pipeline

nodes. They are further fed to a BERT-like Transformer model (step (f)) to produce the output embedding. Our model is different from the vanilla BERT as it leverages the mask and the distance matrix to regulate attention. The pre-training of CodeArt model entails masking input tokens and masking/perturbing the connectivity graph. In the following subsections, we introduce the individual components.

#### 3.1 Tokenization

CODEART directly works on binary executables with symbols stripped. Given an executable, CODEART first disassembles it using IDA-Pro  $^2$ . The disassembled code is then tokenized. The tokenizer breaks each (x86-64) instruction to multiple tokens. Specifically, opcode and operands are denoted by separate tokens. A compound operand may be further broken down to multiple tokens. For example, a memory read instruction mov rdx, [rbx+4\*rax] that reads a value from an address denoted by rbx+4\*rax to rdx is tokenized to a sequence 'mov', 'rdx', ',', '[', 'rbx', '+', '4', '\*', 'rax', ']'. In addition, we use a special delimiter token <INST> to denote the beginning of an instruction. This is critical to the attention regularization which we will discuss in Section 3.4. Formally, a tokenized sequence can be described as  $A = \{i_1, a_{1,1}, \cdots, a_{1,n_1}, i_2, a_{2,1}, \cdots, a_{2,n_2}, \cdots\}$ , where  $i_t$  denotes the delimiter for the t-th instruction and  $a_{t,n}$  the n-th token of the t-th instruction. Similar to other BERT-based models [19, 29, 47], we prepend a [CLS] token to the token sequence. The final input sequence is hence X = [[CLS], A].

# 3.2 Dependence Analysis

Given binary executables, CodeArt first uses program analysis to determine both control dependences and data dependences between instructions, and then uses such information to regulate attention during training and inference. In particular, CodeArt employs IDA Pro [38] to construct the control flow graph (CFG) for each function. Using these CFGs, we resort to a conventional algorithm [30] to determine control dependences. We additionally tailor a source-code data-flow analysis [5] to facilitate the analysis of data dependences in binaries. This data-flow analysis begins by gathering a collection of variables accessed by each statement (or, in the context of binary analysis, an instruction) and subsequently determines the def-use relationship among these variables. Precisely identifying variables accessed by a binary instruction is notably challenging [85], given that all variables are compiled into plain registers and memory locations without any symbol information. To this end, we adopt an approach that overestimates the memory regions an expression

<sup>&</sup>lt;sup>2</sup>https://hex-rays.com/ida-pro/

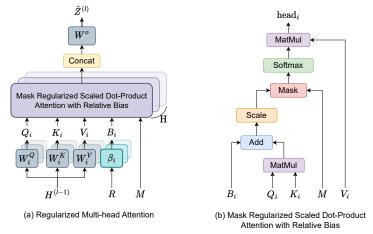


Fig. 5. Regularized Multi-head Attention (with (b) illustrating a zoom-in view of part of (a))

can potentially reference (and hence, the variables that an instruction with the expression can potentially access), shown as follows.

- Expressions denoting stack memory addresses with statically decidable offsets (e.g., [rsp + 0x20]) are interpreted to merely reference the corresponding stack locations.
- Expressions denoting stack memory addresses with statically undecidable offsets (e.g., [rsp + rax \* 8 + 0x30] with rax holding an input parameter) are presumed to reference the entire stack frame of the corresponding function.
- Expressions pointing to addresses not statically associated with stack pointers (e.g., [rbx + rax] denoting some heap access) are conservatively considered to reference the entire memory space of the given binary.

For instance, given the assembly code: "(a) mov rax, rbx; (b) mov [rsp + 0x10], rax; (c) mov rcx, [rsp + 0x10]; (d) mov rdx, [rcx];", our analysis can derive three distinct data dependences from it, including (a)  $\leftarrow$  (b) through the register rax, (b)  $\leftarrow$  (c) involving the stack memory address [rsp + 0x10], and (b)  $\leftarrow$  (d) related to the non-stack memory address [rcx]. Note that [rcx] is assumed to possibly access the entire memory space, which encompasses [rsp + 0x10]. It is worth noting that our dependence analysis is largely standard, and we include it for completeness. We do not claim contributions on the analysis.

## 3.3 Model Architecture

Fig. 4(f) shows our model architecture, which is a multi-layer Transformer encoder [73], with the component *Regularized Multi-head Attention* (RMA) containing the main differences from a standard BERT architecture [19, 47]. As shown in the figure, the encoder takes a sequence X of N tokens and acquires a sequence of input vectors  $\mathbf{H}^{(0)} = \left[\mathbf{h}_1^{(0)}, \cdots, \mathbf{h}_N^{(0)}\right] \in \mathbb{R}^{N \times d_h}$  by summing the token embeddings and the corresponding trainable absolute position embeddings (the sum operation is not shown in the figure for brevity). Here,  $d_h$  is the dimension of hidden states. The output embedding is then obtained by applying L layers of transformer blocks, each regulated by the Codeart attention mask  $\mathbf{M} \in \mathbb{R}^{N \times N}$  and the relative distance matrix  $\mathbf{R} \in \mathbb{N}^{N \times N}$ , as exampled by the block in Fig. 4(f). Formally, the hidden states after layer l are denoted as  $\mathbf{H}^{(l)} = \text{transformer\_block}\left(\mathbf{H}^{(l-1)}, \mathbf{M}, \mathbf{R}\right)$ ,  $l \in \{1...L\}$ . A transformer block (Fig. 4 (f)) is formally

defined as follows.

$$\hat{Z}^{(l)} = \text{RMA}(H^{(l-1)}, M, R), \quad Z^{(l)} = \text{LN}(\hat{Z}^{(l)} + H^{(l-1)}), \quad H^{(l)} = \text{LN}(\text{FFN}(Z^{(l)}) + Z^{(l)})$$
(1)

Here RMA is an enhanced multi-head self-attention module [73], FFN is a two-layer feed-forward network, and LN is layer normalization. Their details are shown in Fig. 5. In particular, for each layer l, the output  $\hat{Z}^{(l)}$  of RMA is computed as follows. We omit the layer annotation for simplicity.

$$Q_i = HW_i^Q, \quad K_i = HW_i^K, \quad V_i = HW_i^V$$
 (2)

$$head_{i} = softmax \left( \frac{Q_{i}K_{i}^{T} + B_{i}}{\sqrt{d_{k}}} + M \right) V_{i}$$
(3)

$$\hat{Z} = [\text{head}_1; \cdots; \text{head}_H] W^o$$
(4)

Here the (l-1)-th layer's output  $H^{(l-1)} \in \mathbb{R}^{N \times d_h}$  is converted to queries  $Q_i = [q_{i,1}, \cdots, q_{i,N}]$ , keys  $K_i = [k_{i,1}, \cdots, k_{i,N}]$  and values  $V_i = [v_{i,1}, \cdots, v_{i,N}]$  for head i, by linear projections with weights  $W_i^Q$ ,  $W_i^K$ , and  $W_i^V \in \mathbb{R}^{d_h \times d_k}$ . H is the number of heads,  $d_k$  the head dimension, and  $W^o$  the weight for the linear projection after concatenating multiple head outputs. M denotes a main difference between our architecture and vanilla Transformer models. It is the *multi-head attention mask*, where  $M_{uv}$  is set to either 0 or  $-\infty$  to determine whether token u is allowed to attend to token v or not, respectively. Intuitively, by setting parts of the mask to 0, we only allow the model to learn self-attention for the corresponding token pairs and preclude spurious attention. We will further explain how to construct the mask later in the section.

As shown in Equation 3 and Fig. 5(b), another main difference between RMA and standard attention is that RMA incorporates relative positional embedding  $B_i$  to explicitly encode position relationship between <INST> delimiter tokens (not between other tokens internal to an instruction). Intuitively, it denotes the dependence distances between instructions as their absolute positions/distances could be misleading when symbols are missing. The relative positional embedding is integrated in the form of bias in Equation 3. It is computed as follows.

$$B_{i,uv} = \beta_i \left( \min(R_{uv}, r_{\text{max}}) \right) \tag{5}$$

Here R is the relative distance matrix pre-computed by our analysis (details in the next section), and  $\beta_i$  is the embedding function that maps a distance value to a trainable parameter for head i. The relative position relationship is learned through these parameters during training. Variable  $r_{\rm max}$  is the max relative distance. Since larger relative distances are less frequent in practice, the model can hardly learn the relative bias for large relative distances. The max distance ensures that when the distance is too large, the bias becomes indistinguishable to that of at distance  $r_{\rm max}$ . As such, the model understands that the distance is large and potentially out of distribution.

## 3.4 Attention Regularization by Masking

In this section, we explain how we construct the attention mask *M*. As discussed earlier, without symbols, model training tends to determine contexts by names of primitive operands (e.g., register names) and absolute positions, which could be misleading. We leverage masking to direct the self-attention to the right places such that correct contexts can be extracted. Fig. 6 provides a conceptual illustration. In (a), a small program consisting of six instructions is shown in the first row (and in their source code form for readability), and each is broken down to multiple tokens. For example in the second row, the green blocks with numbers show the <INST> tokens, each of which is followed by the (grey) tokens of the corresponding instruction. The entire sequence is preceded by a [CLS] token. Assume we are interested in the embedding of the second token of instruction 5 (the 'edx' token in red). First of all, we want the model to pay attention to the co-occurrences

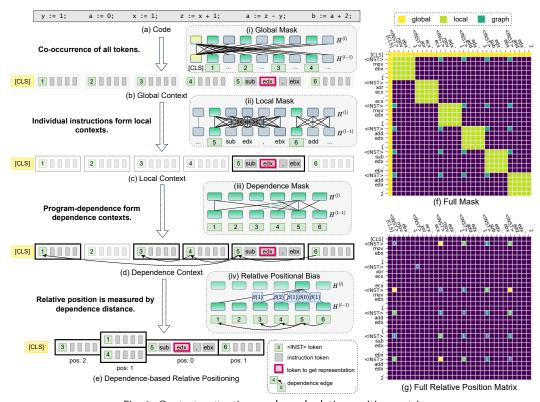


Fig. 6. Contexts, attention masks and relative position matrix

of all the tokens, without focusing on any specific ones. We call it the *global context* (Fig. 6(b)). Second, the model should pay attention to all the tokens in the same instruction as the red token when encoding it. We call it the *local context* of the red token (Fig. 6(c)). Additionally, the model should pay attention to the sub-computation which the red token is in, namely, instructions  $\boxed{1}$ ,  $\boxed{3}$ ,  $\boxed{4}$ , and  $\boxed{6}$  due to program dependences. We call it the *dependence context* (Fig. 6(d)). Finally, the neighboring tokens of the red token should be measured by their dependence distances (i.e., how many dependence edges away) instead of their absolute positions. We call it the *dependence based relative positioning* (Fig. 6(e)). Intuitively, it rearranges the instructions by their dependence distances. For example, instruction 5 is closer to instructions 1, 4, 6 than instruction 3.

In standard Transformer models, the self-attention (without masking) allows a query  $q_u \in \mathbb{R}^{1 \times d_k}$  (an element of Q in Equation 2, corresponding to a token) to attend to individual keys  $k_v \in \mathbb{R}^{1 \times d_k}$  ( $v \in \{1..N\}$ ) (an element of K in Equation 2) by computing a scaled dot-product  $q_u \cdot k_v^T / \sqrt{d_k}$  as the attention score, which is further normalized by a softmax function to get attention weights  $o_{uv}$  for aggregating values  $v_v \in \mathbb{R}^{1 \times d_k}$  ( $v \in \{1..N\}$ ) by  $v_v = \sum_{v=1}^N o_{uv} v_v$  (similar to Equation 3). The aforementioned multi-step context extraction is achieved by attention regularization using masking. As shown in Equation 3, an attention mask  $v_v \in \{1..N\}$  is added to the dot-product before the softmax, which contains a 0 value (to enable attention) or a  $v_v = v_v = v_$ 

**Global Attention Mask**  $M^{Gl}$ . As shown in Fig. 6(i) in between the first and the second rows on the left, the yellow [CLS] token is used to facilitate learning the global context. Observe that it attends to all the tokens, and vice versa. This allows the model to learn *co-occurrences*. For example, the tokens in two separate instructions could indirectly attend to each other through the [CLS] token. The yellow cells in Fig. 6(f) show the 0 values in  $M^{Gl}$  of our small program in the form of heat-map, with the legends the tokens and the black cells denoting  $-\infty$ . Formally,

$$M_{i,j}^{GI} = \begin{cases} 0, & X[i] = [CLS] \lor X[j] = [CLS], \\ -\infty, & \text{otherwise.} \end{cases}$$
 (6)

**Local Attention Mask**  $M^{\text{Lo}}$ .  $M^{\text{Lo}}$  ensures a token inside an instruction has attention to all tokens in the same instruction. Each instruction is broken down to an <INST> delimiter followed by a sequence of instruction tokens denoting opcode and operands (e.g., the 5th instruction in Fig. 6(b)). As shown in Fig. 6(ii), each token in the 5th instruction attends to all the tokens within the instruction. Moreover, any cross-instruction attention is forbidden in this mask. The green cells in Fig. 6(f) show the 0 values in  $M^{\text{Lo}}$  of our small program. Formally,

$$M_{i,j}^{\text{Lo}} = \begin{cases} 0, & \text{instruction\_of}(X[i]) = \text{instruction\_of}(X[j]), \\ -\infty, & \text{otherwise.} \end{cases}$$
 (7)

**Dependence Attention Mask**  $M^{\text{Dep}}$ . A straightforward method to construct the dependence mask is to directly reflect the directed program dependency graph  $G_{\text{dep}} = (V, E_{\text{dep}})$  obtained by our dependency analysis through the <INST> tokens, namely, if an instruction A is dependent on another instruction B (i.e., there is a dependence edge  $A \rightarrow B$ , we allow A's <INST> token to pay attention to B's). However, such a simple design suffers two problems: (1) one layer can only pass information from 1-hop neighbors such that signals from multi-hop neighbors become undesirably weak. (2) the dependence attention is uni-directional whereas bidirectional attention is proved to be better in Transformers pre-training [19].

Thus, we use the Floyd-Warshall algorithm [18] to transform the dependence graph to a connectivity graph  $G_{\text{con}} = (V, E_{\text{con}}, \mathcal{D})$ , where an undirected edge between two nodes denotes if one is reachable from the other in the original graph, and  $\mathcal{D}: V \times V \mapsto \mathbb{N}$  is the distance function which maps a connectivity edge to its path length in the original graph. Then, we construct the mask to enable bidirectional attention if a connectivity edge exists. Fig. 6(iii) shows the dependence mask. Observe that since instruction 5 is dependent on instructions 1 and 4, transitively dependent on 3, and 6 is dependent on 5, symmetric attention is allowed between 5 and 1, 3, 4, 5, and 6. The blue cells in Fig. 6(iii) show the 0 values in  $M^{\text{Dep}}$  of our small example. Note that although we only allow direct attention between <INST> tokens, tokens internal to instructions attend to <INST> and vice versa such that individual internal tokens can attend to other internal tokens in the dependence context through an additional layer of information propagation. Formally,

$$M_{i,j}^{\mathrm{Dep}} = \begin{cases} 0, & X[i] = <\mathrm{INST}> \land X[j] = <\mathrm{INST}> \\ & \land \Big(\mathrm{instruction\_of}(X[i]) \text{ trans-deps } \mathrm{instruction\_of}(X[j]) \\ & \lor \mathrm{instruction\_of}(X[j]) \text{ trans-deps } \mathrm{instruction\_of}(X[i]) \Big), \\ -\infty, & \mathrm{otherwise.} \end{cases}$$
 (8)

The final mask M is the union of above three masks, as in Fig. 6(f). The distance function D of the connectivity graph is further used to construct the relative distance R in Equation 5. Fig. 6(iv)

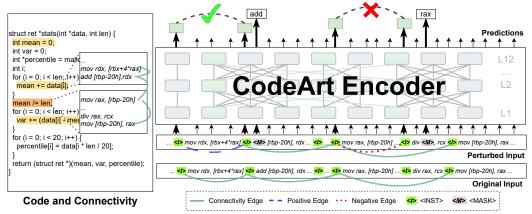


Fig. 7. Pre-training in CODEART

shows the relative positional biases for the 5th instruction (i.e., the annotations on attention edges). Fig. 6(g) shows the constructed R with different colors denoting different distances.

# 3.5 Model Pre-training

The pre-training of CodeArt consists of two parts: the traditional *Masked Language Modeling* (MLM) that masks parts of input tokens and *Masked Dependence Modeling* (MDM) that masks existing dependence edges and introduces spurious dependence edges. The essence of MDM is that well-trained contextual embeddings shall be able to predict existing dependence edges (called *positive edges*) and preclude spurious edges (called *negative edges*).

**Masked Language Modeling.** We perturb the input sequence X following the MLM in RoBERTa [47]. In particular, we sample 15% of the tokens in X. We then replace them with a [MASK] 80% of time, with a random token 10% of time, and have them unchanged in the remaining 10%. During pretraining, the model is supposed to predict the masked tokens. Let  $\tilde{X}$  be the perturbed sequence. The MLM training loss is formally defined as follows.

$$\mathcal{L}_{\text{MLM}} = \sum_{x \in X^{\text{masked}}} -\log P\left(x \middle| \tilde{X}, \tilde{\boldsymbol{M}}, \tilde{\boldsymbol{R}}\right)$$
(9)

where  $\tilde{M}$  and  $\tilde{R}$  are the perturbed attention mask and relative position matrix respectively, which we will discuss next.

**Masked Dependence Modeling.** In each pre-training step, we randomly sample 40% of the nodes in the connectivity graph  $G_{\text{con}} = (V, E_{\text{con}})$ . The sampled set is denoted as  $V_s \subset V$ . Let  $E_{\text{con}}(V_s)$  be the set of edges in the connectivity graph that have at least one node in  $V_s$ . It denotes the sampled positive edges. We then sample an equal number of pair-wise relations in  $V_s \times V \cup V \times V_s - E_{\text{con}}$ , which denote the negative edges. These two edge sets form a balance set  $E_b$  of positive and negative samples. We then force the model to learn to correctly classify the positive and negative edges during pre-training. Let  $\tilde{M}$  and  $\tilde{R}$  be the perturbed attention mask and relative matrix after the sampled positive edges removed from  $G_{\text{con}}$  and the negative edges added, and  $h_u$ ,  $h_v$  the hidden states of u and v. The training loss is formally defined as follows.

$$\mathcal{L}_{\text{EdgePred}} = \sum_{e_{uv} \in E_b} -\mathbb{I}(e_{uv} \in E_{\text{con}}) \log p_{e_{uv}} - \mathbb{I}(e_{uv} \notin E_{\text{con}}) \log(1 - p_{e_{uv}})$$
(10)

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where

$$p_{e_{uv}} = P\left(e_{uv}|\tilde{X}, \tilde{M}, \tilde{R}\right) = \text{sigmoid}(\boldsymbol{h}_u \cdot \boldsymbol{h}_v^T)$$
 (11)

Note that even though MDM is similar to GraphCodeBERT's edge prediction and node alignment losses [35] as they are all in the form of binary cross-entropy, there is a core difference that MDM predicts edges between <INST> tokens instead of variable tokens from original code as in GraphCodeBERT. This avoids the risk that edge prediction loss may affect the representation of variable tokens learned by the MLM loss in an undesirable way.

The final loss is the sum of  $\mathcal{L}_{MLM}$  and  $\mathcal{L}_{EdgePred}$ .

*Example.* Fig. 7 presents how we use the motivation example in pre-training. The left shows the source code with the sub-computation related to mean highlighted. The x86 instructions corresponding to two of these statements are also shown. On the right, we show the model and the two types of masked modeling. The original input sequence is shown at the bottom with the edges denoting connectivity (derived from dependences). The row above shows the perturbed sequence and the perturbed edges. In particular, a positive edge (in blue) is selected and a negative edge (in red) is introduced. On top of the encoder, the output embeddings could be used to correctly classify the positive/negative edges.

## 4 EXPERIMENT

The implementation and pre-training details of CodeArt can be found in Section A of an extended version of this paper [70]. We evaluate CodeArt on three downstream tasks to demonstrate the effectiveness of the pre-training, including binary similarity, malware family classification, and type inference for binary executables. We aim to answer the following research questions.

- RQ1: How does CodeArt perform on binary similarity analysis?
- RQ2: How does CodeArt perform on malware family classification?
- RQ3: How does CodeArt perform on binary type inference?
- RQ4: Model analysis and ablation study of CODEART.
- RQ5: How effective is CodeArt compared to GraphCodeBERT-like pre-training?

# 4.1 RQ1: Performance on Binary Similarity Analysis

**Setup.** Given an input binary function, a binary similarity analysis queries it among a pool of candidate functions, and tries to identify the function that is compiled from the same source code as the query function [75]. It plays a critical role in many security related tasks, such as one day vulnerability detection [24, 76], automatic software patching [66], and software plagiarism detection [49]. Machine-learning based binary similarity tools typically encode the query function and all the candidate functions to their embeddings. After that, the cosine similarity between the embeddings of the query function and each candidate function is computed. The candidate functions are then ranked by the similarity values. The function with the largest value is considered similar to the query function.

**Dataset.** We use the BinaryCorp-3M dataset [75] to finetune CodeArt on the binary similarity task. It is the same training dataset used by SOTA transformer-based models [75, 82, 87]. We use 7 real-world projects (i.e., Curl, Coreutils, Binutils, ImageMagick, SQLite, OpenSSL and Putty) as the test dataset. They are commonly used by the previous binary similarity works [53, 57, 76, 83].

**Baseline.** We compare the performance of CODEART with the SOTA GNN-based model [53], *Graph Matching Network* (GMN)-based model [44], and two SOTA Transformer-based models Jtrans [75] and DiEmph [82]. GMN is a GNN-based model that takes as input a pair of programs and outputs a similarity value. It is worth noting that GMN does not generate embeddings for individual functions,

and can only be used for pair-wise similarity analysis. Note that although there are recent proposals that can achieve very good performance in binary similarity using advanced dynamic analysis such as [76, 83], these techniques require executing the functions (using seed inputs). They are hence not directly comparable.

Training Details. For both CodeArt and JTrans, we finetune their pre-trained models on the BinaryCorp-3M dataset [75]. Specifically, each training data sample is a triplet consisting of two binary functions compiled from a same source code function, and another binary function compiled from a different source code function. The training loss is the triplet loss enforcing a large cosine similarity between similar function pairs and a small similarity between dissimilar ones. For DiEmph, the GNN-based model, and the GMN-based model, we follow the scripts provided by the authors [53, 82] to train the models. For all models, we use the Coreutils project as the validation dataset to select best checkpoints, and report the performance on the remaining 6 projects.

**Metrics.** Following previous work [75], we use recall@1 as the metric to evaluate a binary similarity model. Specifically, suppose that we make N queries, recall@1 is computed as N divided by the number of queries that the function compiled from the same source code is correctly returned as the most similar function. We also adapt the setup of previous work to evaluate a binary similarity model with different sizes of candidate function pools. Intuitively, a larger pool size means a more challenging setup for a model.

Results. The results are shown in Fig. 8. We can see that CodeArt outperforms the GNN-based model and the GMN-based model by a large margin in all setups. The improvement is largely due to Transformer models' better capability of capturing long-range dependences in a data-rich scenario, compared to GNNs. For most projects, CodeArt significantly outperforms the Transformer-based models in challenging setups (i.e., a pool size larger than 100). The improvement demonstrates that CodeArt is able to encode program semantics more precisely. For Putty, CodeArt achieves comparable performance to the previous SOTA model DiEmph. We investigate the cases and found functions in Putty are shorter compared to other projects. For example, more than 75% functions in Putty have less than 50 instructions, while, on the other hand, more than half of the functions in Binutils are longer than that. For those relatively simple functions, the problem of spurious correlations (caused by the lack of symbols) in the baseline models is not as severe and thus the improvement introduced by CodeArt is not significant. Also, in simpler setups (i.e., a pool size smaller than 100), both DiEmph and CodeArt can achieve good performance.

To conclude, the results on the binary similarity task indicate CodeArt can generate embeddings that encode program semantics more precisely, benefiting more realistic use scenarios[75].

**Zero-Shot Performance.** The performance on the binary similarity task relies heavily on the quality of function embeddings generated by a pre-trained model. We thus further evaluate CodeArt on the binary similarity task with the zero-shot setup to measure the effectiveness of pre-training. Specifically, we directly use the embeddings generated by the pre-trained CodeArt model without finetuning it on the binary similarity task. We compare its performance to the pre-trained JTrans model, which is pre-trained on the same dataset as CodeArt. The results are shown in Table 1. We can see that CodeArt demonstrates strong zero-shot performance by achieving over 30% higher performance on all setups. It indicates that the function-level semantics are learned by CodeArt during the pre-training process. We also conduct *t*-test between CodeArt and JTrans' performance and results show that the improvement is statistically significant with *p*-values 1e-6, 6e-8, 1e-6, 2e-9, 3e-9 for pool size 32, 50, 100, 200, 500 respectively.

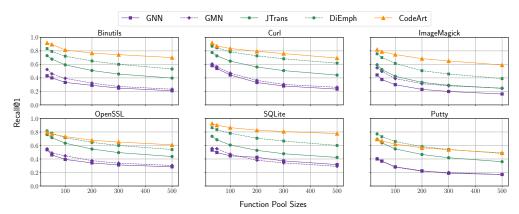


Fig. 8. Performance (recall@1) of CODEART compared to the SOTA GNN-based model, GMN-based model and Transformer-based models. The x-axes denote different sizes of candidate function pools, and the y-axes denote the performance in terms of recall@1.

Table 1. Zero-Shot performance (recall@1) of CODEART compared to JTrans. Each row lists performance of both models w.r.t. one binary project with different sizes of candidate function pools.

Programs	Pool size 32		Pool size 50		Pool size 100		Pool size 200		Pool size 500	
	JTrans	CodeArt	JTrans	CodeArt	JTrans	CodeArt	JTrans	CodeArt	JTrans	CodeArt
Binutils	0.30	0.71	0.26	0.68	0.20	0.60	0.14	0.57	0.09	0.48
Curl	0.32	0.71	0.28	0.66	0.21	0.63	0.16	0.56	0.12	0.47
ImageMagick	0.28	0.60	0.23	0.55	0.18	0.49	0.13	0.45	0.08	0.38
OpenSSL	0.33	0.61	0.28	0.55	0.22	0.52	0.17	0.49	0.12	0.43
SQLite	0.25	0.75	0.21	0.76	0.15	0.69	0.11	0.62	0.07	0.55
Putty	0.30	0.49	0.25	0.45	0.19	0.41	0.14	0.36	0.09	0.31
Average	0.30	0.64	0.25	0.61	0.19	0.56	0.14	0.51	0.10	0.44

# 4.2 RQ2: Performance on Malware Family Classification

**Setup.** To bypass malware detectors, the authors of a malware may create a set of variants of the malware by changing the code while keeping the malicious logic. Such a set of variants is often referred to as a malware family. Malware family classification aims to attribute a given malware sample to a known family. It can help many downstream security applications such as malware detection [81] and malware authorship attribution [33].

In many cases, it is very hard, if not impossible, to precisely attribute a malware sample to a single family. In our dataset, a malware sample may have multiple labels. We therefore use a multi-label classification setting which performs independent binary classification on each class.

**Dataset.** We build a dataset with the malware samples and labels provided by VirusTotal <sup>3</sup> and VirusShare <sup>4</sup>. Since real-world malware families follow a long tail distribution, we use the most frequent 100 families to build our classification dataset. It consists of 5484 malware samples. We randomly split the dataset into train, validation, and test set with a ratio of 70%, 10%, and 20%.

**Baseline.** To demonstrate the effectiveness of CodeArt, we choose JTrans as a Transformer-based baseline because it is pre-trained on the same dataset [75] as CodeArt. Moreover, a mainstream of

<sup>&</sup>lt;sup>3</sup>https://www.virustotal.com

<sup>&</sup>lt;sup>4</sup>https://virusshare.com

Table 2. Malware Family Classification

		AUC (↑)	LRAP (†)	LRL (↓)
2 Funcs.	JTrans	0.705	0.205	0.300
	CodeArt	0.925	<b>0.597</b>	0.086
3 Funcs.	JTrans	0.898	0.478	0.111
	CodeArt	0.924	<b>0.597</b>	0.086
4 Funcs.	JTrans	0.901	0.489	0.107
	CodeArt	<b>0.928</b>	<b>0.597</b>	<b>0.084</b>
	CNN	0.834	0.326	0.183

Table 3. Ablation Study Using Zero-Shot Binary Similarity with Pool-Size 100 on Coreutils

	Recall@1	Recall@3	Recall@5	MRR
CodeArt-3M	0.394	0.548	0.604	0.486
w/o local mask w/o trans-closure max-trans-closure 4 max-trans-closure 6 w/o rel-pos-bias	0.030 0.346 0.322 0.298 0.226	0.096 0.498 0.504 0.456 0.376	0.138 0.590 0.574 0.562 0.464	0.082 0.447 0.429 0.406 0.335

malware family classification leverages Convolutional Neural Network (CNN) to extract features from malware samples [52, 55]. Due to the lack of available code artifacts, we additionally reimplement a CNN SOTA [52] based on ResNet-50 and use it as another baseline.

**Metrics.** We use three metrics typically used in multi-label classification tasks to measure the performance, i.e., ROC-AUC score, Label Ranking Average Precision score (LRAP), and Label Ranking Loss (LRL) [72]. ROC-AUC score is the averaged area under the ROC curve, where ROC curve is the plot of the true positive rate (TPR) against the false positive rate (FPR), at various thresholds. LRAP and LRL rank the predicted probability of all possible types, and compute the scores based on the rank of the groundtruth type. Intuitively, a higher rank of the ground-truth type indicates the model has better capability of identifying the correct type of the input code. Please refer to Section B in the extended version of this paper [70].

**Training Details.** CodeArt is a function-level encoder. To encode a whole binary to an embedding, we encode the forefront k functions separately by our encoder and use an attention pooling strategy [50] to pool the resulting k embeddings into a single binary-level embedding. With the binary-level embedding, we stack a standard classification head to do the prediction. We similarly implement a baseline classifier for JTrans. For the CNN-based baseline, we follow [52] to encode a malware binary to an image. Given a malware binary, we encode the entire binary byte-by-byte to a grey-scale image, compress it to a size of  $256 \times 256$ , and convert it according to ResNet-50's input format. Then we train a ResNet-50 classifier on the images encoded from malware samples.

Results. We report the averaged results on 100 classes in Table 2. Columns 3–5 list the three metrics. Each row presents the performance of one model/setup. Rows 1–2, 3–4, 5–6 present the related models when encoding the forefront 2,3,4 functions, respectively. The last row presents the CNN result, which is on whole binaries. We can see that Codeart achieves the best performance when encoding the forefront 4 functions. It is worth noting that Codeart achieves good performance when encoding even only 2 functions, better than CNN and JTrans. The performance of JTrans degrades when the number of encoded functions decreases because encoding fewer functions requires a model to understand individual functions more precisely. The improvement of Codeart compared to JTrans is significant with *p*-values 3e-4, 2e-4, 3e-3 for AUC-ROC, LRAP, LRL respectively. These results demonstrate that the functional semantics learned in the pre-training of Codeart is more informative for malware family classification. Detailed (per-class) results can be found in Section B in the extended version of this paper [70].

# 4.3 RQ3: Performance on Type Inference

**Setup.** Binary programs do not have high-level type information as in the source code (e.g., pointer, struct, array). The binary type inference task aims to recover the high-level type information from assembly code. Specifically, given a location (e.g., a register) in the binary code, a type inference tool outputs the possible high-level type corresponding to the location. It helps a reverse-engineer

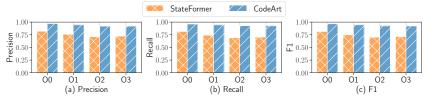


Fig. 9. Binary Type Inference Results on Different Optimization Levels

understand binary code. Moreover, the recovered type information is often an input for other analysis such as vulnerability detection [46], decompilation <sup>5</sup>, and legacy code hardening[28]. Machine-learning based binary type inference tools typically formulate the type inference problem as a sequence labeling task [16, 56]. Following the setup of StateFormer [56], a SOTA binary type inference model, we define 35 common types as labels and include a special label no-access for tokens without groundtruth types.

**Dataset.** We reuse the dataset in StateFormer's repo <sup>6</sup>. As our model is only for x86-64, we only perform experiments on the x86-64 part although the original dataset includes multiple architectures. We randomly split the dataset into train, validation, and test sets with a ratio of 85%, 5%, and 10%.

**Baseline.** We directly compare to the reported results of StateFormer, which was finetuned on the same type inference dataset.

**Training Details.** We finetune CodeArt on the training set by stacking a 1-layer linear classification head to project the last-layer hidden states of tokens to class probabilities. We choose the best-performing checkpoint on the validation set and report the results on the test set.

**Metrics.** We follow the original setting of using precision, recall, and F1-score as the metrics. Here, precision P = TP/(TP + FP), recall R = TP/(TP + FN), and  $F1 = 2 \times P \times R/(P + R)$ , where TP is the number of correctly predicted types, FP is the number of incorrectly predicted types, and FN is the number of types incorrectly predicted as no-access.

**Results on Different Optimization Levels.** We show the results of CodeArt compared to StateFormer in Fig. 9. The three sub-figures show the performance of both models in terms of precision, recall, and F1, respectively. The x-axes denote the optimization levels. We can see that CodeArt achieves better performance in all optimization levels significantly with *p*-values 2e-8, 3e-8, and 3e-8 for precision, recall, and F1-score respectively. We attribute the improvement to CodeArt's capability of precisely encoding tokens in their dependence contexts. Moreover, the performance of CodeArt is more stable across different optimizations, suggesting CodeArt is more robust to syntactic code changes introduced by compiler optimizations.

# 4.4 RQ4: Model Analysis and Ablation Studies on What Helps CODEART

Model Analysis. We now discuss CodeArt in detail. Architecture-wise, CodeArt largely aligns with the BERT-based models in hyperparameters and has slightly more trainable parameters (1296 in addition to 109 million) due to the additional parameters for relative positional bias. The average time of mask construction for a single sample is about 2.5ms. Note that mask construction can be done in parallel with the forwarding and back-propagation of the model. We leave the optimization to our future work. The pre-training dataset is the BinaryCorp-26M dataset [75]. We pre-train CodeArt on the dataset for 25k steps. For more pre-training details, please refer to Section A in the extended version of this paper [70].

<sup>&</sup>lt;sup>5</sup>https://hex-rays.com/ida-pro/

<sup>&</sup>lt;sup>6</sup>https://github.com/CUMLSec/stateformer/

	MFC-AUC	MFC-LRAP	MFC-LRL	TI-PR	TI-Recall	TI-F1
CodeArt-3M	0.89	0.49	0.10	0.96	0.96	0.96
w/o local mask w/o trans-closure max-trans-closure 4 max-trans-closure 6 w/o rel-pos-bias	0.86 0.86 0.90 0.90 0.87	0.40 0.47 0.50 0.47 0.47	0.13 0.14 0.10 0.11 0.12	0.92 0.94 0.96 0.96 0.96	0.91 0.93 0.96 0.95 0.96	0.91 0.94 0.96 0.95 0.96

Table 4. Ablation Study on Other Downstream Tasks that Require Fine-tuning

**Ablation Studies.** To study how each component in CodeArt contributes to the final performance, we alter the pre-training process with different setups and observe how the performance changes. As detailed in Section 4.1, the zero-shot performance on the binary similarity task reflects the quality of embeddings generated by a model and thus demonstrates the effectiveness of pre-training. Therefore, we leverage the zero-shot binary similarity performance on Coreutils (with a pool size of 100) as the metric for pre-training. Due to the limitation of computation resources, we conduct the ablation study on the BinaryCorp-3M dataset. Note that all other evaluations are conducted on the CodeArt model pre-trained on the larger BinaryCorp-26M dataset.

The results are in Table 3. Each row denotes a variant of Codeart and the first row lists the performance achieved with the default setup (denoted by Codeart-3M). We first remove the *local attention masks* from Codeart (denoted by w/o local mask). Without the local masks, Codeart degenerates to a pure masked language model, since all tokens can freely attend to each other and there is no mechanism to ensure that <INST> nodes are aligned with the corresponding instructions, rendering the dependence modeling ineffective. Hence, the performance dramatically drops.

To demonstrate the necessity of modeling transitive dependences in CodeArt, we implement a variant that does not include the *transitive dependence closure* when constructing the dependence attention masks (denoted by w/o trans-closure). That is, the variant mimics the behavior of neighborhood-local aggregating GNNs, meaning that an instruction can only attend to its direct neighbours in the dependence graph. It weakens the perception of transitive dependences. Observe that the performance degrades by 4.8%. It demonstrates that computing transitive closures and using connectivity graphs indeed helps.

We further limit the maximum dependence distances when computing the transitive closures. Specifically, max-trans-closure 4 and max-trans-closure 6 denote variants that only include nodes reachable within 4 and 6 dependence edges, respectively. We can see that the default CodeArt (which does not limit the maximum distance) surpasses the two variants.

Moreover, we remove the *relative positional bias* from CodeArt (denoted by w/o rel-pos-bias). That is, for a given instruction, all the instructions in its dependence context are considered having the same distance to it. We can see that the recall@1 degrades by 17%. It validates that the relative position bias indeed helps the model distinguish instructions with different distances, enabling the model to learn more precise semantics.

In addition to zero-shot binary similarity, we present the ablation study results for other tasks that require fine-tuning in Table 4, where "MFC" denotes "Malware Family Classification" and "TI" denotes "Type Inference". We can see that CodeArt's default setting demonstrates best overall performance, indicating its strong generalizability on all tasks.

# 4.5 RQ5: Comparison with GraphCodeBERT-like Pre-training

As discussed in Section 2, GraphCodeBERT (GCB) leverages data-flow information to enhance the pre-training process. We compare the pre-training of CodeArt to GCB's in terms of *optimization* stability and zero-shot performance on the binary similarity task.

	Recall@1	Recall@3	Recall@5	Recall@10	MRR
CodeArt-3M	0.394	0.548	0.604	0.696	0.486
GCB-like-default GCB-like-weaker-reg	0.030 0.082	0.092 0.176	0.152 0.266	0.264 0.394	0.085 0.159

Table 5. GrapCodeBERT-like Model Results on Zero-Shot Coreutils Binary Similarity with Pool-Size 100

**Training Details.** Since the GCB pre-training code is not publicly available and the origin GCB only worked on source code, we implement a pre-training pipeline following GCB's design and use BinaryCorp-3M[75] to pre-train it for the binary code. We detail how we implement the loss of GCB on binary code in Section C in the extended version of this paper [70]. Specifically, we implement two variants of the training pipeline: (1) *GCB-like-default* that faithfully follows the loss design in GCB. (2) *GCB-like-weaker-reg* that makes the additional losses smaller which means weaker regularization for the MLM loss.

**Optimization Stability.** CodeArt has much better stability. The training curves of both CodeArt and GraphCodeBert-like pre-training are shown in Fig. 11 in Section C in the extended version of this paper [70].

**Performance Difference.** As is shown in Table 5, *GCB-like-weaker-reg* has better performance than *GCB-like-default* due to its stabler optimization. However, both variants are not comparable to CODEART, which demonstrates the effectiveness of our approach when symbols are lacking.

## 5 LIMITATIONS AND FUTURE WORK

The prototype of CodeArt is a pre-trained model for x86-64 binary programs. It can be used in different downstream applications such as binary similarity analysis and malware family classification. Currently, CodeArt is evaluated only on binary programs compiled with different optimizations in commonly-used compilers. The evaluation does not involve obfuscated code. The pre-training dataset is adapted from JTrans [75], which is compiled by GCC. Following the most challenging setup in existing work [82], our test dataset for the binary similarity task is compiled by Clang-O0 and GCC-O3. The type inference dataset is obtained from StateFormer [56], which is compiled by GCC. For the malware dataset, we do not know the precise compiler setups since they are real-world malware samples. While the idea of leveraging data dependences to regularize attentions can generalize to other CPU architectures and potentially to obfuscated code, it requires additional data collection and implementation (for the analysis part) efforts to study CodeArt 's performance on them. We leave it as our future work.

## 6 RELATED WORK

Language Models for Code. A large body of work focuses on pre-training transformer-based language models on source code [3, 20, 29, 40, 77]. They typically perceive source code as a sequence of tokens and utilize the rich natural language artifacts in software, such as symbol names and code comments, to help models understand code semantics. Due to the intrinsic structure of code, researchers have proposed to enhance pre-training with structural information, such as Abstract Syntax Trees (ASTs) [34]. A few works further explore ways to leverage program semantics to improve the quality of language models. For example, OSCAR [58] enhances an IR-level code model with operational semantics of programs. They augment model inputs with abstract program states obtained from static analysis. Their efforts are orthogonal to ours. GraphCodeBERT [35] enhances MLM with data-flow graph structure, and we provide a detailed comparison in Section 4.5. Unlike all the structure-enhanced approaches, our initiative is the first to understand the code structure

from a language perspective. Our encoding pipeline and pre-training methods explicitly model the code language characteristics.

Additionally, there are some transformer-based models on binary code designed for specific downstream tasks [39, 56, 57, 75]. In contrast, CodeArt is designed to pre-train a general model that supports various downstream tasks.

Neural Networks for Explicit Code Structure. Apart from transformer-based language models, numerous code models explicitly encode code structures for specific software engineering tasks, such as program differencing [31], aligning code across platforms [41], disassembling binary code [84], software maintenance [23], and code completion [71]. While these techniques leverage graph structures to derive embeddings, they usually require supervised training. Our empirical results demonstrate that integrating self-supervised learning and self-attention in Transformers with dependence graphs is not only feasible but can also lead to superior performance. Moreover, adapting these models for pre-training tasks requires non-trivial engineering efforts [6, 14, 42]. In contrast, Codeart proposes a unique attention regularization method that is fully compatible with existing efficient and scalable implementations for NLP Transformers. Thus, the pre-training of Codeart can easily scale to larger datasets.

**Binary Program Analysis.** CodeArt works at the level of assembly code, leveraging disassemblers [84] to decode binary files into textual assembly code. There exists a body of research focusing on binary program dependence analysis [85], which could enhance the analysis components in CodeArt. Our work is built upon these fundamental binary program analyses.

## 7 CONCLUSION

We introduce a novel method for pre-training Transformer-based code models in scenarios where symbols are lacking. This method features an innovative attention regularization technique that leverages program analysis to derive potential dependencies between instructions, subsequently forming attention masks. Our pre-trained model is general and can serve a wide range of applications. The empirical results show that our technique substantially outperforms the state-of-the-art, as well as GraphCodeBERT-like pre-trained models, in three downstream tasks, including binary similarity, malware family classification, and type inference for binary executables.

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