

Predictive Modeling of CuInSe₂ Nanocrystal Photovoltaics: The Importance of Band Alignment and Carrier Diffusion

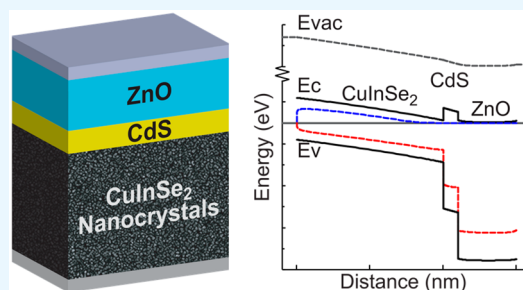
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Supporting Information

ABSTRACT: Nanocrystal inks have been used to make printed photovoltaic devices (PVs) with reasonably high efficiencies; however, little is actually known about the material properties that limit the performance of these devices. Here, we model the output characteristics of PVs with CuInSe₂ nanocrystal absorber layers using the Solar Cell Capacitance Simulator (SCAPS) software package and obtain the thickness-limited response and typical current–voltage behavior, power conversion efficiency (PCE), and external quantum efficiency (EQE) of experimentally fabricated PVs. The device behavior is accurately modeled using a low carrier mobility of $5 \times 10^{-4} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ and measured optical properties for the CuInSe₂ nanocrystal films. The simulations reveal that a reduction in the energy barrier for hole transport at the back contact and increased mobility and lifetime of charge carriers in the CuInSe₂ nanocrystal layer could improve device performance. Furthermore, this system is qualitatively different than the well-studied PVs of PbS nanocrystals, as manipulating the CuInSe₂ nanocrystal electron affinity by ligand exchange actually leads to unfavorable band alignment across the device and does not improve device performance.

KEYWORDS: nanocrystals, photovoltaics, copper indium selenide (CuInSe₂), solar cell device modeling, Solar Cell Capacitance Simulator (SCAPS), ligand exchange, nanocrystal photovoltaics



INTRODUCTION

Photovoltaic devices (PVs) that use an absorber layer of CuInSe₂ nanocrystals can be made on a wide range of unusual substrate materials, including paper¹ and plastic.² These devices rely on the use of colloidal CuInSe₂ nanocrystals deposited from a solvent-based dispersion or ink without any further high temperature processing.^{3,4} This creates new opportunities for integrated PV systems that cannot withstand harsh processing conditions.^{5–8} To date, the power conversion efficiencies (PCEs) of CuInSe₂ nanocrystal PVs made without high temperature sintering of the nanocrystals by selenization have exhibited PCEs of about 3–4%.⁴ This is significantly less than the efficiencies of the best vapor-deposited polycrystalline Cu(In,Ga)Se₂ (CIGS) layers fabricated at high temperature (>500 °C), which have exhibited efficiencies greater than 20%.^{9–12} Solar Cell Capacitance Simulator (SCAPS) PV device modeling software, initially developed to model the device characteristics of CIGS solar cells,^{13–28} has been used to model a variety of different PVs, ranging from flexible CdTe thin film devices to perovskites.^{29–31} Here, we use SCAPS to model the performance characteristics of CuInSe₂ nanocrystal PVs fabricated without additional high temperature processing, identify the fundamental limitations of current devices, and reveal paths toward improvement.

The SCAPS simulations show that device efficiencies could be improved significantly—well above 10%—with a back

contact with higher work function and increased carrier mobilities and lifetimes in the nanocrystal film. We were also interested in identifying fundamental differences between CuInSe₂ nanocrystal PVs and the PbS nanocrystal quantum dot PVs that have achieved a power conversion efficiency (PCE) exceeding 10% by tuning electron affinity with surface chemical treatments.³² We found that ligand exchange strategies for tuning the electron affinity of CuInSe₂ nanocrystals will not lead to improved device performance.

EXPERIMENTAL AND MODELING

CuInSe₂ Nanocrystals. Indium(III) chloride (InCl₃, 99.99%), selenium powder (Se, 99.99%), oleylamine (OLAm, ≥98%), anhydrous toluene, tributylphosphine (TBP, 97%), and diphenylphosphine (DPP, 98%) were obtained from Sigma-Aldrich. Copper(I) chloride (CuCl, 99.999%) was obtained from Strem Chemicals. OLAm was degassed by maintaining a vacuum under 200 mTorr for 4 h at 110 °C and stored in a nitrogen filled glovebox. All other chemicals were used as received.

CuInSe₂ nanocrystals were synthesized according to published procedures.^{1,33} In a nitrogen-filled glovebox, 5 mmol of CuCl, 5 mmol of InCl₃, 50 mL of OLAm, and 1.5 mL of DPP were added to a three-neck flask. The flask was removed from the glovebox and transferred to a Schlenk line. The flask was heated to 110 °C under a

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vacuum (200 mTorr) for 1 h. The flask was then filled with nitrogen and heated to 240 °C. In a separate vial in a glovebox, 10 mmol of Se powder was mixed with 10 mL of TBP. The TBP–Se mixture was injected into the reaction after reaching 180 °C. The temperature of the reaction flask was then maintained at 240 °C for 10 min and then removed from the heating mantle and allowed to cool to room temperature. CuInSe₂ nanocrystals were purified by antisolvent precipitation using ethanol.^{1,33} The isolated nanocrystal product was redispersed in toluene at a concentration of 100 mg/mL. The reaction yields 740 mg of dispersible nanocrystals (44% molar conversion).

Transmission electron microscopy (TEM) images of nanocrystals drop-cast from toluene dispersions on carbon-coated nickel mesh TEM grids (Electron Microscopy Sciences) were acquired with a FEI Tecnai Spirit Bio Twin with 80 kV accelerating voltage. X-ray diffraction (XRD) of the nanocrystals was carried out using a Rigaku R-axis Spider Diffractometer with Cu K α (λ = 1.54 Å) radiation with samples dried on a 0.5 mm nylon loop rotated at 1 deg/s and scanned for 10 min. Absorbance spectra were obtained for nanocrystals dispersed in toluene in a quartz cuvette with a concentration of 0.02 mg/mL using a Cary-5000 UV–vis-NIR spectrophotometer.

Absorption Coefficient Measurements. Absorption coefficient data for CuInSe₂ nanocrystals needed for the SCAPS simulation were experimentally measured. The transmittance and reflectance spectra were measured using a Carry 5000 UV-vis-NIR spectrophotometer with a diffuse reflectance accessory for nanocrystals spin-coated onto one square inch of polished soda-lime glass. The nanocrystals were deposited by spinning the substrate at 600 rpm for 3 s followed by 2000 rpm for 40 s. The absorption coefficient data between 850 and 880 nm were blanked from the data set input into the SCAPS simulation because of the noise in this wavelength region caused by the detector changeover. The thickness of the CuInSe₂ layer was estimated using a KLA Tencor D-500 profilometer. Thickness measurements were taken at three locations over the film, and each measurement varied by less than 20 nm from each other.

SCAPS Modeling. Table 1 lists the parameters used to simulate the response of CuInSe₂ nanocrystal PVs. These parameters were chosen on the basis of guidelines provided by Gloeckler et al.³⁴ for polycrystalline CIGS and the properties of CuInSe₂ nanocrystal absorber layers reported by Akhavan et al.⁴ The key difference between the parameters used to simulate polycrystalline CIGS and CuInSe₂ nanocrystal devices is the charge carrier mobility in the absorber layer. As discussed below, the effective mobility of the electrons and holes that most accurately modeled the experimental device response was 5×10^{-4} cm² V⁻¹ s⁻¹, which is 4 orders of magnitude lower than values measured for polycrystalline CIGS.^{35,36} This value is in the range of effective charge carrier mobilities commonly observed in ligand capped metal chalcogenide nanocrystal films.^{37–41}

Neutral, midgap, defects were included to simulate Shockley–Read–Hall (SRH) recombination, which tends to be the dominant recombination mechanism for photoexcited carriers in polycrystalline CIGS films.³⁴ The SRH lifetime τ is related to the thermal velocity ν , the defect concentration N , and the capture cross section of the defect S_{capture} :^{34,42}

$$\tau = \frac{1}{\nu N S_{\text{capture}}} \quad (1)$$

The minority carrier SRH lifetime in the CuInSe₂ nanocrystal layer of a typical device calculated from the parameters given in Table 1 is 10 ns, which is consistent with the measured recombination lifetime in polycrystalline CIGS, which is often reported to be on the order of 1–10 ns.^{43–47}

The barrier heights at the metal contacts Φ_b are defined by the differences in Fermi energy of the metal and the semiconductor band edge appropriate for the majority carriers.⁴⁸ The majority carrier barriers at the back and front metal contacts are 0.4 and 0.05 eV, corresponding to metal work functions of 5.10 and 4.45 eV,

Table 1. Parameters Used in the SCAPS Simulation of CuInSe₂ Nanocrystal PVs

	CuInSe ₂	CdS	ZnO
thickness (nm)	100	10	40
band gap (eV)	1.0	2.4	3.3
electron affinity (eV)	4.5	4.2	4.4
dielectric permittivity	13.6	10.0	9.0
conduction band density of states (cm ⁻³)	2.2×10^{18}	2.2×10^{18}	2.2×10^{18}
valence band density of states (cm ⁻³)	1.8×10^{19}	1.8×10^{19}	1.8×10^{19}
electron and hole thermal velocity (cm s ⁻¹)	1×10^7	1×10^7	1×10^7
electron mobility (cm ² V ⁻¹ s ⁻¹)	5×10^{-4}	100	100
hole mobility (cm ² V ⁻¹ s ⁻¹)	5×10^{-4}	25	25
donor density (cm ⁻³)	0	1×10^{17}	1×10^{18}
acceptor density (cm ⁻³)	2×10^{16}	0	0
defect type	neutral	neutral	neutral
capture cross section of electrons (cm ²)	1×10^{-13}	1×10^{-17}	1×10^{-15}
capture cross section of holes (cm ²)	1×10^{-15}	1×10^{-12}	1×10^{-12}
total defect density (cm ⁻³)	1×10^{14}	1×10^{18}	1×10^{17}
energetic distribution	single at E_i	single at E_i	single at E_i
		back contact	front contact
electron recombination velocity (cm s ⁻¹)		1×10^7	1×10^7
hole recombination velocity (cm s ⁻¹)		1×10^7	1×10^7
majority carrier barrier relative to E_f (eV)		0.4	0.05
reflectivity		0.8	0.05

respectively. In the absence of Fermi level pinning, these values correspond to the reported work functions of contact layers of gold^{49,50} and tin doped indium oxide (ITO)⁵¹ typically used in CuInSe₂ nanocrystal PVs.

RESULTS

Optical Properties of CuInSe₂ Nanocrystal Films. For the model calculations of device performance, the optical absorption coefficients α of the CdS and ZnO layers were taken from Adachi et al.⁵² For the CuInSe₂ nanocrystal layer, α was measured experimentally for thin films deposited on glass using an integrating sphere. The absorption coefficient α was determined from the measured reflectance R , transmission T , and thickness x of the nanocrystal film: $\alpha = (1/x) \ln((1 - R)/T)$.⁴⁸ The thickness of the nanocrystal film used to determine α was determined by profilometry. The profilometry data are shown in Figure 1d; the average film thickness was 315 ± 8 nm. Parts a and b of Figure 1 show TEM and XRD of the nanocrystals. The average diameter of the nanocrystals was 10.3 ± 2.3 nm, and the XRD data match chalcopyrite CuInSe₂.⁵³ (See the Supporting Information for a histogram of the nanocrystal size distribution.) Figure 1c shows optical absorbance spectra of the nanocrystals dispersed in toluene. The absorption edge is close to the bulk band gap of CuInSe₂, which is consistent with the relatively large nanocrystal size and a lack of quantum confinement.^{53,57} Figure 1f shows the value of α for the nanocrystals compared to bulk CuInSe₂. The wavelength-dependent absorption coefficient of the nanocrystal film is about half of the value for polycrystalline CuInSe₂.⁵⁴ The lower absorption coefficient may be due to the significant void volume between nanocrystals in the film. Defects in the nanocrystals may also be playing a role, as the optical properties of the CuInSe₂

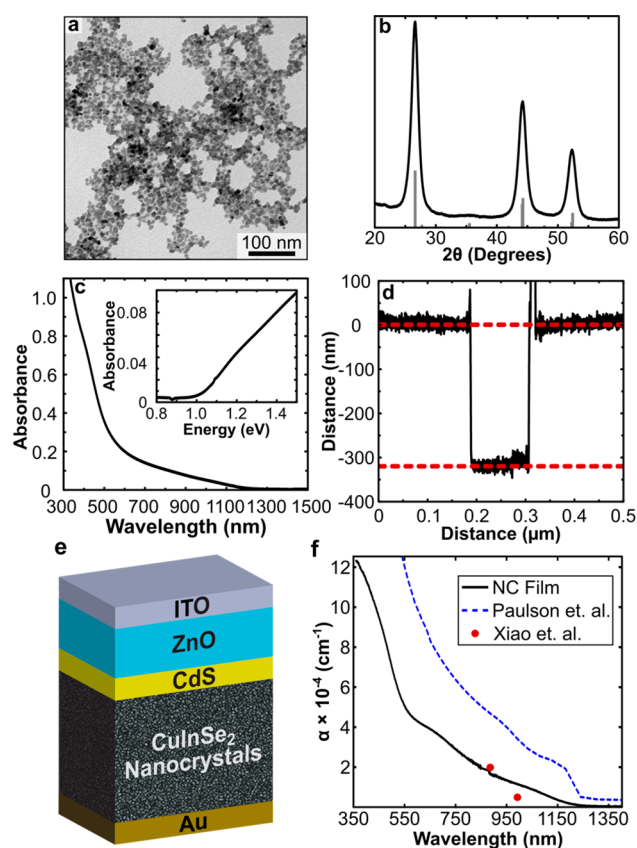


Figure 1. (a) TEM, (b) XRD, and (c) room temperature UV-vis-NIR absorbance spectra of the CuInSe₂ nanocrystals used to measure the optical absorption coefficient for input into the SCAPS device calculations. In part b, the gray drop lines correspond to JCPDS No. 00-040-1487 for chalcopyrite CuInSe₂. The spectra in part c were measured with the CuInSe₂ nanocrystals dispersed in toluene. The inset in part c shows the absorbance spectra rescaled to highlight the absorption onset energy. (d) Profilometry data used to estimate the CuInSe₂ nanocrystal film thickness. The dashed red lines are extrapolations of the data (black) at the substrate and surface of the nanocrystal layer. (e) Illustration of the materials stack for the CuInSe₂ PVs simulated in this work. (f) The absorption coefficient α , measured for a film of CuInSe₂ nanocrystals on glass that was used as input into the SCAPS device simulations. The absorption coefficient determined by Paulson et al.⁵⁴ for polycrystalline CuInSe₂ is shown by the dashed blue line, and absorption coefficients from Xiao et al.⁵⁵ for a CuIn₃Se₅ film are shown by red circles.

nanocrystal film are much closer to the values of a Cu-deficient ordered vacancy compound (OVC), such as CuIn₃Se₅.⁵⁵ The CuInSe₂ nanocrystals are known to be deficient in copper; however, the band gap of 1.0 eV is similar to a polycrystalline CuInSe₂ film.^{35,53,56} Figure 1e shows the device stack used in the simulations, and an electron affinity of 4.5 eV was used for the CuInSe₂ nanocrystals in the device simulations on the basis of reported values for polycrystalline CuInSe₂.^{15,18,22}

The material parameters used in the SCAPS simulations are summarized in Table 1. Figure 2 shows the current–voltage characteristics (*J*–*V* curve) and external quantum efficiency (EQE) response of a CuInSe₂ nanocrystal PV device simulated with a nanocrystal layer thickness of 100 nm. This thickness was used because it is similar to the nanocrystal thickness used in the report by Akhavan et al.⁴ The experimentally observed PCE, V_{oc} , J_{sc} , and FF values of

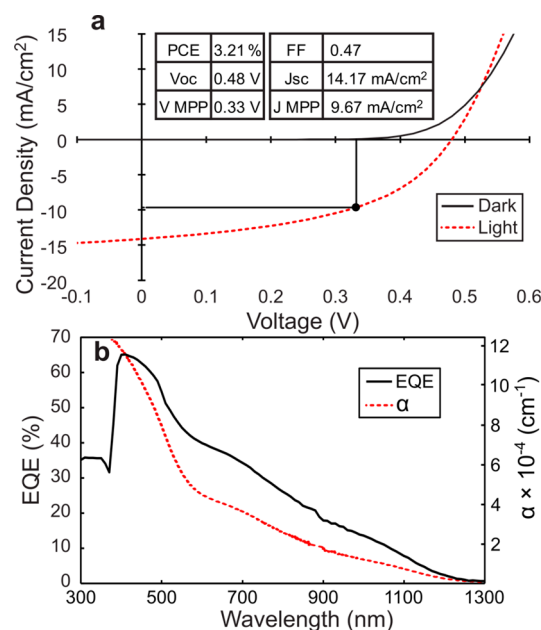


Figure 2. (a) *J*–*V* curves and (b) EQE of a PV device with a 100 nm thick absorber layer of CuInSe₂ nanocrystals generated by SCAPS using the parameters in Table 1. The light *J*–*V* curve is simulated using AM 1.5 conditions at 1000 W/m², and the EQE is simulated under short circuit conditions and 1000 W/m² AM 1.5 white light bias. The experimentally measured absorption coefficient data used for the SCAPS simulations are shown with the dashed red line.

3.21%, 0.48 V, 14.17 mA/cm², and 0.47 compare well to the calculated values of 3.10%, 0.41 V, 16.3 mA/cm², and 0.46. The “crossover” between light and dark *J*–*V* curves in Figure 2a at 0.52 V results from changes in carrier concentration in the CdS layer under illumination and is commonly observed in CuInSe₂ and CIGS photovoltaics.^{4,15,58} In the work of Akhavan et al.,⁴ a crossover was observed at 0.41 V. The difference in the voltage at the crossover points is most likely due to differences in defect concentration in the CdS layer and interface that are not completely accounted for in the model. The EQE values in Figure 2b are relatively low between 1240 and 600 nm and increase significantly at shorter wavelengths between 375 and 600 nm. This is also consistent with experimental observations of Akhavan et al.,⁴ resulting from the relatively thin nanocrystal layer and the noticeably distinct increase in α as the wavelength drops below ~600 nm. The drop in EQE at wavelengths less than 375 nm results from light absorption by the ZnO window layer.

Akhavan et al.^{3,4} studied the effect of the CuInSe₂ nanocrystal layer thickness on device performance and found an optimum thickness of 150 nm for maximum PCE. This is too thin to absorb all of the incident light—conventional polycrystalline devices use CIGS layers that are several micrometers thick.^{11,58} The device efficiency could be increased some by stacking devices using transparent top and bottom contacts, but the overall efficiencies of those devices were still limited.³ Figure 3 shows device characteristics calculated using SCAPS for PVs with different nanocrystal layer thicknesses. The highest PCE was obtained in the simulations for devices with a nanocrystal layer of only 75 nm thick. This is thinner than what has been observed experimentally, most likely due to the fact that it is not practical to fabricate devices with spray-deposited nanocrystal

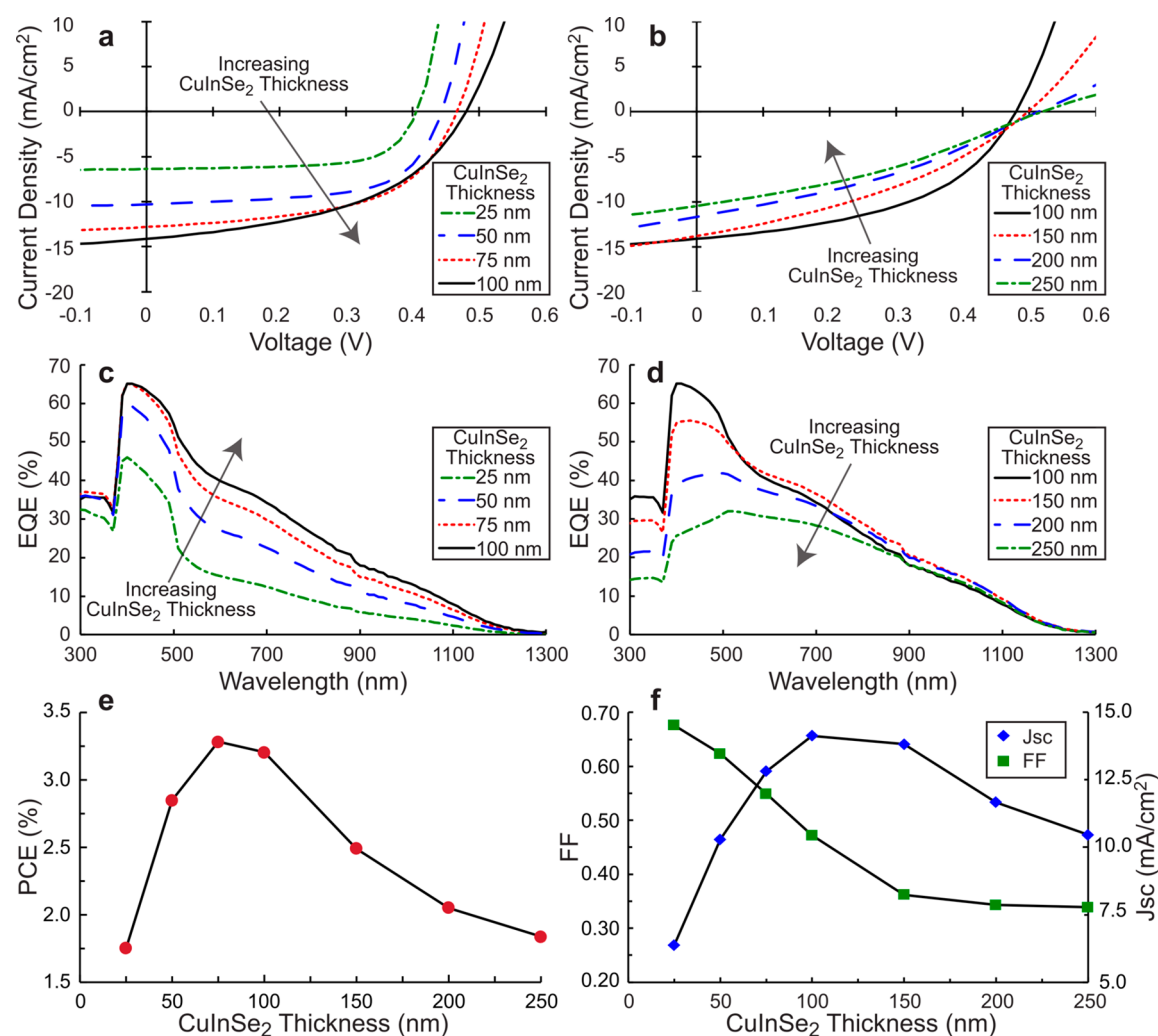


Figure 3. Simulated device response for CuInSe₂ nanocrystal PVs with different nanocrystal layer thicknesses. (a, b) *J*–*V* curves for PV devices under 1000 W/m² AM 1.5 lighting with CuInSe₂ thicknesses ranging from 25 to 250 nm. (c, d) External quantum efficiency (EQE) for PV devices with CuInSe₂ thicknesses ranging from 25 to 250 nm under 1000 W/m² AM 1.5 white light bias and short circuit conditions. (e) Effect of CuInSe₂ nanocrystal layer thickness on PCE. (f) Effect of the CuInSe₂ nanocrystal layer thickness on FF and *J*_{sc}.

layers less than 100 nm thick, as the presence of even the slightest of pinholes leads to device shunting. In any case, the device simulations show that, although thicker nanocrystal layers absorb more light, the FF and the resulting PCE are decreased because the carrier mobility in the nanocrystal layer is so low. Devices with extremely thin CuInSe₂ layers have low *J*_{sc} because of insufficient light absorption, and low *V*_{oc} due to reduced quasi-Fermi level splitting resulting from the low free carrier concentration in the photoexcited film. When the nanocrystal film is thicker than 100 nm, the poor carrier mobility in the CuInSe₂ film begins to generate significant internal series resistance that results in the lowered FF observed in the *J*–*V* curves in Figure 3b.⁴⁸

One of the most appealing attributes of the nanocrystals is the ability to fabricate PVs at room temperature on a wide range of surfaces.^{1–3} While polycrystalline CIGS layers are typically deposited on a Mo back contact, since Mo can withstand the high temperatures necessary to form CIGS,⁵⁸ CuInSe₂ nanocrystals can be deposited onto virtually any back contact.^{1–3} The voltage output of the device depends on the energy barrier at the back contact,^{48,59,60} and CuInSe₂ nanocrystal PVs have been fabricated on Au and Mo back

contacts, with Au contacts providing a higher PCE.^{3,4,61} The barrier heights in Table 1 used in the simulations assume no Fermi Level pinning and are based on Au and Mo work functions of 5.1 and 4.6 eV, respectively.^{49,50} In a conventional polycrystalline CIGS device, a thin layer of MoSe₂ formed during the high temperature selenization process and Fermi level pinning effects significantly reduce the energy barrier at the back contact.^{59,60} Figure 4a shows simulated *J*–*V* curves for CuInSe₂ nanocrystal PVs with different barrier heights at the back contact. *V*_{oc} and PCE improve significantly as the barrier height is decreased, as summarized in Figure 4b. The maximum PCE of 5% obtained in the simulations occurs when the Fermi energy of the metal equals the quasi-Fermi level of the hole in the illuminated CuInSe₂ nanocrystal layer. Parts c–e of Figure 4 show the band alignment across the device layer under illumination. The device configuration exhibiting the maximum PCE has a barrier height at a back contact of 0.2 eV, at which point the Fermi level in the metal back contact aligns with the quasi-Fermi level for holes in the illuminated CuInSe₂ layer. These device simulations show that the PCE and *V*_{oc} of CuInSe₂ nanocrystal photovoltaics could

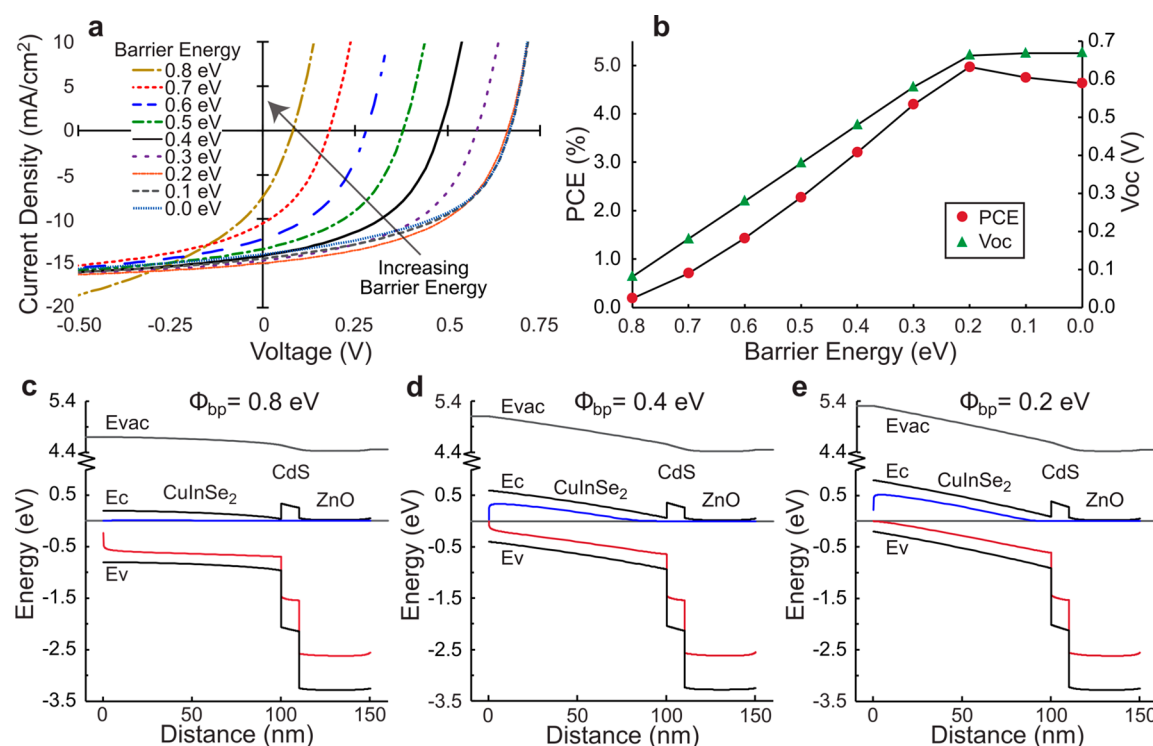


Figure 4. SCAPS simulations of CuInSe₂ nanocrystal PVs with various energy barriers at the back contact. The barrier energy (Φ_{bp}) is the energy difference between the Fermi level of the back contact and the CuInSe₂ valence band energy. (a) J - V curves of PV devices under 1000 W/m² AM 1.5 lighting with Φ_{bp} varied between 0 and 0.8 eV. (b) Effect of Φ_{bp} on V_{oc} and PCE. (c–e) Energy band diagrams generated by SCAPS for PV devices under short circuit conditions with majority carrier barrier energies of 0.8, 0.4, and 0.2 eV, respectively. The quasi-Fermi levels of electrons and holes are indicated by the blue and red lines, respectively.

be improved with further optimization of the back contact and using a metal with a higher work function than gold.

Figure 5 shows the effect of improved electron and hole mobility in the CuInSe₂ nanocrystal layer on device performance. Perhaps counterintuitively, the device performance does not improve with increased carrier mobilities when the nanocrystal layer thickness remains constant. Figure 5 shows the response of a PV with a thin nanocrystal layer of 100 nm, similar to the condition that has yielded the highest efficiency experimentally. The increased mobility leads to an improved FF, but it reduces the V_{oc} and does not change J_{sc} significantly. The combination of these effects leads to little change in the PCE. J_{sc} does not change because it is already limited by the amount of light absorbed by the thin layer of nanocrystals, and V_{oc} decreases because the enhanced mobility reduces the concentration of photoexcited carriers in the absorber layer, which reduces the splitting of the quasi-Fermi levels. Increasing the mobility of the nanocrystal layer, however, would make it possible to use thicker nanocrystal layers to obtain higher performance, and ultimately provides a path toward higher efficiency devices. As shown in Figure 6, the nanocrystal layer thickness that yields optimum device performance increases significantly with improved carrier mobilities and PCE well above 10% is possible. As a benchmark, Figure 6b shows J - V curves simulated for devices with carrier mobility μ , similar to polycrystalline CIGS ($\mu = 5 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$). In this case, a nanocrystal layer thickness of 1 μm would yield a PCE of 12%.

In addition to the mobility, the carrier recombination lifetime also affects the charge carrier diffusion length and influences the performance of PV devices.⁶² Figure 7 shows

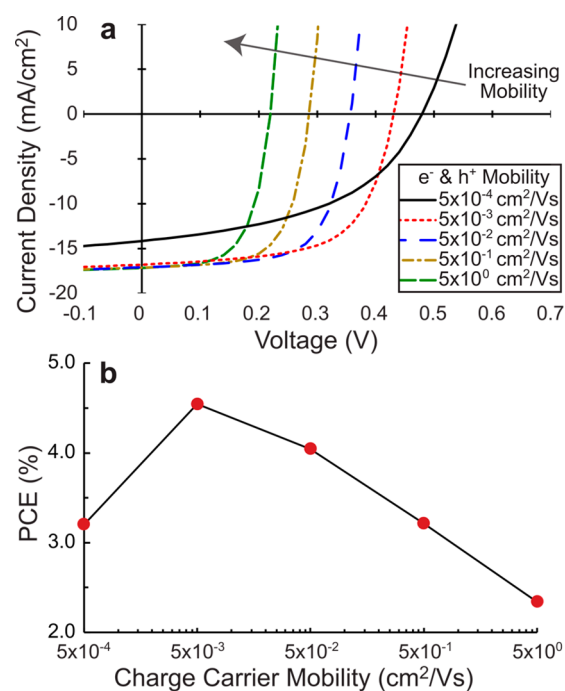


Figure 5. Simulated effect of the electron and hole mobility on the performance of a CuInSe₂ nanocrystal PV device with a constant CuInSe₂ layer thickness of 100 nm. The electron and hole mobilities were equal in each simulation. (a) J - V curves and (b) PCE for PV devices under 1000 W/m² AM 1.5 illumination with the CuInSe₂ electron and hole mobilities varied from 5×10^{-4} to $5 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$. The carrier lifetime was 10 ns.

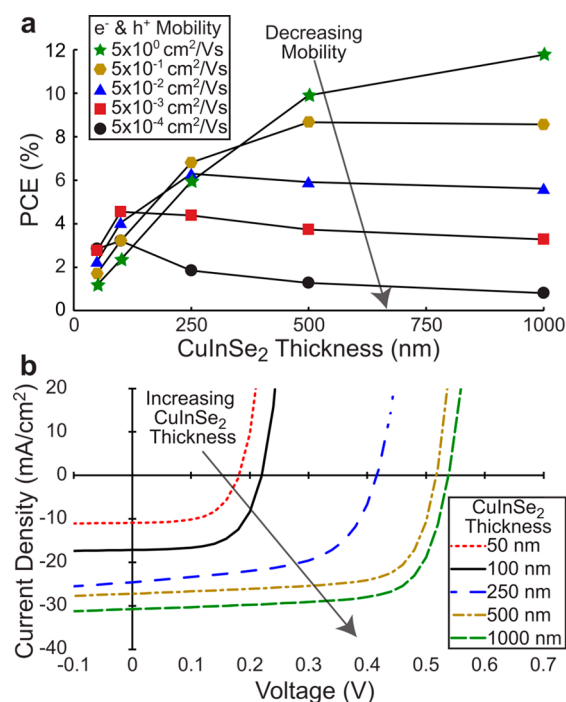


Figure 6. (a) Simulated effect of electron and hole mobility on the PCE of CuInSe₂ nanocrystal PV devices with CuInSe₂ layer thicknesses ranging from 50 to 1000 nm. The electron and hole mobilities were equal in each simulation. (b) *J*–*V* curves for PV devices under 1000 W/m² AM 1.5 illumination with the CuInSe₂ layer thickness ranging from 50 nm to 1 μm and electron and hole mobilities of 5 cm² V⁻¹ s⁻¹. The carrier lifetime was 10 ns.

simulated device responses for PVs with thin 100 nm CuInSe₂ nanocrystal layers with various carrier lifetimes. Unlike the carrier mobility, increases in carrier lifetime could significantly improve the device efficiency without changing the nanocrystal film thickness. The experimentally observed PCE is about 3%, which corresponds to a carrier lifetime of about 10 ns, as shown in Figure 7b. Short lifetimes of 1 ns lead to devices with efficiencies lower than 1%. Short carrier lifetimes lead to low *J*_{sc} because of the loss of carriers due to the high rate of carrier recombination. The resulting loss in carrier concentration because of the significant recombination also lowers *V*_{oc} because of the corresponding lowering of the quasi-Fermi level splitting. An increase in carrier lifetime to 100 ns would bring the PCE to about 5%. Improvements in PCE are much less when the minority carrier lifetime increases above 100 ns because the PV performance again becomes limited by the amount of light absorbed by the thin (100 nm) nanocrystal layer. The maximum PCE for a device with such a thin nanocrystal layer is about 6%, with carrier lifetimes of >1 μs.

Figure 8 shows simulated device performance with varying carrier lifetimes when the CuInSe₂ nanocrystal layer thickness is modified. There is an optimum layer thickness for different carrier lifetimes. For example, when the lifetime is very short (1 ns), the highest performance is achieved for extremely thin nanocrystal layers. The highest efficiency shown in Figure 8a is 10%, corresponding to a layer thickness of 500 nm with a carrier lifetime of 1 μs. Even without any improvements in carrier mobility in the device layer, increases in the carrier lifetime could significantly improve device performance to >5%. The low mobility of the nanocrystal films ultimately

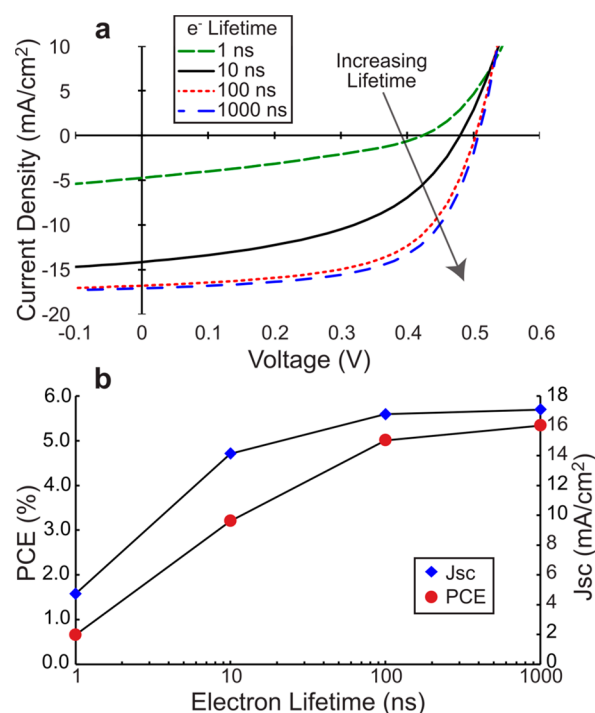


Figure 7. Simulated effect of carrier lifetime in the CuInSe₂ layer on the performance of a CuInSe₂ nanocrystal PV device with a constant CuInSe₂ layer thickness of 100 nm. The lifetimes τ were varied by changing the defect concentration *N* in the CuInSe₂ layer using eq 1 with cross sections for defect capture for electrons and holes of 1 × 10⁻¹³ and 1 × 10⁻¹⁵ cm². For comparison, the minority carrier lifetime of the CuInSe₂ device in Figure 2 is 10 ns. The capture cross section and thermal velocity for electrons and holes was the same for all device simulations. (a) *J*–*V* curves for PV devices under 1000 W/m² AM 1.5 lighting where the minority carrier lifetime CuInSe₂ is varied from 1 to 1000 ns. (b) Effect of carrier lifetime in the CuInSe₂ layer on PCE and *J*_{sc}. The mobility was 5 × 10⁻⁴ cm² V⁻¹ s⁻¹.

limits the performance of the devices by creating internal series resistance that lowers *J*_{sc} and FF when the nanocrystal layer is too thick.

Another strategy for improving the efficiency of nanocrystal PVs has been to manipulate the electron affinity of the nanocrystals by surface modification of the capping ligands.^{32,63} This has been done successfully in the case of PbS nanocrystal quantum dot PVs to achieve device efficiencies exceeding 10% without any high temperature processing.³² Chuang et al.³² suggest that ligand exchange chemistry on PbS nanocrystals can generate a relatively large electron affinity offset of 0.63 eV. This electron affinity offset was then used to create an electron blocking layer and extend the depletion region in the light absorbing layer. This approach was explored for CuInSe₂ nanocrystal PVs, as shown in Figures 9 and 10 (see also Tables S1–S4 in the Supporting Information). The SCAPS device simulations reveal that this is not a viable approach for CuInSe₂ nanocrystal devices. As shown in Figure 9a–d, there is a relatively narrow range of viable electron affinity values for optimal CuInSe₂ nanocrystal photovoltaics. Either an increase or decrease in the electron affinity of the nanocrystals results in reduced PCE. For lower electron affinity, this is the result of increased contact resistance that gives rise to rollover in the *J*–*V* curves and low FF due to a poor conduction band offset between the CuInSe₂ and CdS, as shown in Figure 9e. When

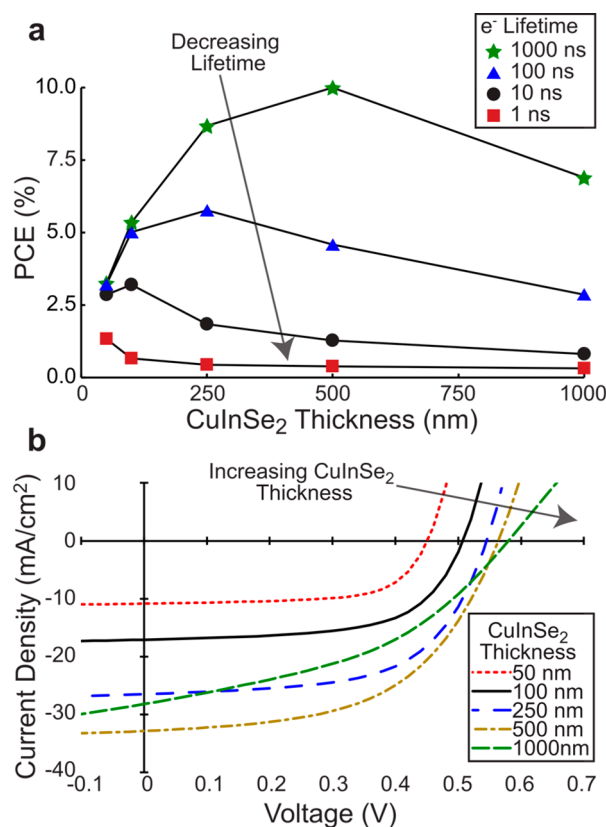


Figure 8. Effect of carrier lifetime in the CuInSe₂ layer on the performance of PV devices with varying CuInSe₂ nanocrystal layer thickness. Lifetimes were varied using the same approach as that described in the caption of Figure 7, using eq 1. (a) PCE of CuInSe₂ nanocrystal PV devices with the CuInSe₂ thickness varied from 50 nm to 1 μ m and minority carrier lifetimes varied from 1 ns to 1 μ s. (b) J - V curves for PV devices under 1000 W/m² AM 1.5 lighting with various CuInSe₂ layer thicknesses and an electron lifetime of 1 μ s. The mobility was 5×10^{-4} cm² V⁻¹ s⁻¹.

the electron affinity is higher than 4.4 eV, the resistance at the back contact becomes a problem and leads to a drop in V_{oc} and PCE. Any alterations in the electron affinity of the CuInSe₂ nanocrystals will also require significant changes to the device architecture. Figure 10 shows the effects of graded or stepped electron affinity in the nanocrystal layer. As shown in Figure 10a, this leads to s-shaped J - V characteristics. Essentially, the nanocrystal layer can be thought of as two CuInSe₂ layers with different maximum power point voltages. At the maximum power point voltage, the CuInSe₂ near the back contact is slightly overbiased, while the CuInSe₂ near the CdS is slightly underbiased. Several variations of this graded structure were studied, and none exhibited a major increase in device performance. The PCE, J_{sc} , V_{oc} , and FF for these devices are available in the Supporting Information.

DISCUSSION

Increasing the Carrier Mobility of CuInSe₂ Nanocrystal Films. Increasing the effective charge carrier mobility in the CuInSe₂ nanocrystal film so that it is closer to the mobility of polycrystalline CIGS is a conceptually straightforward strategy for improving the PCE; however, it has not yet been achieved in practice. For polycrystalline CIGS films, the majority and minority carrier mobilities have been measured by various techniques (such as the Hall effect,⁶⁴ photocarrier

time-of-flight spectroscopy,⁶⁵ and high frequency admittance spectroscopy⁶⁶) to be on the order of 1–10 cm² V⁻¹ s⁻¹. The most common way to measure carrier mobility in nanocrystal films is to create a transistor and measure the field effect mobility.^{38,40,41} Other techniques that have been used to characterize the mobility of nanocrystal films include time-of-flight spectroscopy³⁹ and time-resolved terahertz spectroscopy.³⁷ As deposited metal chalcogenide nanocrystal films with organic capping ligands tend to have mobilities on the order of 10⁻⁴ to 10⁻³ cm² V⁻¹ s⁻¹, while various treatments can improve the mobility up to the order of 10⁰ cm² V⁻¹ s⁻¹.^{37–41} A proven method used to improve the PCE of PbS nanocrystal PV devices is to implement surface ligand treatments to minimize the barrier between nanocrystals and increase the electron and hole mobility in the film.⁶⁷ The CuInSe₂ nanocrystal device simulation in Figure 3 uses electron and hole mobility values of 5×10^{-4} cm² V⁻¹ s⁻¹ for the untreated CuInSe₂ nanocrystal layer. The electron and hole mobilities were set equal in all simulations, which is a simplified first-order approximation; however, since the charge transport through a nanocrystalline film is typically dominated by nearest neighbor hopping, we expect the carrier mobilities to have similar, but not necessarily identical, values in any given film.³⁸ The SCAPS simulations show that higher carrier mobilities would lead to improved device performance. The ligand exchange strategies that have been used so successfully for PbS nanocrystals have not been readily applied to CuInSe₂ nanocrystals.^{33,68} This could be because of chemical differences in the nature of the capping ligands and may require alternative approaches like a photonic curing⁶⁹ process that enables sintering nanocrystals without damaging the underlying substrate.

Increasing the Lifetime of Carriers in CuInSe₂ Nanocrystal Films. Carrier lifetime is generally measured in polycrystalline CIGS by time-resolved photoluminescence (TRPL), which probes photoluminescence intensity changes at ultrafast time scales.^{43–47,70} These studies typically report recombination mechanisms dominated by the nonradiative Shockley–Read–Hall (SRH) process with lifetimes between 1 and 10 ns. However, by implementing strategies for defect passivation, much longer carrier lifetimes, dominated by radiative recombination processes, have been demonstrated. Metzger et al.⁷⁰ showed that eliminating oxidation of polycrystalline thin films can help extend the carrier lifetimes in polycrystalline CIGS thin films up to 250 ns, a value which was over an order of magnitude larger than previous reports. Additionally, Li et al.⁷¹ demonstrated that CuInSe₂ nanocrystals coated with a CdS or ZnS shell exhibit lifetimes near 500 ns, representing a three orders of magnitude enhancement compared to the bare nanocrystal cores. Our model indicates that increasing the carrier lifetime in the CuInSe₂ nanocrystal films using some variation of these methods can increase device efficiencies to nearly 10%.

CONCLUSIONS

Modeling the device response of CuInSe₂ nanocrystal PVs using SCAPS simulation software has provided insights about how to improve performance without resorting to high temperature processing of the nanocrystal layer. The simulated J - V and EQE characteristics of the CuInSe₂ nanocrystal PVs show good agreement with experimental results and exhibit the expected thickness-limited performance. Reducing the majority carrier barrier at the back contact,

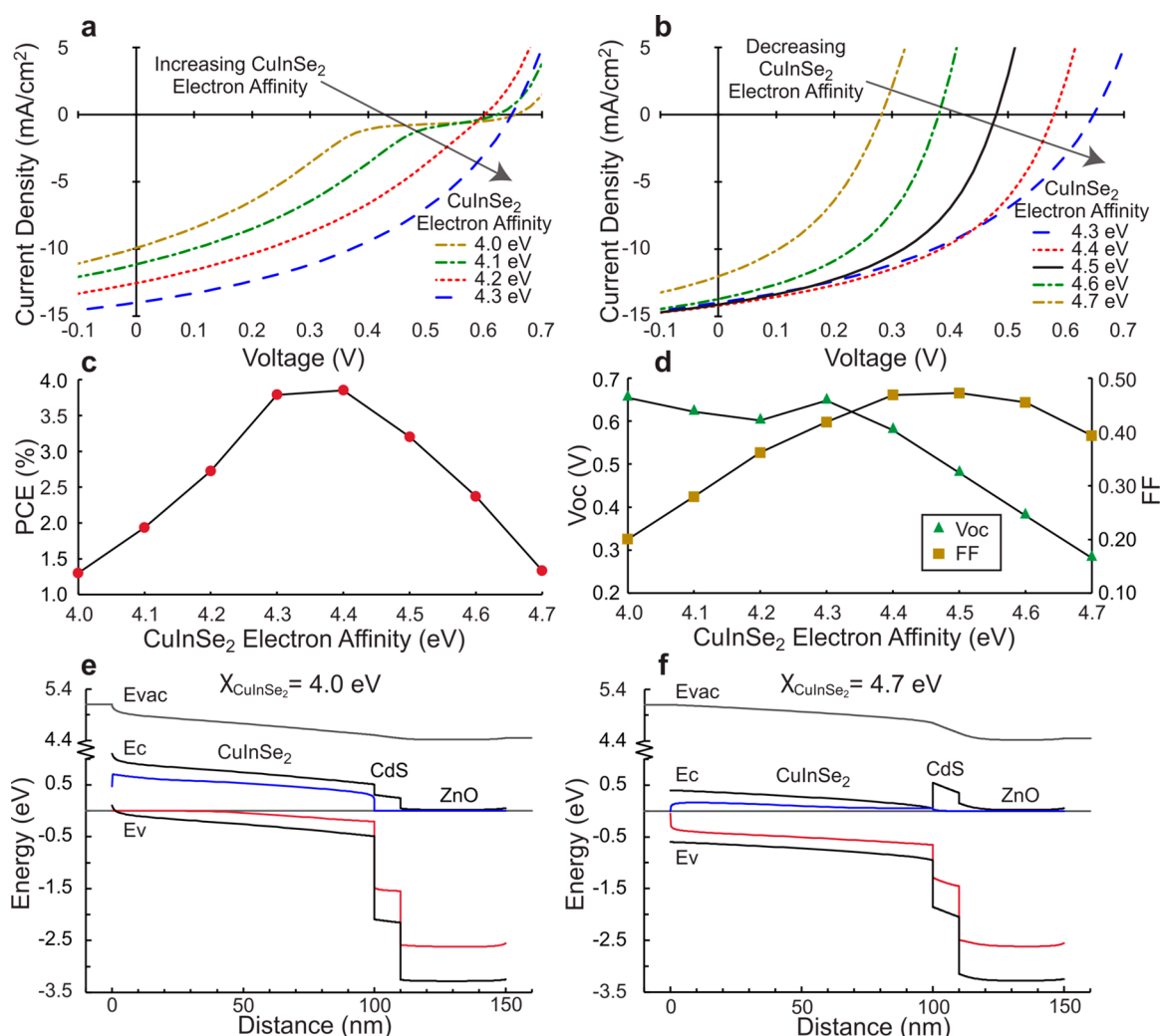


Figure 9. Simulated effect of the CuInSe₂ electron affinity on PV performance. (a, b) J–V curves under 1000 W/m² AM 1.5 illumination with the CuInSe₂ electron affinity varied from 4.0 to 4.7 eV. (c) Effect of the CuInSe₂ electron affinity on PCE. (d) Effect of the CuInSe₂ electron affinity on FF and V_{oc}. (e, f) Energy band diagrams generated by SCAPS for CuInSe₂ PV devices under short circuit conditions with electron affinities of 4.0 and 4.7 eV, respectively.

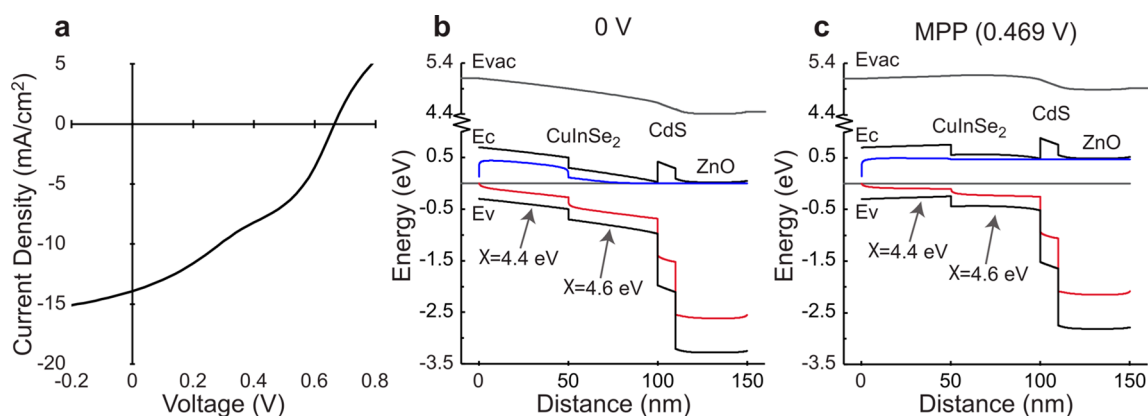


Figure 10. Performance of a PV device with a graded electron affinity in the CuInSe₂ nanocrystal layer. The electron affinity for the half of the layer near the back contact is 4.4 eV, and that for the half near the CdS layer is 4.6 eV. (a) J–V curve for the device under 1000 W/m² AM 1.5 lighting. (b, c) Energy band diagrams generated by SCAPS for the device under (b) short circuit conditions and (c) the max power point voltage of 0.469 V.

increasing the effective charge carrier mobility in the CuInSe₂ film, and increasing the carrier lifetime CuInSe₂ film are all shown to be viable strategies for significantly improving device

performance. Modifying the electron affinity of the CuInSe₂ layer, without making other significant changes to the device architecture, is shown to provide little to no improvement in

device efficiency. These results provide insight about how to improve material properties of ink-deposited CuInSe₂ nanocrystals in PV devices without resorting to high temperature processing.

■ ASSOCIATED CONTENT

● Supporting Information

The Supporting Information is available free of charge on the ACS Publications website at DOI: 10.1021/acsaem.8b02090.

Histogram showing the nanocrystal size distribution and tabulated PCE, V_{oc} , J_{sc} and FF for photovoltaic devices with variations of the graded electron affinity structure (PDF)

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Notes

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