

Highly tin doped GaAs at low growth temperatures using tetraethyl tin by metal organic vapor phase epitaxy

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ABSTRACT

GaAs layers with a high tin (Sn) doping concentration and a smooth surface morphology were successfully grown at temperatures as low as 425 °C using tetraethyl tin (TESn) by metal organic vapor phase epitaxy (MOVPE). The samples grown at 650 °C showed Sn-rich droplets even with a low TESn molar flow rate of 0.06 μmol/min, indicating that Sn atoms were not incorporated into the GaAs film but segregated and accumulated on the film surface. Droplet formation was suppressed at a low growth temperature of 425 °C. This suggests that the segregation process is kinetically-limited, with the segregation mechanism is slow compared to the growth rate at low growth temperatures. Uncorrected Hall effect measurements found an electron concentration of $\sim 1 \times 10^{19} \text{ cm}^{-3}$, which is close to the maximum reported doping-limit obtainable in GaAs:Sn grown by MOVPE, while avoiding droplet formation and maintaining a smooth surface morphology. The electron mobility is dominated by the ionized impurity scattering. The sample possesses a generally flat Sn profile extending from the substrate to the film surface. Secondary ion mass spectroscopy indicates that the Sn is electrically active but compensated by carbon acceptors to yield the measured carrier concentration.

1. Introduction

Heavily-doped GaAs is a key technology in the fabrication of high-performance AlGaAs/GaAs heterojunctions bipolar transistors (HBTs) [1–8]. High doping concentrations offer low contact resistance in the emitter and the collector regions, low heat dissipation, and short RC time constants, thus increasing the speed of the device. The heavily-doped layers with a doping concentration on the order of 10^{19} cm^{-3} or higher are desirable. The commonly used n-type dopants for GaAs are sulfur (S), selenium (Se), tellurium (Te), silicon (Si), and tin (Sn). However, the diffusion of S [9–13], and Se [14,15] exhibited a complex double-profile shape. Te doping suffers from the superdilation of the lattice at a carrier concentration above $2 \times 10^{18} \text{ cm}^{-3}$, where the lattice parameter increases with doping at a rate which is 15 times that given by Vegard's law prediction [16,17] indicative of the co-incorporation of point defects. In addition, Te has a very low sticking coefficient when using molecular beam epitaxy (MBE) due to the low condensation coefficients and/or low adsorption lifetimes, thus its incorporation has not been readily successful by MBE [18,19]. The highest available carrier concentration reported with Si is only $6 \times 10^{18} \text{ cm}^{-3}$ due to its amphoteric behavior and its limited solid solubility [20–22].

Sn is an alternative dopant that can overcome several of these

disadvantages of the other dopants because: (i) it is less amphoteric than Si, (ii) can achieve a carrier concentration $\sim 2\times$ higher than Si, at $> 10^{19} \text{ cm}^{-3}$, yielding one of the highest, if not the highest, n-type carrier concentrations in GaAs [23], and (iii) Sn has a lower vapor pressure than S, Se, and Te, which decreases the ‘memory effect’ [24]. Sn doping does have some complexities. Sn can exhibit concentration dependent diffusion, leading to the ‘double knee’ profile seen with Zn and other dopants at high carrier concentrations and high temperatures for extended times [25]. Sn can also exhibit surface segregation and subsequent accumulation due to solubility limitations under certain growth conditions. The reported carrier concentration for Sn doping when using metal organic vapor phase epitaxy (MOVPE) has, however, been much higher than other dopants with reported carrier concentrations of as high as $1.6\text{--}1.7 \times 10^{19} \text{ cm}^{-3}$ [26]. Typically, there is limited thermodynamic solubility for many dopants, such as Sn, which can lead to surface segregation. There was significant Sn segregation reported in this study for a growth temperature of 650 °C. A previous report showed that the surface segregation of Sn in GaAs can be significant at growth temperatures > 490 °C and with the degree of surface accumulation increasing with growth temperature [27]. Film growth at temperatures lower than 490 °C is an approach to suppress the surface segregation of Sn and to obtain highly doped layers possessing a low contact resistance without anomalous diffusion. Here,

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ethyl-based growth sources, triethyl gallium (TEGa) and tetraethyl tin (TESn), as an alternative to the more commonly employed trimethyl gallium (TMGa) and tetramethyl tin (TMSn), are employed to form high quality and heavily-doped GaAs films grown at low growth temperatures. TEGa has been used to deposit low carbon, high-purity GaAs epilayers at temperatures lower than typically achieved using TMGa [28,29]. Compared with TMSn, TESn has a lower decomposition temperature well-suited to low temperature growth and a suitable vapor pressure, which offer the possibility of growing n-type GaAs over a large range of dopant concentration.

In this paper, GaAs layers with high tin-doping concentration, smooth surface morphology, and controlled tin profile were successfully grown at low temperature of 425 °C using TEGa and TESn by MOVPE. The electron concentration obtained by uncorrected Hall-effect measurements is $\sim 1.0 \times 10^{19} \text{ cm}^{-3}$. The ionized impurity scattering dominates the electron mobility. The doping profiles of Sn is flat without observed segregation to the surface.

2. Experimental procedure

GaAs:Sn films were grown on nominally singular (1 0 0) GaAs substrates within a horizontal MOVPE reactor. The reactor pressure was 0.1 bar and the growth temperatures were 650 and 425 °C. Triethyl gallium ($(\text{C}_2\text{H}_5)_3\text{Ga}$), arsine (AsH_3), and tetraethyl tin ($(\text{C}_2\text{H}_5)_4\text{Sn}$) were used as the Ga, As, and Sn precursors, respectively. The molar flow rates for $(\text{C}_2\text{H}_5)_3\text{Ga}$ and AsH_3 are 60.6 and 822 $\mu\text{mol/min}$. The molar flow rates of $(\text{C}_2\text{H}_5)_4\text{Sn}$ are 0.06, 0.13, 0.25, 3.80, 7.60, 10.8 and 13.2 $\mu\text{mol/min}$. The sample structure is shown in the inset of Fig. 1. The growth of GaAs:Sn layer was preceded by the growth of a 1 μm -thick AlGaAs and a 100 nm-thick undoped GaAs buffer layer at 700 °C with an AsH_3 flow of 3288 $\mu\text{mol/min}$. The secondary ion mass spectroscopy data of preliminary samples showed Sn spike at the interface between GaAs:Sn layer and buffer layer. The use of AlGaAs layer eliminated the Sn spike at the interface and an uniform doping profile was obtained along the GaAs:Sn layer (see Fig. 3). The top GaAs buffer layer electrically isolates the AlGaAs layer and eliminates the possibility of the formation of a 2DEG at the GaAs-AlGaAs interface arising from the Sn doping. A hydrogen carrier gas was used at a total flow rate of ~ 7 slpm ($\sim 0.28 \text{ mol/min}$) within the 76 mm diameter horizontal reactor. Purified H_2 carrier gas was used. A growth time of 263 s was kept constant for all samples.

Carrier concentration and mobility were measured at room temperature using the Hall effect by the Van der Pauw method. The surface

morphology and thickness of the samples was characterized by scanning electron microscopy (SEM) and atomic force microscope (AFM). Secondary ion mass spectroscopy (SIMS) was used to obtain the atomic concentrations of Sn and C in GaAs epilayers allowing a general comparison of the physical and electrically-active Sn concentrations. SIMS measurement was performed using 14.5 keV Cs^+ primary ions.

3. Experimental results

The TESn molar flow rate dependence of the thicknesses of GaAs:Sn films grown at 425 °C is illustrated in Fig. 1. The thickness decreases when the TESn molar flow rate increases. The decrease in the growth rate at high TESn flow rate indicates an influence of the Sn or some component of the TESn in modifying the growth front chemistry.

The surface morphology of Sn-doped GaAs films grown at 650 and 425 °C is shown in Fig. 2. At 650 °C, Sn droplets exist with a low TESn molar flow rate of 0.13 $\mu\text{mol/min}$ evidencing surface segregation with subsequent surface accumulation and aggregation. Rough or non-planar surface morphology has been reported, even without Sn droplet formation in several previous MOVPE studies, particularly at high doping levels [30], for layers grown ~ 625 – 685 °C. Both SEM and AFM images reveal that the growth at lower temperature led to a smooth morphology with no Sn droplet formation even at the highest achieved doping level, where the RMS roughness is $< 1 \text{ nm}$ with values of 0.32 and 0.52 nm at $\text{TESn} = 0.13$ and 13.22 $\mu\text{mol/min}$, respectively.

The concentrations of Sn and C in GaAs:Sn grown at 425 °C are shown in Fig. 3. The sample shows a generally flat Sn profile extending from the substrate to the film surface. The Sn and C concentrations are around 2.1×10^{19} and $1 \times 10^{19} \text{ cm}^{-3}$, respectively. The C concentration in the GaAs:Sn film is higher than in GaAs buffer layer. A primary reason is the difference in growth temperatures, which are 425 and 700 °C for GaAs:Sn and GaAs buffer, respectively. The decomposition of organometallic sources in MOVPE is thermally activated and when the temperature is low the decomposition efficiency decreases, leading to a higher C concentration. In addition, the decomposition of TESn may also lead to extraneous C in the GaAs:Sn film.

The carrier concentration as a function of TESn molar flow rate for GaAs:Sn films grown at 425 °C is shown in Fig. 4. If the carrier concentration was defined as $n = \frac{-1}{eR_H}$, where R_H is the measured Hall coefficient, there will be an underestimate of both the carrier concentration and the mobility. In the case of mobility, there is generally a $\sim 30\%$ underestimate of the mobility using the conventional, uncorrected Hall measurements. In the case of degenerate semiconductors, the carrier transport is dominated by ionized impurity scattering, as is the case here, and the band structure is modified by impurity banding and other band structure modifications. The Hall coefficient is more accurately defined as $R_H = \frac{-1}{ne} \frac{\langle \tau^2 \rangle}{\langle \tau \rangle^2}$ where $\frac{\langle \tau^2 \rangle}{\langle \tau \rangle^2}$ is the appropriate averages of the energy dependent carrier scattering factor, $\tau(\varepsilon)$ [31]. The typical case where lattice scattering dominates ($\tau(\varepsilon) \propto \varepsilon^{-\frac{1}{2}}$) in non-degenerate, high temperature measurements, $\frac{\langle \tau^2 \rangle}{\langle \tau \rangle^2} = \frac{3\pi}{8}$ which is generally set equal to unity. For cases where impurity scattering dominates ($\tau(\varepsilon) \propto \varepsilon^{\frac{3}{2}}$), as in the current measurements, $\frac{\langle \tau^2 \rangle}{\langle \tau \rangle^2} = \frac{315\pi}{512} = 1.93$. The real case is yet more complicated by the presence of impurity bands and potentially non-parabolic bands. This indicates that the actual carrier concentration is substantially higher than indicated by the simplest interpretation of the Hall measurements. Similar modifications and concerns over the interpretation of the mobility derived from the Hall measurement must be taken [32,33]. In this paper we will use the corrected Hall coefficient, i.e. $R_H = \frac{-1.93}{en_e}$, to provide a more accurate estimate of the carrier concentration relevant for device applications.

All films were highly n-type as grown. The electron concentration, n_e , increases with TESn molar flow rate then saturates due to both

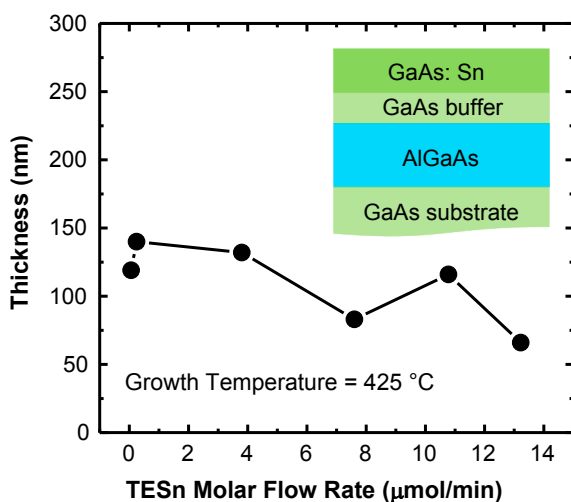


Fig. 1. The GaAs:Sn film thickness (growth rate) dependence on the TESn molar flow rate. The growth time and growth temperature were fixed at 263 sec and 425 °C, respectively.

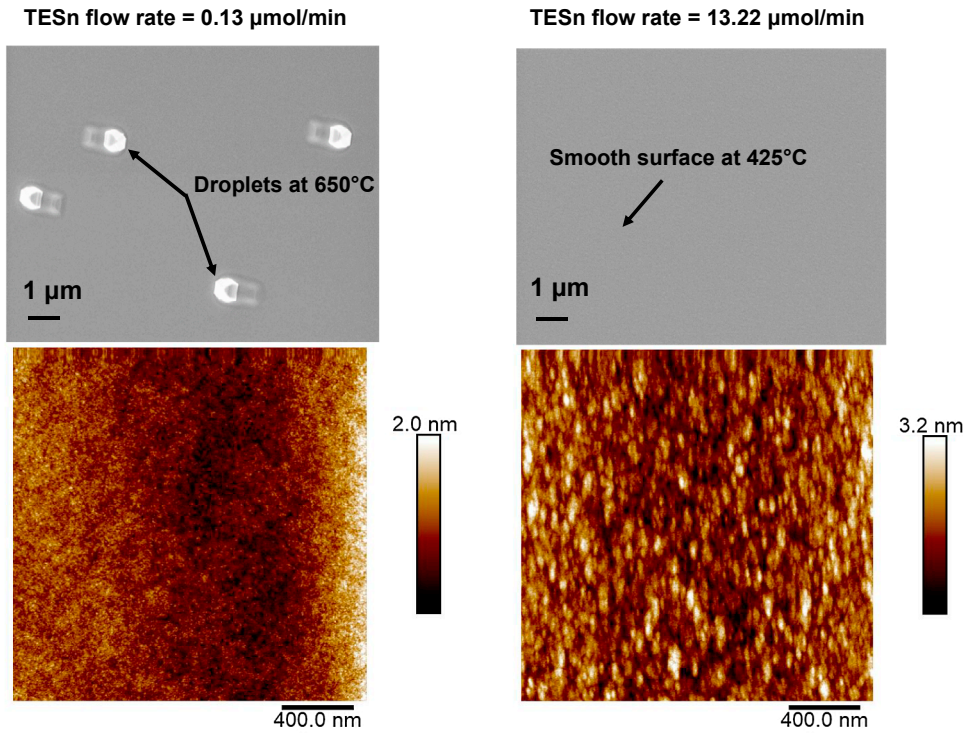


Fig. 2. Surface morphology of GaAs:Sn films grown at 650 and 425 °C. The TESn molar flow rates are 0.13 and 13.22 $\mu\text{mol/min}$, respectively. Sn droplets appear at 650 °C but are suppressed at a growth temperature of 425 °C. The RMS roughness for both samples is < 1 nm with values of 0.32 and 0.52 nm at TESn = 0.13 and 13.22 $\mu\text{mol/min}$, respectively.

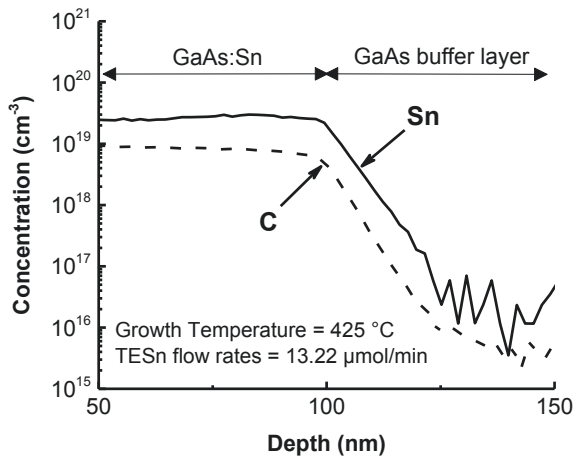


Fig. 3. SIMS-based measurement of the Sn and C concentrations in a typical GaAs:Sn layer grown at 425 °C.

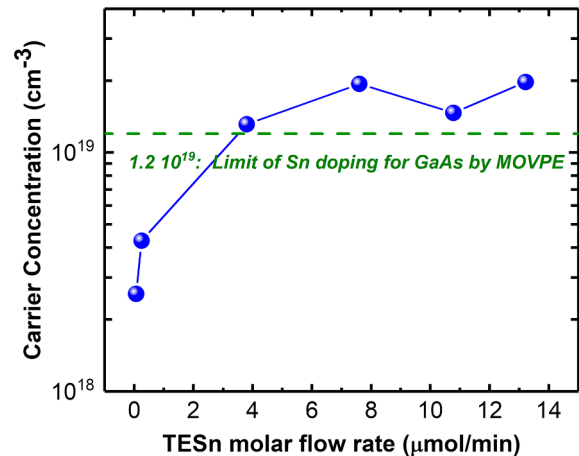


Fig. 4. Carrier concentration versus the TESn molar flow rate for GaAs:Sn films grown at 425 °C assuming a Hall coefficient corrected as outlined in text.

approaching the dopant solubility and perhaps limitations due to surface incorporation at these high TESn flow rates and low growth temperatures. At some point, the Fermi level is pulled sufficiently far from the impurity band that further ionization is not possible with increased dopant incorporation. A value of $n_e = 2.00 \times 10^{19} \text{ cm}^{-3}$ was achieved, which is among the highest reported for MOVPE growth without surface segregation [24,26] and higher than the reported solid solubility at this temperature, also indicated on the graph. The smooth surface morphology was preserved even at the high doping level. In this work, the use of TESn offers not only a high electron concentration, but also allow the low temperature growth, which is beneficial in the growth of other materials systems which cannot support the high temperature growth.

Fig. 5 shows the electron mobility, μ_e , versus the carrier concentration at room temperature. μ_e decreases with increases in Sn, and hence, electron concentration. The mobility would be dominated by ionized impurity scattering at these doping levels. A mobility of $978 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ is achieved at the highest doping concentration of

$\sim 2 \times 10^{19} \text{ cm}^{-3}$. This is similar to ones reported in GaAs:Sn films grown by MOVPE [24,34]. The low-temperature growth of Sn-doped GaAs present drawbacks such as the incorporation of high density of carbon. However, the mobility is high enough to be acceptable for device fabrication [1]. Further works will be carried out to study the effect of defects and impurities on device performance.

4. Discussion

The solubility of Sn in GaAs has been discussed in the literature, particularly for melt-grown GaAs:Sn and liquid phase epitaxy (LPE) materials grown from a Ga-rich melt [35,36]. The Sn segregation coefficient, k_{Sn} , i.e. the ratio of Sn in the solid to Sn in the liquid, in LPE was a function of temperature and typical quite low on the order of $k_{\text{Sn}} \approx 10^{-4}$ at 700–800 °C [37–39]. Similar values are predicted, $k_{\text{Sn}} = 4 \times 10^{-3}$ [40], and seen for bulk grown GaAs however at much higher temperatures.

There have been estimates of the vapor phase segregation

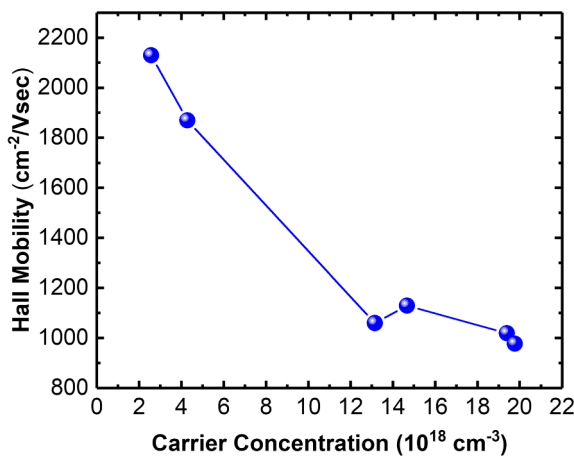


Fig. 5. Room temperature electron mobility, μ_e , as a function of carrier concentration.

coefficient as well for both molecular beam epitaxy (MBE) and MOVPE. Thermodynamics estimates for MBE and MOVPE indicate that the measured Sn concentrations are far in excess of those predicted and hence kinetic processes are seen to dominate the Sn-incorporation process at low growth temperatures [41–43]. The low segregation coefficients indicate that the Sn incorporation process occurs from an adsorbed layer or layers of Sn on the surface. Sn is initially rejected from the film until a steady-state surface layer is developed. This has been demonstrated in surface science and MBE growth studies [44,45] and is expected to be present in MOVPE.

The results of the present study, as well as the previously cited MBE studies, indicate that low growth temperatures allow for the incorporation of Sn well-above the thermodynamic solubility limit. In all cases, it is anticipated that there exists a monolayer or more of Sn on the surface which serves as a reservoir of Sn during growth. The kinetic models developed for the case of MBE rely on impurity trapping at the growth front by the growing film leading to Sn incorporation of Sn and preventing macroscopic accumulation on the surface. We expect similar mechanisms at these temperatures for MOVPE.

The thermodynamic analysis of the point defect structure of Sn-doped GaAs has been carried out by Hurlé [46,47]. The partial compensation of the Sn, as a simple donor, Sn_{Ga}^+ , has been attributed to the thermodynamically favored formation of an acceptor defect complex, $\text{Sn}_{\text{Ga}}\text{V}_{\text{Ga}}^-$, as well as unintentional carbon acceptors. The choice of specific compensating point defects also depends on the specific As activity at the growth front which is difficult to ascertain within the MOVPE system. The SIMS data also indicates the presence of carbon which are assumed to be acceptors.

5. Conclusions

A controlled electron concentration with a smooth morphology and controlled doping profile were successfully grown at low temperature (425 °C) using TESn by MOVPE, where a carrier concentration of $> 10^{19} \text{ cm}^{-3}$ has been achieved. This establishes TESn doping as an efficient doping platform for GaAs at low growth temperature potentially extendable to other materials systems requiring a low growth temperature.

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