

# Edge Termination for III-Nitride Vertical Power Devices Using Polarization Engineering

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**Abstract**—A method for edge termination utilizing polarization-induced charge for GaN vertical power devices is presented. The polarization edge termination is simulated on a GaN power diode and consists of a 5-nm-thick n-type AlGaN layer on top of a p-type GaN layer that is located at the periphery of the main p-n junction. The spontaneous and piezoelectric polarization present in III-nitrides result in fixed charges at the AlGaN/GaN heterointerface, and the p-GaN layer becomes depleted at this interface under reverse bias. Numerical simulations show that this AlGaN/GaN heterointerface can be engineered to control the depletion region under reverse bias to prevent localization of electric fields and premature avalanche breakdown. Nearly parallel-plate reverse breakdown performance can be achieved. In addition, a simple analytical model based on charge balancing predicts the performance of this edge termination method.

**Index Terms**—Aluminum gallium nitride (AlGaN), edge termination, field plate, gallium nitride (GaN), junction termination extension (JTE), polarization, power diode, power semiconductor devices, vertical devices.

## I. INTRODUCTION

WIDE bandgap semiconductor power devices are being developed as alternatives to conventional Si power devices and for new applications. Advantages of wide bandgap semiconductors include higher operating voltages, higher power densities, and faster switching speeds. SiC and gallium nitride (GaN) power devices have progressed to become commercially viable competitors to Si for some applications [1], [2]. In particular, GaN diodes with avalanche breakdown voltages up to 5 kV with resultant electric field strengths around 3.5 MV/cm have been achieved [3]–[7].

In order to realize maximum breakdown voltages, localization of the electric field must be prevented. This field localization results from crowding of the depletion layer under the reverse bias that is due to the finite size of the device, and it occurs at contact discontinuities or device edges. Creating edge

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termination structures can be difficult in GaN. For example, it is difficult to selectively create the doped layers although there has been some recent progress [8], [9]. The most successful edge terminations are formed by implantation-based methods to create junction termination extensions (JTEs), field rings, bilayer structures, and bevels [4]–[6], [10]–[12].

The III-nitrides have high piezoelectric and spontaneous polarization, and heterointerfaces can have large amounts of polarization fixed charge. This property is most successfully used in aluminum GaN (AlGaN) high electron mobility transistors to create highly conductive channels [13]. It is also possible that this polarization-induced charge could be used for edge termination similar to a field plate wherein the additional charge is used to expand the depletion region under high reverse bias [14]. By properly engineering the heterointerfaces, one can control the polarization sheet charge.

In this article, the design of an edge termination using the polarization-induced sheet charge is demonstrated for III-nitride power devices. An AlGaN layer is placed on the p-GaN layer at the periphery of the main p-n junction, and this creates a positive charge and depletion of the p-GaN layer at the heterointerface that spreads the electric field under reverse bias. This charge can be tuned with Al composition, thickness, and dopant concentration within the AlGaN layer. Numerical simulations show nearly parallel-plate reverse bias performance. In addition, a simple analytical model is presented, which can predict the simulation results.

## II. DESIGN AND SIMULATION

A 2-D cross-sectional schematic of the simulated device is shown in Fig. 1. The vertical p-n power junction consists of a 200-nm-thick p<sup>+</sup>-type layer, a 10-μm-thick n-type drift layer with a Si concentration [Si] of  $2 \times 10^{16}/\text{cm}^3$ , and a 2-μm-thick n<sup>+</sup>-type contact layer with a [Si] of  $10^{19}/\text{cm}^3$  on the bottom. Ohmic contacts are placed on either side with the top 20-μm-wide p-contact, only contacting the left-hand side. A nearly intrinsic i-GaN layer is added with a donor density of  $10^{15}/\text{cm}^3$  (lower donor concentrations work too), which simulates a deadening implant and creates a simple boundary condition at the edge of the device. The edge of the device could have been equivalently simulated as a mesa terminated with a dielectric.

The edge termination consists of a 5-nm-thick Al<sub>x</sub>Ga<sub>x-1</sub>N layer that is 70-μm-wide on top of the p<sup>+</sup>-GaN layer between the p-contact and the device edge. The Al<sub>x</sub>Ga<sub>x-1</sub>N edge termination layer is doped n-type with [Si] at  $10^{17}/\text{cm}^3$ .

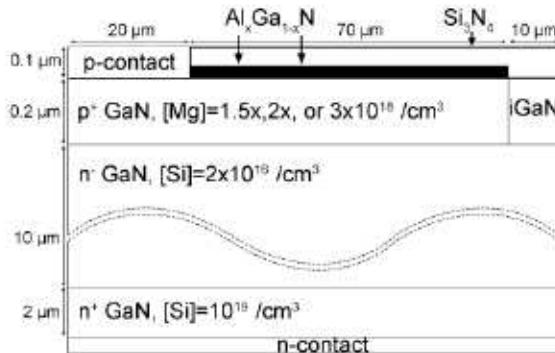


Fig. 1. Schematic cross section of the simulated GaN vertical p-n junction power diode. A highly doped p<sup>+</sup>-GaN layer and lightly doped n<sup>-</sup>-GaN drift form the main junction. A highly doped n<sup>+</sup> contact layer is at the bottom. A nearly intrinsic i-GaN layer is located in the right-hand side to isolate the edge of the device. The Al<sub>x</sub>Ga<sub>1-x</sub>N edge termination layer is the heavy black line at the top of the device between the p-contact and device edge.

In the simulation,  $x$  is varied from 0 to 0.8, and the [Mg] is varied from  $1.5 \times 10^{18}/\text{cm}^3$  to  $3 \times 10^{18}/\text{cm}^3$  to change the polarization sheet charge or depletion width in the p<sup>+</sup>-GaN layer. This p-type doping level is similar to GaN power diodes with record reverse breakdowns [3].

The device is simulated using Silvaco Atlas TCAD software building on previous reports [10], [11]. Best known values for spontaneous and piezoelectric polarization are taken from [15], and other relevant parameters are taken from [15] and [16]. The ionization coefficients were taken from experimental values from [17] and produce a critical electric field of 3.449 MV/cm for a  $2 \times 10^{16}/\text{cm}^3$  doped drift layer. A reverse bias sweep, where the cathode is grounded and reverse bias is applied to the anode, is used to study the effect on the electric field and breakdown voltage  $V_{br}$ . Breakdown occurs when the reverse current reaches 100  $\mu\text{A}$ . This threshold value for leakage was chosen as it was several orders of magnitude greater than the reverse leakage current.

### III. RESULTS AND DISCUSSION

In this polarization edge termination method, the Al<sub>x</sub>Ga<sub>1-x</sub>N/p<sup>+</sup>-GaN interface has a positive charge. A 2-D electron gas (2DEG) forms in the p<sup>+</sup>-GaN layer and partially screens this positive charge. However, under reverse bias, the 2DEG is fully depleted and, thus, allows the polarization charge to create a depletion region at the top of the p<sup>+</sup>-GaN. This changes the conductivity in the p<sup>+</sup>-GaN and spreads the electric field across the termination with the proper design. If designed properly, the diode should achieve the reverse breakdown performance of a parallel plate device. The parallel plate breakdown voltage  $V_{br-pp}$  is calculated assuming a uniform doping distribution in the drift layer and an asymmetrically doped junction given by

$$V_{br-pp} = \frac{\epsilon_s E_c}{2q N_d} \quad (1)$$

where  $E_c$  is the critical electric field,  $N_d$  is the doping of the drift layer,  $\epsilon_s$  is the permittivity, and  $q$  is the fundamental

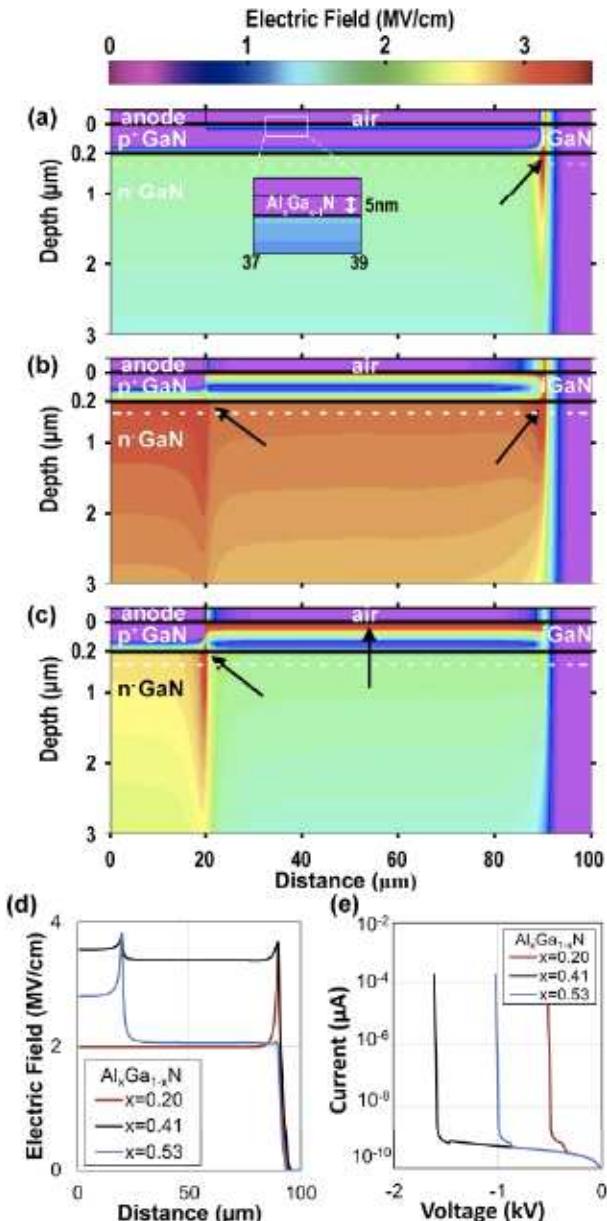


Fig. 2. 2-D electric field intensity plots of the power diode at breakdown with [Mg] =  $2 \times 10^{18}/\text{cm}^3$  in the p<sup>+</sup>-GaN layer and aluminum composition  $x$  of (a) 0.2, (b) 0.41, and (c) 0.53. At  $x = 0.2$ , electric field is localized (arrow) at the edge of the device and results in  $V_{br} = 520$  V. At  $x = 0.41$ , the electric field is spread optimally across the edge termination, resulting in the highest fields at the outside and p-contact edge (arrows) and results in a  $V_{br} = 1616$  V. At  $x = 0.53$ , the electric field is localized at the p-contact and is also too high at the AlGaN/GaN heterointerface, lowering  $V_{br} = 1020$  V. (d) 1-D electric field intensity near the junction at a position identified by the white dotted line in the 2-D plots. From the previous three cases, the flatness of the optimal 0.41 result demonstrates the optimized field spreading. (e) Reverse current versus voltage sweeps for the three different aluminum compositions.

charge. Using a critical electric field of 3.44 MV/cm results in a parallel-plate breakdown voltage of 1586 V.

Fig. 2 shows the 2-D plots of the electric field intensity with a different  $x$  within the Al<sub>x</sub>Ga<sub>1-x</sub>N edge termination layer and with a [Mg] of  $2 \times 10^{18}/\text{cm}^3$  in the p<sup>+</sup>-GaN layer. At  $x = 0.2$ , there is insufficient polarization charge and not enough depletion at the top of the p<sup>+</sup>-GaN to help spread

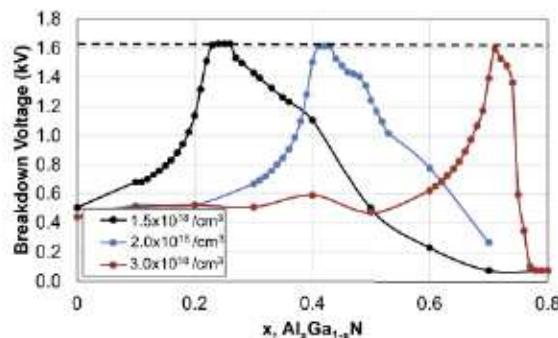


Fig. 3. Plot of the breakdown voltage versus Al composition,  $x$ , for  $[\text{Mg}] = 1.5 \times 10^{18}/\text{cm}^3, 2 \times 10^{18}/\text{cm}^3$ , and  $3 \times 10^{18}/\text{cm}^3$  in the  $\text{p}^+-\text{GaN}$  layer. The horizontal dotted line is the parallel-plate breakdown voltage.

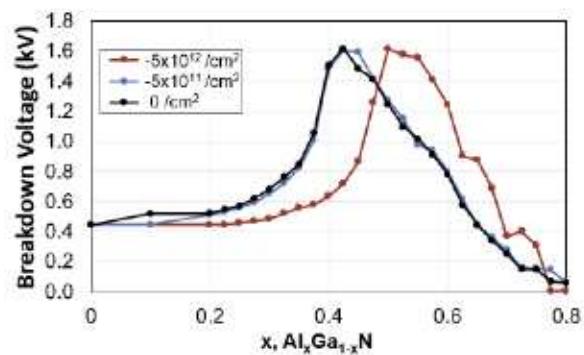


Fig. 4. Plot of the breakdown voltage versus Al composition,  $x$ , for  $[\text{Mg}] = 2 \times 10^{18}/\text{cm}^3$  and  $\text{Si}_3\text{N}_4/\text{Al}_x\text{Ga}_{1-x}\text{N}$  interface sheet charge densities of  $0/\text{cm}^2$ ,  $-5 \times 10^{11}/\text{cm}^2$ , and  $-5 \times 10^{12}/\text{cm}^2$ .

the electric field, as shown in Fig. 2(a). The electric field is localized at the edge of the device (as it would be without edge termination) and  $V_{br} = 520$  V. In Fig. 2(b),  $x = 0.41$ , and the  $\text{p}^+-\text{GaN}$  layer is depleted optimally by the polarization charge. The depletion width and electric field are spread uniformly across the edge termination length. For this  $x$ ,  $V_{br} = 1616$  V, and the diode has close to parallel-plate reverse breakdown performance. (Note that this  $V_{br}$  is only limited by the drift layer thickness to control computation time and would be better with thicker drift layers.) As the aluminum composition is increased to  $x = 0.53$  in Fig. 2(c), the polarization sheet charge further increases, and the edge of the depletion width is moved near the p-contact resulting in a large electric field and a lower  $V_{br} = 1020$  V. Also, the sheet charge is too large, resulting in too much depletion and a large electric field across the top of the  $\text{p}^+-\text{GaN}$  layer. With further increases in  $x$ , the electric field increases at the  $\text{AlGaN}/\text{GaN}$  heterointerface and ultimately presents an upper limit on the polarization sheet charge possible for these simulations at  $x \sim 0.78$ . The horizontal electric fields near the junction are shown in Fig. 2(d), and the current versus voltage under reverse bias are shown in Fig. 2(e) for the three cases.

When the doping or thickness in the  $\text{p}^+-\text{GaN}$  layer is increased, the optimal  $x$  will also increase to compensate for the increased charge required to expand the depletion width within the  $\text{p}^+-\text{GaN}$  layer. Conversely, as the doping in the  $\text{p}^+-\text{GaN}$  layer is reduced, the polarization charge required to balance the charge is reduced. This is demonstrated in Fig. 3 that shows the results of a series of simulations and plots of  $V_{br}$  versus  $x$  for  $[\text{Mg}] = 1.5 \times 10^{18}/\text{cm}^3, 2 \times 10^{18}/\text{cm}^3$ , and  $3 \times 10^{18}/\text{cm}^3$  in the  $\text{p}^+-\text{GaN}$ . There is an optimal  $x$  to properly balance the charge and achieve high  $V_{br}$ , and this optimum  $x$  shifts with  $[\text{Mg}]$ . For the chosen layer's thicknesses, compositions, and doping, the optimum point shifts to higher  $x$  as  $[\text{Mg}]$  is increased. The two lower  $[\text{Mg}]$  exhibit  $V_{br}$  that gradually decreases with  $x$  after peaking. At  $[\text{Mg}] = 3 \times 10^{18}/\text{cm}^3$ ,  $V_{br}$  decreases more rapidly with  $x$  after peaking due to the premature breakdown and large electric fields occurring at the top of the  $\text{p}^+-\text{GaN}$  layer for  $x \sim 0.78$ , as described earlier. No variations in  $R_{on-sp}$  are observed in the simulations. The simulation for the forward voltage sweep results in a consistent value of  $\sim 0.27 \text{ m}\Omega\text{-cm}^2$

that agrees closely with the theoretical value for the drift region resistance of  $\sim 0.26 \text{ m}\Omega\text{-cm}^2$ . The forward turn-on voltage is  $\sim 3$  V. With a  $10\text{-}\mu\text{m}$  drift layer thickness, the structure with the highest breakdown voltage has a figure of merit ( $V_{br}^2/R_{on-sp}$ ) of  $\sim 9.5 \text{ GW/cm}^2$ . It should also be noted that this edge termination also works for thicker and lower doped drift layers that operate in the higher breakdown voltages.

The simulations, thus far, do not include the potentially large negative-bound polarization charge at the air/ $\text{Al}_x\text{Ga}_{1-x}\text{N}$  interface and assumes that this interface is charge neutral. While this charge density can theoretically be as high as the polarization charge at the  $\text{Al}_x\text{Ga}_{1-x}\text{N}/\text{GaN}$  interface, experimental values typically are in the range of  $\sim 5.6 \times 10^{11}$ – $1.3 \times 10^{12}/\text{cm}^2$  for passivated interfaces [18]. The remaining charge is compensated by electronic surface states. To gauge the impact of the uncompensated charge, the air at the surface is replaced with  $\text{Si}_3\text{N}_4$ , forming a  $\text{Si}_3\text{N}_4/\text{Al}_x\text{Ga}_{1-x}\text{N}$  interface, and additional simulations are performed for interface charge densities of  $-1 \times 10^{11}/\text{cm}^2$  and  $-5 \times 10^{12}/\text{cm}^2$ . (Note higher interface charge densities do not converge in the simulation.) Fig. 4 shows the breakdown voltages versus composition for the different interface charges. For the simulation with interface charge densities of  $-1 \times 10^{11}/\text{cm}^2$ , the effect of the uncompensated charge is minimal. However, for charge densities of  $-5 \times 10^{12}/\text{cm}^2$ , which represents the upper bound of experimental measurements, the Al composition must be increased by 0.1 to reach the optimal breakdown voltage compared to the charge-neutral simulations. Proper knowledge of the  $\text{Si}_3\text{N}_4/\text{Al}_x\text{Ga}_{1-x}\text{N}$  charge will be necessary to experimentally realize this edge termination scheme. This surface charge could be controlled by using surface passivation techniques, as shown in GaN [18]–[21].

To understand the tradeoffs and guide the design, a simple predictive analytical model is constructed. This analytical model also helps in verifying the numerical simulations. Fig. 5(a) shows the band diagram at the breakdown of the top-most layers. The energy bands are bent over keV, and the p-GaN is in U shape. The p-GaN is bent down near the AlGaN layer due to the polarization interface charge at the AlGaN/p-GaN interface and bent down near the n-GaN drift layer because of the large reverse bias. There is a small portion in the

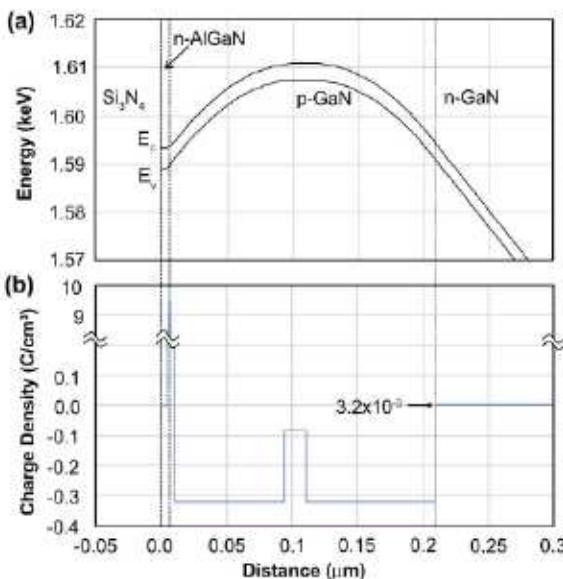


Fig. 5. Plot of (a) the energy bands (conduction  $E_c$  and valence  $E_v$ ), and (b) charge density of the top layers for the optimal case of  $x = 0.41$  at breakdown. The reverse bias causes severe band bending with the p-GaN bent into a U shape caused by the fixed polarization interface charge at the  $\text{AlGaN/p-GaN}$  interface and reverse bias at the  $\text{p-GaN/n-GaN}$  interface. The band bending causes depleted charge in the p-GaN (negative) and n-GaN drift layer (positive). The smaller negative charge in the center of the p-GaN is caused by the flat band. The charge density of the n-GaN is small ( $3.2 \times 10^{-3} \text{ C}/\text{cm}^3$ ) because of the large thickness of that layer.

middle of the p-GaN that is relatively flat. This band bending is manifested in the charge density given in Fig. 5(b). There are three regions of charge: the positive  $\text{AlGaN/p-GaN}$  interface charge, the negative charge in the p-GaN due to depleted acceptors, and the positive charge in the n-GaN drift layer due to depleted donors. The small variation in the middle of the p-GaN is due to the flat band and the U-shaped energy band.

The charge density plot helps in illustrating the total charge in the system to build a simple model. Assuming no free charge within the depletion regions under reverse bias (depletion approximation) and using Gauss's law, the sum of the sheet charge density  $\rho_s$  required to balance the depleted charges in the n-GaN drift, p-GaN, and AlGaN layers outside the p-contact area can be expressed as

$$\rho_s = -q[N_{d,\text{drift}}X_{\text{drift}} - N_a W_p + N_{d,\text{ET}}W_{\text{ET}}] \quad (2)$$

where  $N_{d,\text{drift}}$ ,  $N_a$ , and  $N_{d,\text{ET}}$  are the doping in the  $n^-$ -GaN drift,  $p^+$ -GaN layer, and the AlGaN edge termination layer, respectively.  $W_p$  and  $W_{\text{ET}}$  are the depletion widths of the  $p^+$ -GaN and AlGaN edge termination layer, and  $X_{\text{drift}}$  is the depletion width in the  $n^-$ -GaN drift layer all at the breakdown. The small rise in the middle of the p-GaN is ignored. In these simulations,  $X_{\text{drift}}$  is equal to the total drift thickness. The drift layer thickness was chosen to be equal to the depletion width for this nonpunchthrough design.

In the numerical and analytical model, the polarization charge at the  $\text{AlGaN/GaN}$  heterointerface  $P$  is given by [13]

$$P = -10^{-4} [0.0492x^2 + 0.0593x] \quad (3)$$

which includes the sum of the spontaneous and piezoelectric polarization charges. To achieve optimum breakdown voltage, the sheet charge density must equal to the polarization charge ( $\rho_s = P$ ) and can be achieved by adjusting  $x$  of the  $\text{Al}_x\text{Ga}_{1-x}\text{N}$ . Using this condition and (1) and (2), the optimum  $x$  for  $[\text{Mg}] = 1.5 \times 10^{18}/\text{cm}^3$ ,  $2 \times 10^{18}/\text{cm}^3$ , and  $3 \times 10^{18}/\text{cm}^3$  are  $x = 0.23$ ,  $0.41$ , and  $0.69$ , respectively. These are close to the peak breakdown values found by numerical simulations, as shown in Fig. 3, and this verifies this simple model and the simulations.

Other variations beyond AlGaN/GaN structures are possible for polarization edge termination. For example,  $\text{AlInN}$  could be used as an alternative providing more control of the piezoelectric charge with the ability to change from tensile to compressive strain. If the termination structure is required to be on n-type layers, such as the case of a Schottky diode with an n-GaN drift layer, then an InGaN edge termination layer could be used. This would create a negative charge on the top of the drift layer and extend the depletion region more like a traditional JTE. Another embodiment is to segment the layer to create field rings [14]. Finally, polarization edge terminations may be used as an edge termination for not only power diodes but also for other III-nitride-based power devices.

This edge termination is different from other forms because it is formed during epitaxial growth instead of during device processing. The growth of AlGaN on GaN is well known, but there are some developments needed to realize this edge termination. If the structure is grown in one step, then additional fabrication steps are required to remove the AlGaN from the p-contact area and, successfully, form an ohmic contact. Conversely, AlGaN could be selectively grown in a second growth step. The simulations show that any gap between the p-contact and edge termination layer results in nominal differences in the breakdown voltage.

#### IV. CONCLUSION

A method for edge termination utilizing polarization-induced charge is presented for GaN vertical power devices by numerical simulation. With the proper composition, thickness, and doping in the AlGaN edge termination and p-GaN layer, nearly parallel-plate reverse voltages can be achieved. A simple analytical model using charge balancing predicts the optimum edge termination layer conditions.

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