Zero Voltage Switching Switched-Tank Modular Converter for Data Center Application

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Abstract— Recently, Switched-tank converter (STC) using zero current switching (ZCS) for 48 V-12 V data center application has reached 98.55% efficiency. However, the Coss loss is not recycled and it increases with frequency and voltage. This paper describes a switched-tank modular (STM) converter with zero voltage switching (ZVS) for all devices over full load range which can eliminate Coss loss and further improve efficiency. Different from traditional STC, the STM is more modularized and much easier to be integrated into high voltage application. Besides, the STM converter has smaller clamping capacitance compared with large clamping capacitor in traditional STC. As a result, higher efficiency and power density can be achieved. In this paper, the ZVS operation for STM converter is discussed. Also, the efficiency and power loss comparison using GaN and Si devices is analyzed. The simulation and experiment results based on a Si prototype are presented.

Keywords—switched-tank modular converter, zero-voltage switching, dc-dc converter

I. INTRODUCTION

With the rapid development of the internet service such as cloud computing and big data, the electric consume for data center is more than 1.8% in 2014, and will raise to73 billion kWh by 2020[1]. Therefore, handling the increasing server computing capability and minimizing the energy usage of data center at the same time is a new trend. As a result, in order to shrink the electricity bill, efficiency improvement has been well studied these days. Thanks to the fast development of wide-bandgap device, it offers a new choice for the power electronics application. By utilizing the wide-bandgap device, the converter can be operating at a faster speed with smaller volume. For the better system efficiency, the 48 V intermediate bus architecture (IBA) has been widely adopted in modern data centers.

Recently, many works have been done to improve the 48 V-12 V IBA bus converter efficiency. Switched-tank converter (STC) [2][3] was first introduced in 2017 based on the switched capacitor concept. The STC with zero current switching (ZCS) operation reaches 98.55% efficiency and the Coss loss is the dominator at light load. It is more suitable for the low voltage application high current and not able to achieve full zero voltage switching (ZVS). In order to further improve the efficiency to 98.71%, the on-time control is discussed in [4]. Expect STC, in [5], cascaded resonant switched-capacitor converter (CRSCC) is proposed and can reach 98.9% high efficiency under ZCS operation. To further decrease power loss of CRSCC, [6] utilize the ZVS control for CRSCC and it achieves ZVS for all devices and increase the efficiency to 99%, but the switching frequency is only 100 kHz, which doesn't fully utilize the high-speed switching

capability of the wide band-gap devices. The comparison of STC and CRSCC is detailed analyzed in [7]. The Transformerless stacked active bridge (TSAB) converter [8] achieves partial ZVS but the peak efficiency is 98.6% at only 30 W. ZVS Switched Capacitor Converter (ZSC)that derived from the STC is proposed in [9] and it reaches the peak efficiency 99%. In this paper, a switched-tank modular (STM) converter is proposed and it can achieve ZVS on all services. By applying ZVS control, the Coss loss is recycled which is a big concern with frequency and voltage condition. The STM converter have several benefits such as only uses devices with voltage stress equal to the output voltage which is more modularized and much easier to be integrated into high voltage application. Also, the STM converter has smaller clamping capacitance compared with large clamping capacitor in traditional STC.

This paper contains three parts. Section II discuss the ZVS operation principles of the STM converter while the loss modeling and efficiency analysis based on Si and GaN device is presented in Section III. The Section IV shows the simulation and the experiment results. Finally, Section V concludes the paper.

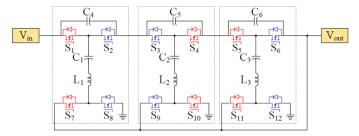


Fig. 1. Switched-tank modular converter topology

II. OPERATING PRINCOPLES OF THE STM CONVERTER

The schematic drawing of the four times conversion ratio (4x) switched-tanked modular converter is shown in Fig.1. In this topology, half bridges are used as the basic blocks, two half bridge blocks, a clamping capacitor and a LC tank makes up a module. A converter with N times conversion ratio will need N+1 module. When the device switching frequency equals to the frequency of the LC tank, $f_s = f_r = \frac{1}{2\pi\sqrt{LC}}$, the converter is working under the ZCS operation. Similar as switched-tank converter, the ZCS operation mode for the switched-tanked modular converter have two working states. The two complementary control signals turn on the red devices and blue device in sequence for ZCS operation.

Comparing to ZCS, ZVS can improve the light-load efficiency further by reducing the switching loss recycling the energy from MOSFET output capacitance. The realization of ZVS for switched-tanked modular converter requires that the switching frequency is higher than the resonance frequency, $f_s > f_r = \frac{1}{2\pi\sqrt{LC}}$. The corresponding control signals for ZVS operation are presented in Fig.2. Sr and Sb are two complementary control signals for the upper side red and blue devices respectively. Similarly, S_{rr} and S_{bb} are control signal for bottom side red and blue devices. There is a phase shift angle between S_r and S_{rr} , S_b and S_{bb} . To achieve ZVS turn on for the device, the requirement is that the body diode conducts before the device turns on. To meet this requirement, during the turn-on period, the device current should flow from source to drain.

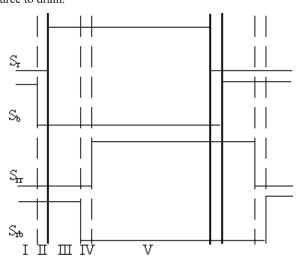


Fig. 2. Control Signal for ZVS Operation

There are two steady-state mode, i.e. Mode I and Mode V as shown in Fig 3(a)(e). In the mode I, all the blue devices are conducting, while in mode V all the red devices stay on. Between the two modes are three ZVS turn on transient modes. The ZVS turn-on process consists of Mode II~IV. Fig.3 (b)(c)(d) shows the three ZVS equivalent circuits. The grey dashed and solid line devices are off and on, respectively in the corresponding modes. While, colored dashed devices prepare for ZVS turn on. As shown in Fig.3 (b), at the start of the mode II, the S₂, S₃, S₆ turns off and the S₈, S₉, S₁₂ continue conducting. During this mode, the output capacitor of the S₁, S₄, S₅ are discharging to prepare for the ZVS turn on. In (c), at the beginning of the mode III, the S₁, S₄, S₅ ZVS turned on and the S_8 , S_9 , S_{12} continue conducting. In this mode, the inductor current changed directions. In mode IV, S₈, S₉, S₁₂ turns off at the start, S7, S10, S11 output capacitor is discharging. To achieve this ZVS operation, make sure that the inductors have enough energy to charge and discharge the device output capacitances.

Table I shows the comparation of different topologist which are developed from switched-capacitor concept. According to the control strategy, CRSCC and switched-tank modular converter can both operate at ZCS and ZVS operation however STC can only operate at ZCS and TSAV can operate at ZVS. Assume the low current stress device is the device that RMS current is $V_{\text{out}}/R_{\text{load}}.$ When considering about the device current stress, CRSCC have four high currents dress device used and the TSAB utilize two high current devices in the topology, however for the STC and switched-tank modular

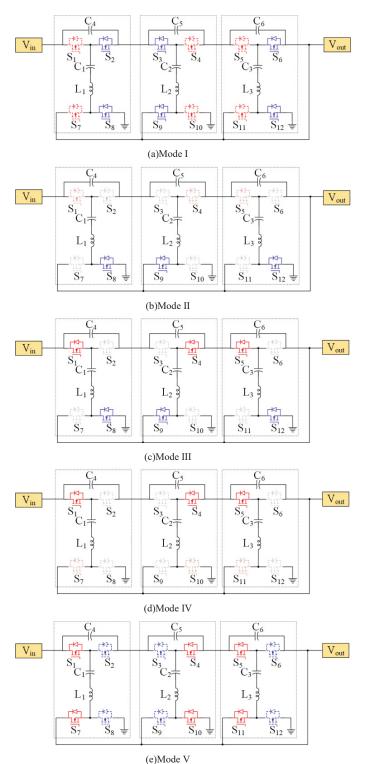


Fig. 3. ZVS Operation Equivalent Circuit

converter, all the devices are low current dress device. Similarly, by defining the device voltage stress equals V_{out} as the low voltage stress device. For the device voltage stress, CRSCC have four high voltage dress devices, TSAB and STC both have two high voltage stress devices, all the device in Switched-tank modular convert are low voltage stress device. Because of these features, the switched-tank modular converter has the best scalability

TABLE I. COMPARISON OF SEVERAL TOPOLOGIES WITH 4-TO-1 CONVERSION RATIO

Converter	Control Strategy	Device Current Stress	Device Voltage Stress	Scalability
CRSCC [5][6]	ZCS, ZVS	4 High current stress devices	4 High voltage stress devices	Average
		4 Low current stress devices	4 Low voltage stress devices	
STC [2][3]	ZCS	10 Low current stress devices	2High voltage stress devices	Average
			8 Low voltage stress devices	
TSAB [7]	ZVS	2 High current stress devices	2High voltage stress devices	Average
		6 Low current stress devices	6 Low voltage stress devices	
STM	ZCS, ZVS	12 Low current stress devices	12 Low voltage stress devices	Good

III. EFFIENCY ANALYSIS AND DEVICE COMPARASION

The power loss on the semiconductor device contains three parts: the gate driver loss (P_{gate}), the output capacitor loss(P_{coss}) and the conduction loss(P_{cond}). When the switched-tank

modular converter is working at ZVS mode, the P_{coss} is recycled. Thus, the total power loss on the semiconductor device P_{semi} is the sum of gate driver loss and conduction Loss ,as shown in (1)

$$P_{semi} = P_{gate} + P_{cond} \tag{1}$$

- Gate drive loss: The gate drive loss can be calculated using (2), where V_{gs} is the gate drive voltage, Q_g is total gate charge, and f_s is devices switching frequency.
- Conduction loss: The conduction loss can be calculated using (3), The drain-to-source on state resistance of one device is R_{ds(on)}. The root mean square (RMS) value of current flow through it.

$$P_{gate} = V_{gs}Q_g f_s \tag{2}$$

$$P_{cond} = i_{rms}^2 \times R_{ds(on)} \tag{3}$$

When working in the extreme case, the input voltage will be 60V, all the devices have the maximum voltage, for the 4 to 1 conversion ratio topology it is 15V. As a result, to build the converter, several types of switching devices which have 11.325V or 30V voltage rating are compared. For GaN devices, the EPC have 30V voltage rating MOSFET available. For Si device, Infineon optimos5 provide both 25V and 30V MOSFET.

TABLE II. PARAMETERS OF SWITCHING DEVICES USED IN ANALYSIS

Part#	Rating	V _{gs} (V)	Q _g (nC)	$R_{ds(on)}(m\Omega)$
BSZ031NE2LS5	25V 40A	5	6.5	3.1
BSZ013NE2LS5I	25V 40A	5	16	1.25
BSZ017NE2LS5I	25V40A	5	11	1.78
BSZ014NE2LS5IF	25V 40A	5	11.3	1.5
EPC2023	30V 90A	5	19	1.15

Since higher gate driver voltage can result in a lower MOSFET on-state resistance which can reduced conduction loss and cause higher gate driver Loss at the same time. For the GaN device, the 5 V gate driver voltage is the only choice, however, Infineon optimos5 Si device have wild range of gate

driver voltage. To make sure that the comparison is fair enough, assume the gate driver voltage for all devices are 5V. Besides to purely compared switching device Loss, the inductor loss, capacitor loss and the PCB loss are assumed the same when the converter is operating in the same condition. This analysis includes four Si MOSFETs and one eGaNFET that have either low $R_{\rm ds(on)}$ or $Q_{\rm g}$ among 25 and 30 V devices. Their key parameters used in the analysis can be found in Table II

Fig.4 shows the efficiency curve over all load range when switched-tank modular converter utilizing these five types of MOSFET. As shown in Fig.4 the BSZ031NE2LS5 has the highest peak efficiency at the light load. In the light load circumstances, the gate drive loss will be the dominator for the semiconductor power loss. Due to the small Qg, the gate drive loss for BSZ031NE2LS5 is the smallest. This cause the highest peak efficiency at light load. However, for the full load efficacy circumstances, the conduction loss becomes the dominator for the semiconductor device power loss due to the increase of the current flow though the device. Since the BSZ031NE2LS5 has a relatively large R_{ds(on)}, the efficiency at full load is lowest. For the EPC2023 and BSZ013NE2LS5I, the performance is nearly the same over all load range but BSZ013NE2LS5I has a higher peak efficiency

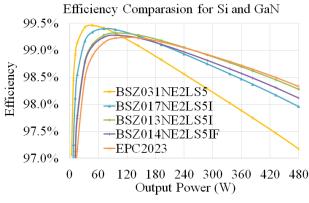


Fig. 4. Efficiency Comparasion on Different Device (Si and GaN)

Detailed breakdown comparison is shown in (b). Since the gate drive loss only related to $V_{\rm gs}$, switching frequency and $Q_{\rm g}$, these values are fixed for the fixed condition. Therefore, GaN devices have larger $Q_{\rm g}$ that gates driver lost is larger than Si devices under full load range. For the conduction loss, the GaN device have a slightly better performance for the conduction loss due to the smaller $R_{\rm ds(on)}$. Thus, during the light load, the conduction loss is not the dominator of the power loss and Si device have s less power loss which result

in a higher peak efficiency. Under the full load conduction, since the $R_{\text{ds(on)}}$ difference between Si devices and GaN devices is quite small, even though the conduction loss is the dominator, the efficiency is nearly the same. Base on all these consideration, the BSZ013NE2LS5I will be the most suitable device to use.

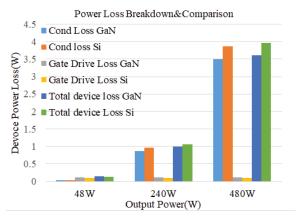


Fig. 5. Power Loss Breakdown Comparision between Si and GaN

IV. SIMULATION AND EXPERIMENTAL RESULT

Simulation studies of the switched-tank modular converters with PLECS is conducted. The simulation parameter is shown in table III.

TABLE III. PARAMETER USED IN SIMULATION

Items	Symbols	Values	
Switching Frequency	f_s	350kHz	
Input Voltage	V _{in}	48V	
Output Voltage	V _{out}	12V	
Resonant Inductance	$L_{\rm r}$	36nH	
Resonant Capacitance	$C_{\rm r}$	35μF	
Device On-state Resistance	R _{ds(on)}	1.25mΩ	
Device Output Capacitance	C_{oss}	1.8nF	

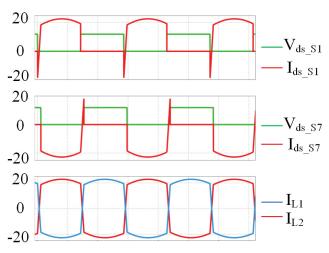


Fig. 6. Simulation Result at Full Load

In Fig.6, the first and second plot shows the voltage and current waveform for the top and bottom device respectively As shown in the plot, the voltage across the device reach zero

before the device starts conducting. Thus, the ZVS operation is achieved both for the top and bottom devices. The last plot shows the inductor current for L1 and L2 and it also reflects the realization of ZVS operation.

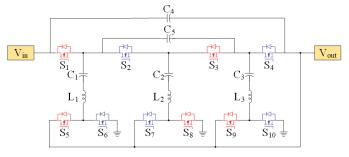


Fig. 7. SCRC Topology

Due to the relocation of the lab, the test bench is unable to set up for testing the proposed converter and verify the analysis result. However, a similar concept converter, Switched-capacitor Resonant Converter (SCRC), prototype is built, and the testing result can prove the analysis in some way. The topology is shown below in Fig.7. The change of this topology is using two high voltage stress devices to replace four low voltage stress devices. It has similar working performance when it is also operating at ZVS.

Since SCRC use less device than the switched-tank modular converter, the efficiency for the SCRC will be higher than switched-tank modular converter. By applying the same efficiency analysis model and set the parameters such as the switching frequency, the resonant inductance and resonant capacitance the same value, the relationship of the two converter is shown in Fig.8. Based on the analysis result, the peak efficiency for the SCRC is 99.44% and for the switched-tank modular converter is 99.35%

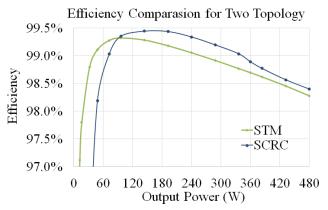


Fig. 8. Analysis Efficiency of Two Converters

The prototype is shown in Fig.9. The input voltage for the test is 48V and the output is 12V. The converter working with the switching frequency at 200kHz. Fig.10 show the experiment result for the inductor current under the light load 50W(a) and heavy load 450W(b). The inductor waveform shows that the converter achieves the ZVS.

The Fig,11 show the measured efficiency for the SCRC and compare it with other same function converters such as CRSCC, TSAB and ZSC. By inheriting the relationship from the analysis result, the efficiency of switched-tank converter can also be predicted, and it is also shown in the graph.

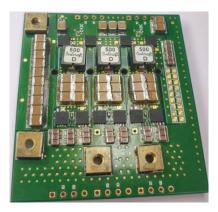
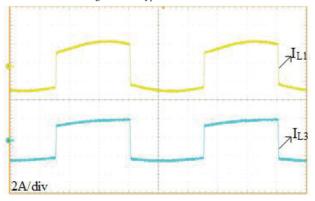
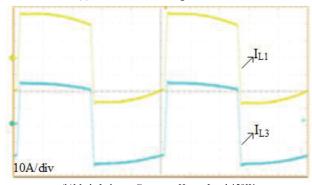


Fig. 9. Prototype of 4-to-1 SCRC



(a) Inductor Current at Light Load 50W



(b)Mode Inductor Current at Heavy Load 450W

Fig. 10. Experiment result for 4-to-1 SCRC

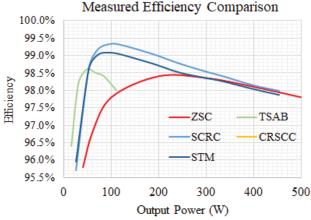
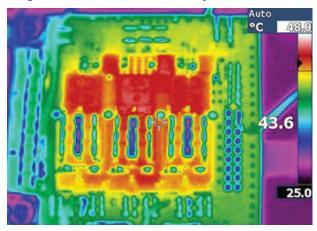


Fig. 11. Measured Efficency Comparison Between Different Topology

The thermal performance of the prototype at 450W is shown in Fig.13. The environment temperature is 25 °C and a fan is used for to cooling. The highest temperature showed is 48°C and it occurs near the high voltage stress device. The typical temperature of the board is 43.6°C and it is close to the highest temperature. This reflects that the high-efficiency design contribute the excellent thermal performance.



V. CONCLUSION

This paper presents the switched-tank modular converter derived from the traditional switched-tank converter. This converter not only inherent all the feature of the STC but also the ZVS operation can be achieved. By applying the ZVS control, the light load efficiency is greatly improved. Besides the increase of the peak efficiency, the switched-tank modular converter also has good scalability which allows the topology apply for the high voltage application. The experiment and analysis result shows the excellent performance of the switched-tank modular converter. It indicates that this topology can be adopted as a good option for the data center application where the high efficiency high density power delivery is a key factor

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