

Development of an Optimized Real-Time Radio Transient Imager for LWA-SV

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Abstract—In this paper, we describe our efforts towards the development of a real-time radio imaging correlator for the Long-Wavelength Array station in Sevilleta, New Mexico. We briefly discuss the direct-imaging algorithm and present the architecture of the GPU implementation. We describe the code-level modifications carried out for one of the modules in the algorithm that improves GPU-memory management and highlight the performance improvements achieved through it. We emphasize our ongoing efforts in tuning the overall run-time duration of the correlator which in turn is expected to increase the operating bandwidth in order to address the demands of wide-band capability for radio transient science.

I. INTRODUCTION

Modern radio astronomy is driven towards an upward trend in the size and computational capacity of the conventional FX correlators, primarily by the requirements of sensitivity, field-of-view and resolution. Next-generation radio telescopes (viz. HERA, SKA, PUMA etc.) will require large-N correlators with significantly higher computational and storage requirements. Digital signal processing back-ends are also expected to be capable of handling reasonably large bandwidths on the order of a few hundred MHz. One of the key scientific objectives of modern radio telescopes is to understand the physics of radio transient phenomena and to explore the space weather environment on other sun-like stars that may harbor exoplanets. In the above context, real-time imaging across a wide frequency band at very high temporal resolution is ideal for the detection and localization of Fast Radio Bursts (FRBs), Meteor Radio Afterglows (MRAs), Giant Pulse (GP) emission from pulsars and flares/CMEs from active/solar-type stars.

Modern Graphical Processing Units (GPUs), offering theoretical computational capacities on the order of few Tflop/s, are ideally suited for the growing high-performance requirements of radio astronomy. The current-generation radio telescopes have been successful in deploying the compute intensive task of cross-correlation and integration of multiple data streams from individual antennas of an aperture array on GPUs. Through visibility mapping from cross-correlation of antenna voltage pairs, conventional correlators spatially sample the sky thereby synthesizing the aperture [1]. However, for real-time

sky imaging the resulting visibility products must be subsequently gridded and Fourier transformed which compounds the computational intensity.

II. E-FIELD PARALLEL IMAGING CORRELATOR (EPIC)

In view of the above, we present a generic correlator implementation to reduce the computational requirements for real-time imaging in large-N ($N > 1000$) arrays with our E-field Parallel Imaging Correlator (EPIC : [2]). The EPIC is based on the Modular Optimal Frequency-Fourier (MOFF : [3]) mathematical formalism for direct Fourier imaging. The fundamental working principle of EPIC is to grid the electric fields from individual antennas and spatially Fourier transform it to the sky image, thus synthesizing the aperture on-the-fly (Fig. 1). In the process, EPIC introduces a significant reduction in computational scaling from $\mathcal{O}(n_a^2)$ to $\mathcal{O}(n_g \log_2 n_g)$ (where n_a is the number of antennas and n_g is the number of grid points) [4] in comparison to the traditional correlators.

EPIC is now implemented on a GPU-accelerated architecture and integrated with a python/C++ based high-performance streaming framework, Bifrost [5]. It was successfully deployed and tested on the Long Wavelength Array station [6] located at the Sevilleta National Wildlife Refuge (LWA-SV) in New Mexico, USA [7]. The LWA-SV is a compact array consisting of 256 dual-polarization active dipole antennas arranged in a pseudo-random pattern with an elliptical footprint operating in the frequency range of 10-88 MHz. The real-time digital signal flow into EPIC is described in the block diagram representation in Fig. 1. The GPU implementation of EPIC is composed of multiple kernels/modules that perform various functions, viz. voltage gridding, spatial Fourier transform, cross-correlation in the image plane, making use of some of the standard signal processing blocks of Bifrost [5] as highlighted in Fig. 1.

III. CODE OPTIMIZATION

The instantaneous bandwidth achieved with the initial deployment of EPIC on LWA-SV is limited to ≈ 400 kHz per GPU. We seek to optimize the GPU-code of the correlator for better performance in order to increase the bandwidth processable per device in real-time. We decided to begin with the optimization of the *romein* voltage gridding module,

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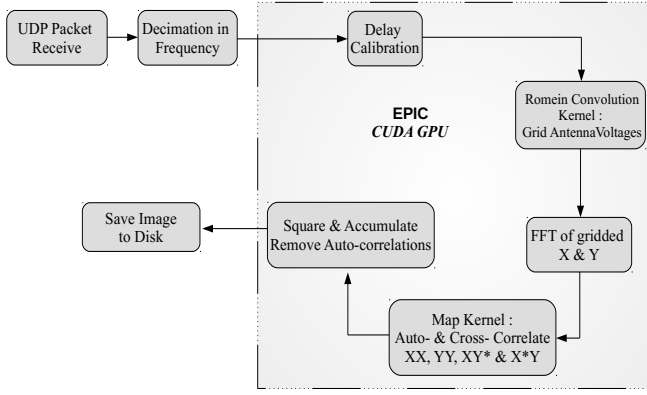


Fig. 1. GPU-implementation of the EPIC architecture in Bifrost for LWA-SV

which is one of the critical blocks of the EPIC, that is based on a GPU-accelerated convolution algorithm [8] written in C++/CUDA. In the *romein* module, the delay corrected frequency domain signals are convolved with an antenna illumination pattern described as a user-defined convolution kernel and then gridded with a spacing of $< \lambda/2$ on to a 2-D grid [7].

It was observed that the configuration of CUDA threads in the *romein* kernel implementation, and subsequently the memory access pattern, required modifications to improve the kernel performance. Optimizing memory accesses has a huge effect on GPU code efficiency, in the original implementation only one CUDA thread addressed all antennas of the array and handled multiple load/store operations directly from/to the global memory of the GPU. This was modified by re-configuring the CUDA thread-block description to include all antennas as part of one of the thread-block dimension. With this modification one CUDA thread is now able to grid one antenna and the shared memory (which is relatively faster) with increased memory coalescence is now used for load/store operations by the threads. The improved performance can be clearly noticed in Fig. 2 which shows a comparison of the run-time duration for the modified and original versions of the *romein* gridding kernel for different grid-size dimensions. Also, with better memory management the modified gridding module is now able to grid twice the grid-size dimension than earlier.

DISCUSSION

Code-level modifications and efficient GPU-memory management can significantly improve kernel performances in terms of run-time duration. We are currently working to optimize the other modules of the pipeline to achieve similar improvements. This is expected to reduce the overall run-time duration of the correlator that constrains the bandwidth that the correlator can process in real-time. We aim to achieve a reasonable bandwidth of ≈ 5 -10 MHz per GPU device in order to deploy a commensal transient imager system for the LWA-SV. We also plan to integrate additional modules to the EPIC

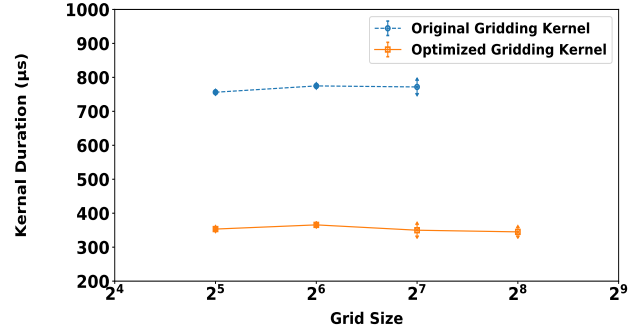


Fig. 2. Comparison of the kernel run-time duration vs Grid-size Dimension: original (blue) and modified (orange) Romein Gridding kernel

architecture for the purpose of RFI-mitigation and transient detection to bolster the science capabilities of the commensal system.

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