# A 27–46-GHz Low-Noise Amplifier With Dual-Resonant Input Matching and a Transformer-Based Broadband Output Network

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Abstract—This letter presents a 27-46-GHz low-noise amplifier (LNA) in a 45-nm CMOS silicon-on-insulator (SOI) process. Two circuit techniques are employed to enhance the LNA bandwidth. First, the intrinsic gate-to-drain parasitic capacitance of the input transistor and the frequency-dependent behavior of the first-stage load impedance are explored to realize dual resonances for  $S_{11}$ , thus extending the input matching bandwidth. Second, a network synthesis methodology is presented to convert a canonical second-order bandpass filter to a transformer-based output network, which realizes broadband power gain while occupying only one inductor footprint. In the measurements, the LNA 3-dB gain bandwidth is from 25.5 to 50 GHz with a peak gain of 21.2 dB at 37.8 GHz. The effective bandwidth of the LNA is limited by the 10-dB return loss bandwidth, which is from 27 to 46 GHz. The minimum noise figure (NF) is 2.4 dB at 27.8 GHz, and the NF remains <4.2 dB within the effective bandwidth. The measured IIP3 is -11.0 dBm at 38 GHz with 25.5-mW dc power consumption.

Index Terms—5G, broadband, CMOS, input matching, low-noise amplifier (LNA), millimeter-wave (mmWave), transformer.

#### I. Introduction

THERE is a growing interest in exploring wideband transceivers (TRX) to simultaneously cover multiple millimeter-wave (mmWave) 5G bands around 28, 39, and 42 GHz [1]–[6]. Compared to narrowband implementations, broadband 5G TRX can enable unique application scenarios, including interband carrier aggregation, international roaming, and agile frequency hopping. In addition to high-speed wireless communication, emerging wireless sensing applications, such as 3-D mmWave imaging and radar, also favor broadband TRX frontends to improve the sensing spatial resolution.

One key challenge in designing broadband TRX is to extend the bandwidth of the low-noise amplifier (LNA) beyond conventional narrowband implementations. In this work, we aim to enhance the LNA bandwidth through two circuit innovations [see Fig. 1(a)]. First, we make use of the intrinsic gate-to-drain parasitic capacitance of the input transistor  $C_{\rm gd1}$  and the frequency-dependent behavior of the first-stage load impedance  $Z_{\rm L1}$  to realize dual resonances for the input reflection coefficient ( $S_{\rm 11}$ ), thus broadening the input

Manuscript received February 6, 2021; accepted February 8, 2021. Date of publication February 15, 2021; date of current version June 7, 2021. This work was supported in part by the National Science Foundation under Grant CNS-1956297. (Corresponding author: Yaolong Hu.)

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This article was presented at the IEEE MTT-S International Microwave Symposium (IMS 2021), Atlanta, GA, USA, June 6–11, 2021.

Color versions of one or more figures in this letter are available at https://doi.org/10.1109/LMWC.2021.3059592.

Digital Object Identifier 10.1109/LMWC.2021.3059592

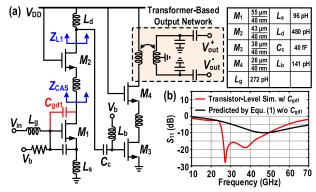


Fig. 1. (a) Schematic of the proposed broadband LNA. (b) Simulated dual-resonant input matching, including  $C_{\rm gd1}$ , and predicted input matching based on (1) when neglecting  $C_{\rm gd1}$ .



Fig. 2. Equivalent circuit of the input matching network, including  $C_{\rm gd1}$ .

matching bandwidth [see Fig. 1(b)]. Second, we propose a network synthesis methodology that can miniaturize a canonical second-order bandpass filter into a single on-chip transformer footprint, serving as the broadband output network. It naturally absorbs the transformer nonideal magnetic coupling, finite winding inductances, and parasitic capacitances while achieving a uniform transimpedance gain across a wide frequency range.

This letter is organized as follows. Section II elaborates on the design details of the dual-resonant input matching and transformer-based broadband output network. Section III presents the measurement results. Section IV concludes this letter with a performance comparison table.

## II. BROADBAND LNA IMPLEMENTATION

# A. Dual-Resonant Input Matching

The most frequently used mmWave LNA topology is the common source with inductive degeneration [7]–[15]. Neglecting the gate-to-drain parasitic capacitance  $C_{\rm gd}$  of the input transistor, the input impedance is derived as

$$Z_{\rm in}(s) = \frac{1}{sC_{\rm gs}} + (L_{\rm g} + L_{\rm s})s + \frac{g_{\rm m}L_{\rm s}}{C_{\rm gs}}$$
 (1)

where  $L_{\rm g}$  is the series gate inductance,  $L_{\rm s}$  is the degeneration inductance, and  $C_{\rm gs}$  is the gate-to-source capacitance [16].

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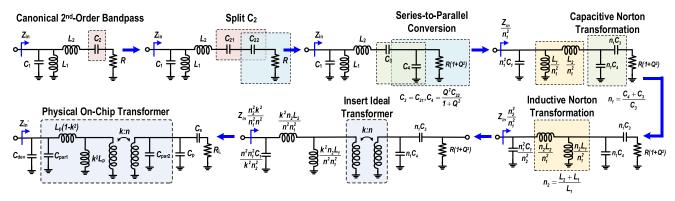


Fig. 3. Synthesis procedure to miniaturize a canonical second-order bandpass filter into a single-transformer footprint for the broadband output network.

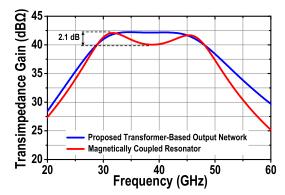


Fig. 4. Transimpedance gain comparison between the proposed transformer-based second-order bandpass output network and a carefully designed magnetically coupled resonator with similar 3-dB bandwidth.

As shown in (1), the input impedance consists of a series LC resonance and a frequency-independent real part. As such, it can only achieve a single  $S_{11}$  resonance, resulting in limited input matching bandwidth [see Fig. 1(b)]. One may improve the input matching bandwidth by employing a high-order matching network. However, it usually involves multiple inductors at the LNA input, resulting in a compromised noise figure (NF).

Although neglecting  $C_{\rm gd}$  has little effect on the input matching condition for low-gigahertz LNAs, the input impedance predicted by (1) certainly deviates from transistor-level simulations at mmWave, as shown in Fig. 1(b). To address this issue, we rederive the equivalent circuit of the input matching network, including  $C_{\rm gdl}$  (see Fig. 2). The equivalent circuit consists of three parallel paths in series with  $L_{\rm g}$ . The first path is the same as in the conventional common source with inductive degeneration topology. The second path is a scaled version of the first path with a coefficient of  $\alpha$ . The expression of  $\alpha$  is shown in Fig. 2, which is a function of the transistor intrinsic parameters  $(g_{m1}, C_{gs1}, \text{ and } C_{gd1})$  and the impedance looking into the upper cascode node  $Z_{CAS}$ . The third path models the feedforward current through  $C_{\rm gd1}$ , which remains a higher impedance than the other two within the frequency of interest. We neglect the third path in our analysis and consolidate the first two paths, as shown in Fig. 2.

The key to achieve broadband input matching is to explore the frequency-dependent behavior of  $Z_{L1}$ ,  $Z_{CAS}$ , and  $\alpha$ . A simplified yet intuitive design procedure to synthesize dual-resonant  $S_{11}$  at two desired frequencies  $f_1$  and  $f_2$  is summarized as follows. First, the size and biasing of the

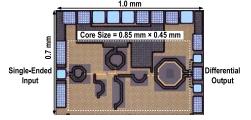


Fig. 5. LNA micrograph.

input transistor are chosen based on the dc power budget and NF<sub>min</sub> versus biasing simulation. Therefore, the intrinsic parameters of the input transistor, i.e.,  $g_{m1}$ ,  $C_{gs1}$ , and  $C_{gd1}$ are determined. Next, we assume that the first-stage load impedance  $Z_{L1}$  has a single resonance around  $f_1$ . The purpose of this assumption is to differentiate the value of  $Z_{CAS}$  at the two  $S_{11}$  resonances, i.e., set  $Z_{CAS}$  as a high impedance at  $f_1$ but a low impedance at  $f_2$ , allowing us to optimize the two  $S_{11}$ resonances sequentially. Beginning with the high-frequency resonance at  $f_2$ , as  $Z_{CAS}$  is a low impedance and much smaller than  $1/2\pi f_2 C_{\rm gd1}$ ,  $\alpha$  becomes purely real. The values of  $L_{\rm g}$ ,  $L_{\rm s}$ , and  $g_{m2}$  can then be determined from the equivalent circuit in Fig. 2 by setting series LC resonance at  $f_2$  and the real part of  $Z_{\rm in}$  close to 50  $\Omega$ . The only undetermined circuit parameters for the first stage are the passive elements in the load network, i.e.,  $L_{\rm d}$ ,  $C_{\rm c}$ , and  $L_{\rm b}$  in Fig. 1. Their values can be derived based on the conditions to achieve  $Z_{L1}$  resonance around  $f_1$ and close to 50  $\Omega$  for the real part of  $Z_{in}$ .

Following the procedure described above, the simulated  $f_1 = 27.1$  GHz and the simulated  $f_2 = 39.5$  GHz, which is very close to our targeted values of 27 and 41 GHz. Although our proof-of-concept implementation targets at the 28 GHz/39 GHz/42 GHz 5G bands, the theoretical framework to achieve dual-resonant  $S_{11}$  presented in this section can be readily extended to mmWave LNA designs at other frequency bands.

## B. Transformer-Based Broadband Output Network

In addition to dual-resonant input matching, the output load network also plays an essential role in achieving a broadband LNA gain. As the cascode second stage can be modeled as a current source with a high output impedance, the design goal of the output network is to deliver a uniform transimpedance gain within the frequency of interest while absorbing the parasitic capacitance of the second stage.

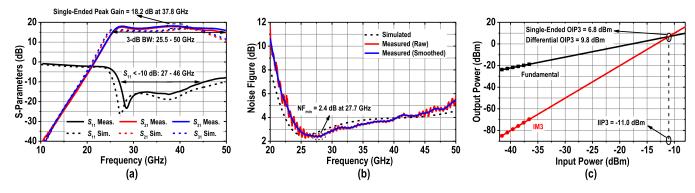


Fig. 6. (a) Measured and simulated S-parameters. (b) Measured and simulated NF. (c) Measured IIP3 and OIP3 at 38 GHz.

Starting with a canonical second-order bandpass filter, the output network synthesis procedure is summarized in Fig. 3. First, we split the capacitor  $C_2$  into two series capacitors  $C_{21}$  and  $C_{22}$  and perform a series-to-parallel conversion for  $C_{22}$  and the load resistor R. Note that a low quality factor  $(Q=1/\omega C_{22}R)$  is desired to broaden the bandwidth of the series-to-parallel conversation. Next, we apply two Norton transformations to the series-shunt capacitors and shunt-series inductors. The two transformation ratios  $n_1$  and  $n_2$  are highlighted in Fig. 3. Finally, we insert an ideal transformer with a turn ratio of k:n into the network and merge it with the series and shunt inductors and the shunt capacitors as a physical on-chip transformer.

The above network synthesis methodology naturally utilizes the transformer nonideal magnetic coupling, finite winding inductances, and parasitic capacitances to achieve second-order bandpass filtering while occupying only a single inductor footprint [17], [18]. It also serves as an output balun, making the LNA easier to be employed in an mmWave RX chain, where the following building blocks, such as phase shifter, variable-gain amplifier, and mixer, are mostly designed in a differential manner. As the proposed network is derived from a canonical second-order bandpass filter, its bandwidth and gain flatness outperform commonly used magnetically coupled resonators [19], as illustrated in Fig. 4.

## III. MEASUREMENT RESULTS

The broadband LNA is implemented in a 45-nm CMOS silicon-on-insulator (SOI) process (see Fig. 5). The supply voltage is 1.3 V with a dc current of 19.6 mA, resulting in 25.5-mW dc power consumption.

Fig. 6 summarizes the small-signal, NF, and linearity measurement results. The effective LNA bandwidth is defined as the intersection of the 3-dB gain bandwidth and 10-dB return loss bandwidth [20], which is from 27 to 46 GHz. The single-ended peak gain is 18.2 dB at 37.8 GHz, resulting in a differential peak gain of 21.8 dB. The gain ripple within the effective bandwidth is 1.2 dB. The measured common-mode rejection ratio (CMRR) is >24 dB, which indicates that the LNA differential outputs are well balanced. The measured common-mode and differential-mode stability factors (K) are greater than 1. For the NF measurements, the minimum NF is 2.4 dB at 27.7 GHz, and the NF remains <4.2 dB within the effective bandwidth [see Fig. 6(b)]. For the IIP3 measurement, two tones at 38 and 38.1 GHz are applied to the LNA input

TABLE I
PERFORMANCE SUMMARY AND COMPARISON

	JSSC 2020 [20]		RFIC 2019 [21]		GSMM 2018 [22]	IMS 2018 [23]	This Work
BW <sub>eff</sub> * (GHz)	22-32	22-32	24-29 <sup>†</sup>	37-42†	27-47.5 <sup>†</sup>	26-28 <sup>†</sup>	27-46
3-dB BW (GHz)	19-36	20-36	24-29 <sup>†</sup>	37-42†	24-47.5	14-31	25.5-50
Peak Gain (dB)	21.5	17.9	19.1†	23	20	14	21.2
NF (dB)	1.7-2.2	2.1-2.9	3.1-3.7		4.2-5.5	1.3-1.6	2.4-4.2
IIP3 (dBm)	-13.4	-14.4	-13.2	-19	<b>-</b> 9.4 <sup>††</sup>	+4†	-11
P <sub>dc</sub> (mW)	17.3	5.6	20.5		58	15	25.5
Core Size (mm²)	0.05		0.22		0.2	0.3	0.38
FoM**	24.7	30.8	3.8	1.1	7.0	178.3	26.1
Tech.	22-nm FDSOI		22-nm FDSOI		45-nm RFSOI	45-nm RFSOI	45-nm RFSOI

<sup>\*</sup> Intersection of 3-dB gain BW and 10-dB return loss BW.

with a power level from -41.6 to -36.6 dBm. The measured IIP3 is -11.0 dBm, and the differential OIP3 is 9.8 dBm [see Fig. 6(c)].

#### IV. CONCLUSION

This letter presents a 27–46 GHz broadband LNA design. Leveraging the intrinsic gate-to-drain parasitic capacitance of the input transistor and the frequency-dependent behavior of the first-stage load impedance, we realize dual resonances for  $S_{11}$ , hence extending the input matching bandwidth. We also develop a network synthesis flow that converts a canonical second-order bandpass filter to a transformer-based output network to extend the 3-dB gain bandwidth and guarantee the gain flatness. Table I summarizes our LNA performance and compares it with state-of-the-art LNAs at a similar operating frequency. Our design achieves the highest 3-dB gain bandwidth (25.5–50 GHz) and a very competitive Figure-of-Merit.

## ACKNOWLEDGMENT

The authors would like to thank GlobalFoundries for chip fabrication and members of the Rice Integrated Systems and Electromagnetics (RISE) Lab for helpful technical discussions.

 $<sup>^{\</sup>dagger}$  Graphically estimated.  $\,^{\dagger\dagger}$  Estimated using  $\mathrm{IP}_{1\mathrm{dB}} + 9.6\mathrm{dB}.$ 

<sup>\*\*</sup> FoM =  $\frac{10^{3} \times Gain[\frac{V}{V}] \times BW_{\text{eff}}[GHz] \times IIP3[mW]}{P_{\text{dc}}[mW] \times (NF[linear] - 1) \times f_{\text{c}}[GHz]} \cdot f_{\text{c}} \text{ is the geometric mean.}$ 

Peak gain and NF<sub>min</sub> are taken in FoM calculation.

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