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A Hole Transport Bilayer for Efficient and Stable Inverted Perovskite Solar Cells

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ABSTRACT We demonstrate that a PTAA/CuI bilayer as a hole transport layer (bilayer HTL) leads to efficient and stable inverted, i.e., p-i-n, perovskite solar cells (PSCs). We observed that devices with a single PTAA interlayer have an average power conversion efficiency (PCE) of 18.1%. In contrast, devices with a bilayer show an enhanced average PCE of 19.4%, with a maximum PCE of 20.34%. The XRD and AFM studies show an improved crystallinity and larger grain size of perovskite films on the CuI surface. Data from ultraviolet photoelectron spectroscopy and impedance spectroscopy suggest that devices with bilayer HTL have increased built-in potential within the device with an enhanced upward band bending at the CuI interface. Compared to devices with a single CuI interlayer, the presence of PTAA in the bilayer-based devices leads to significant suppression of current hysteresis and stable current output at the maximum power point. The bilayer-based devices demonstrate remarkable long-term stability in an inert atmosphere.

1. Introduction

The photovoltaic community is excited about metal halide perovskites because their optoelectronic properties and solution-based fabrication methods are most suited to realize efficient thin-film solar cells at low manufacturing cost. After the first reports of perovskite solar cells (PSC),¹⁻³ power conversion efficiencies (PCEs) of PSCs have steadily increased from ~3% to ~25% in single-junction devices in a short time.⁴ Development of fabrication methods,^{5,6} compositional engineering,⁷ passivations of surface defects,⁸ engineering of advanced device structures^{9,10} have been among the significant steps in improving the PCE towards the theoretical Shockley-Queisser limit of 33%,¹¹ and in prolonging the long-term stability of PSCs.

State-of-the-art PSCs with record PCEs have a regular n-i-p architecture.¹²⁻¹⁴ PSCs with the inverted p-i-n architecture have lower PCEs,^{17,18} but they have key features that are attractive from a large-scale manufacturing point-of-view. We can fabricate p-i-n devices without multiple high-temperature processing steps.^{15,16} Therefore, we can use flexible substrates or incorporate tandem architectures via layer-by-layer fabrication. The lower PCE's in p-i-n devices has been attributed primarily to defects in these devices that assist recombination. Thus, the research in this area is focused on passivating these defects using organic ligands.

An alternate approach would be to use hole transport layers that enable nucleation and growth of perovskite films with lower defect density to minimize charge recombination and have optimum frontier orbital energy levels to maximize charge extraction. In early p-i-n PSCs, poly(3,4-ethylenedioxythiophene) polystyrene-sulfonate (PEDOT:PSS) was used as the HTL.^{21,22} Huang and co-workers demonstrated that devices with poly[bis(4-phenyl)(2,4,6-trimethylphenyl)amine] (PTAA) as HTL performed better and attributed the improved performance to an improved morphology of perovskite films and favorable

energy levels that reduce the bulk and interfacial charge recombination.²³ Yet, studies also show that a large density of deep trap states is located at the PTAA/perovskite interface, limiting the efficiency of solar cells.²⁴ Cuprous iodide (CuI) is another HTL that has been used as top HTL in n-i-p devices and as bottom HTL in p-i-n devices.^{25,26} CuI is inexpensive, solution processible, has high electrical conductivity, and good chemical stability making it suitable for PSCs. As a wide-band gap semiconductor, CuI possesses good optical transparency and potentially suitable energy levels for charge/exciton blocking.²⁶ However, devices with CuI as an HTL have not been demonstrated to have high efficiency and long-term stability.

We reasoned that if we use multi-layer HTLs instead of a single HTL, we can take advantage of the constituent layers' complementary properties.²⁸ Many electron transport layers (ETL) in the state-of-the-art devices are composed of two or more layers.²⁷ Multi-layer charge transport layers allow us to choose HTL pairs with appropriate work functions that set up an energy level cascade and appropriate surface energy to nucleate perovskite layer. Multiple layers can also help passivate surface defects, block charge extraction at wrong electrodes, and create physical barriers for ionic and moisture diffusion.²⁹⁻³³ Herein, we report highly efficient and stable p-i-n perovskite solar cells with a bilayer HTL using PTAA and CuI. We show that bilayer devices outperform devices with single HTLs of PTAA, CuI, or PEDOT:PSS HTLs. Using X-ray diffraction (XRD) and atomic force microscopy (AFM), we show that perovskite films on CuI surface have higher crystallinity and larger grain size in both the single-layer CuI- and bilayer PTAA/CuI-based HTL devices. The ultraviolet photoelectron spectroscopy (UPS) and impedance spectroscopy (IS) with Mott-Schottky analysis indicate an increased built-in potential in the device and an enhanced upward band bending in the perovskite film at the interface with CuI. They originate from the higher work function of CuI. Overall, these properties are expected to increase the PCE of devices, particularly open-circuit voltage (V_{oc}) and fill factor

(*FF*), in agreement with our experimental observations. Additionally, We observed that the hysteresis in current density (*J*)-voltage (*V*) curves in devices with single CuI HTL is significantly suppressed in bilayer HTL-based devices. We speculate that iodide migration, mainly from CuI top layer, into PTAA bottom layer leads to removal of the hysteresis in device operation, to the enhancement of p-type doping of CuI, and to p-type doping of PTAA. Hence, a high PCE and long-term stability originate from each component's properties in the bilayer HTL and a synergistic effect of their interaction.

2. Experimental Section

Materials. The ITO-coated glass substrates (15 ohms sq⁻¹) were obtained from Kintec Company. PEDOT:PSS solution (AI 4083) was purchased from Ossila. Methylammonium iodide (MAI) and formamidinium iodide (FAI) were purchased from Dyesol. PTAA, CuI, and PbI₂, phenyl-C₆₁-butyric acid methyl ester (PC₆₁BM), as well as anhydrous toluene, acetonitrile, gamma-butyrolactone (GBL), chlorobenzene, 2,2,2-trifluoroethanol (TFE), dimethyl sulfoxide (DMSO) were purchased from Sigma-Aldrich. All materials were used without further purification.

Device fabrication. ITO-coated glass substrates were cleaned by ultrasonic treatment in detergent, water, acetone, and isopropyl alcohol for 10 min each. HTL solutions were prepared by dissolving PTAA in toluene (1.5 mg ml⁻¹) and CuI in acetonitrile (15 mg/ml). The as-prepared PTAA and CuI solutions were then spin coated atop of ITO-coated glass substrates at 3500 rpm for 40 s and 3000 rpm for 60 s, respectively. PTAA and CuI samples were annealed at 100 °C for 10 mins, while PEDOT:PSS was annealed at 150 °C for 30 mins. In the post-annealing step, PTAA HTL was treated with ozone for 40 s. For PTAA/CuI bilayer, CuI was spin coated atop of ozone-treated PTAA at 3500 rpm and subsequently annealed at 100 °C for 10 min. A 1.4 M precursor solution of MAI/FAI (0.95:0.05) and PbI₂ (1:1) in GBL/DMSO (v/v 7:3) was prepared and stirred overnight at 60 °C. The perovskite solution was then spin coated on the HTL by a two-step spin-coating procedure: 1500 rpm for 20 s and 2000 rpm for 60 s. During the second step, chlorobenzene (antisolvent) was dripped onto the substrate after 20 s. As-cast films were left at 65 °C for 1 min, followed by thermal annealing at 100 °C for 10 min inside the N₂-filled glovebox (<1 ppm of O₂, <1 ppm of H₂O). A solution of PC₆₁BM (15 mg ml⁻¹) in chlorobenzene was spin coated atop the perovskite layer at 1000 rpm for 60 s. Then, C₆₀-N in TFE (3 mg ml⁻¹) was spin coated at 4000 rpm to give a 15 nm-thick film. Finally, 100 nm Ag metal (area 5.2 mm²) was thermally deposited onto the active layer under a high vacuum (2 x 10⁻⁶ mbar).

Device characterization. The *J-V* studies were performed under AM1.5G (100 mW cm⁻²) irradiation using Newport SOL2A 300 W solar simulator. For each configuration, 4 substrates were fabricated in total in 3 different batches. The devices were kept under illumination for 2 minutes, and multiple *J-V* scans were obtained from -0.2 to 1.2 V (forward bias) or 1.2 V to -0.2 V (reverse bias) at a scan rate of 0.14 V s⁻¹. The *J-V* curve was recorded after the *J*_{SC} was stabilized. The light intensity was adjusted using NREL-calibrated Si solar cell with a KG-5 filter. The average device metrics were calculated based on 7 devices for PTAA, CuI and PEDOT:PSS, and 8 devices

for PTAA/CuI HTLs. Agilent 4294A Precision Impedance Analyzer was used for impedance spectroscopy. A 50 mV AC amplitude and 100 kHz frequency were used for *C-V* measurements for Mott-Schottky analysis. All *J-V* and *C-V* measurements were performed inside the N₂-filled glovebox.

The samples for AFM measurement were prepared by spin coating the HTLs and perovskite solution on glass substrates using the same device fabrication procedure given above. AFM measurements were made in air using an Asylum Research MFP3D stand-alone AFM. The probes (ANSCM-PT) used were Pt/Ir-coated (≈25 nm) Si probes with a spring constant of 1–5 N m⁻¹ as supplied from AppNano. Scans were typically 5 μm × 5 μm at a scan speed of 1.0 Hz. The grain size distribution was extracted using ImageJ software.

Powder X-ray diffraction (PXRD) measurements were taken from 2θ = 10° to 50° under Cu Kα (1.542 Å) with a PANalytical X'Pert3 X-ray diffractometer.

All samples were fabricated identical to the respective steps in the solar cell fabrication procedure for UPS measurements. The measurements were performed with Electron Spectroscopy for Chemical Analysis instrument (Scienta Omicron Nanotechnology, model ESCA+S) at a base pressure of 4×10⁻¹⁰ mbar. The instrument configuration consisted of a He discharge lamp (He I line, 21.2 eV) as the UV excitation source and a hemispherical SPHERA energy analyzer. A sample bias of -3 V was used to collect the low kinetic energy electrons. The binding energy scale, which is given with reference to the instrument Fermi level, was shifted accordingly. The E_{SEC} were determined from the intersection of a tangent line to the signal onset with the abscissa axis; the respective vacuum levels were taken 21.2 eV away from E_{SEC}. The HOMO/VB energies were determined from the intersection of lines tangent to the signal onsets and low-binding energy background. The instrument resolution of ~0.1 eV was calculated from the full width at half maximum of a Fermi level of Ar⁺-sputtered silver.

Photoluminescence studies were carried out emission studies were carried out on an Agilent Cary Eclipse Fluorescence spectrometer. Films were excited at 475 nm, and the emission spectra were measured in the range of 700 – 820 nm. The absorption studies were carried out on an Agilent Cary 50 Bio UV-Visible Spectrometer.

3. Results and Discussion

Inverted PSCs with p-i-n device architecture consisting of ITO/HTL/FA_{0.05}MA_{0.95}PbI₃/[6,6]-phenyl C₆₁ butyric acid methyl ester (PC₆₁BM)/C₆₀-N/Ag were fabricated using four different HTLs. A C₆₀-N (Figure S1) was used as an interfacial modification layer at the Ag electrode.²⁰ **Figure 1a** shows the photocurrent density-voltage (*J-V*) curves measured in the reverse scan direction for the devices with PEDOT:PSS, PTAA, CuI, or PTAA/CuI as HTLs. **Figure 1b** shows the *J-V* curves for the device with bilayer PTAA/CuI HTL for both scan directions. **Figure 1c-f** summarizes the average short-circuit current density (*J*_{SC}), fill factor (*FF*), *V*_{OC} and efficiency (η) values. **Table S1** lists the average and best device performance metrics. The operation of PEDOT:PSS-based devices with an average PCE of 13.2 ± 0.63% is consistent with many other studies.^{23,26,27} Replacement of PEDOT:PSS with PTAA leads to a significant enhancement of average PCE up to 18.1 ± 0.76% (**Figure 1c**). In particular, the *J*_{SC} and *V*_{OC} of these PTAA-HTL

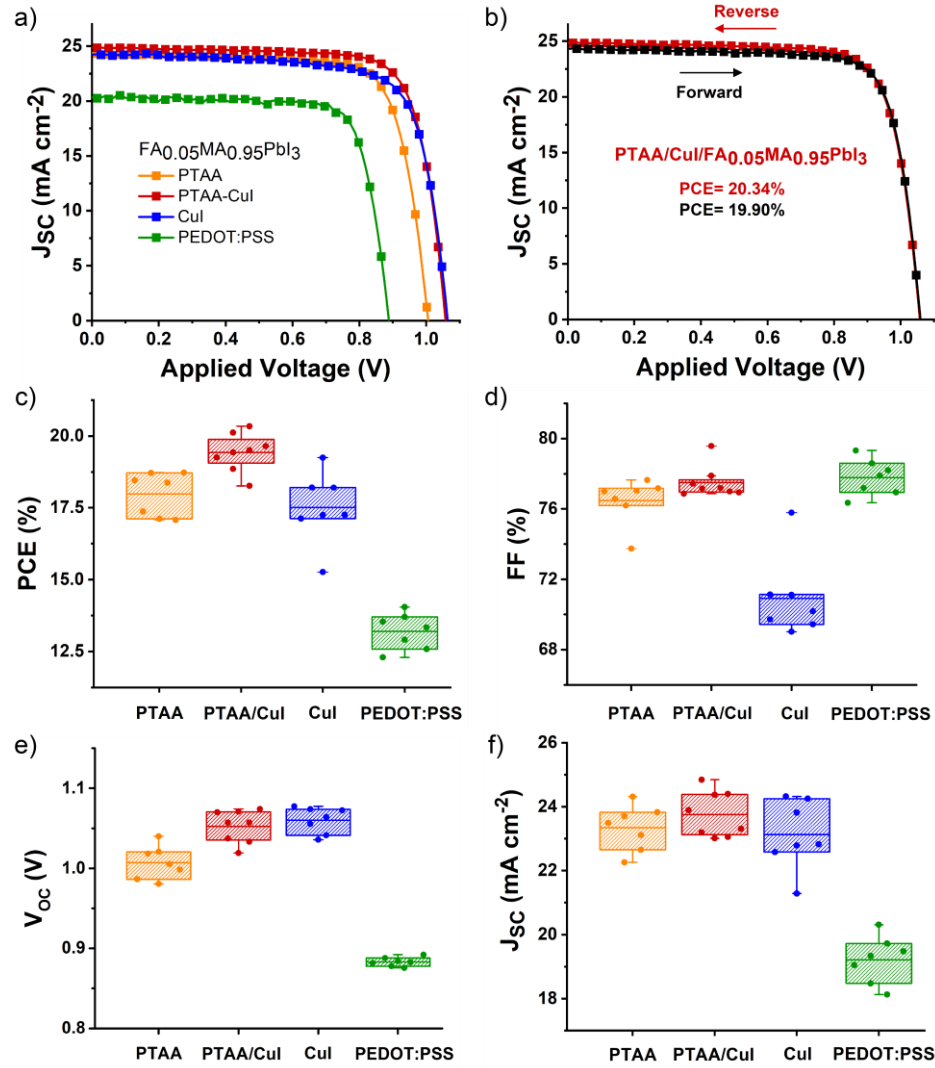


Figure 1. (a) The reverse scan J-V curves for different HTL-containing FA_{0.05}MA_{0.95}PbI₃ devices; (b) The forward and reverse scan J-V curves for PTAA/CuI bilayer containing device and (c-f) the average device performance characteristics for PTAA, PTAA/CuI, CuI and PEDOT:PSS-based FA_{0.05}MA_{0.95}PbI₃ devices.

devices increased dramatically. Despite the highest V_{oc} of ~1.06 V and large J_{sc} of 23.1 mA cm⁻² in CuI-HTL devices, their average PCE is comparable to PTAA devices due to a lower FF of $71.4 \pm 2.1\%$. Low FF in CuI-HTL devices could be due to low shunt resistance (avg. ~16.9 kΩ) compared to the PTAA-based devices (avg. ~38 kΩ). Low shunt resistance indicates the presence of small pinholes in the CuI film that could form during the nucleation due to random crystal orientation. The presence of pinholes could cause leakage at the interface for hole collection, thus can contribute to the observed low FF. Note also that, while small current hysteresis was observed for PEDOT:PSS-based devices, the performance of CuI-HTL devices strongly depends on the scan direction (Figure S2 and Table S2). When we introduced a PTAA layer between CuI and ITO, the device performance significantly improved. The average FF increased to $77.5 \pm 0.8\%$ the devices maintained a high average J_{sc} and V_{oc} of 23.8 mA cm⁻² and 1.05 V, respectively. The devices with bilayer PTAA/CuI achieve the best average PCEs of $19.4 \pm 0.66\%$ with a fairly narrow distribution of all device metrics. The best device produced a PCE of 20.34% (Table S2). Figure S3 shows the external quantum efficiency

(EQE) profile of multiple bilayer devices, where broad photo-responses with high values were obtained.

Iodide vacancies in CuI have been reported to form trap states above the valence band.^{34,35} During forward and reverse voltage sweeps, these trap states can be reversibly populated and depopulated with iodide ions or holes at the timescale of scan rates. We observed that the hysteresis is dramatically suppressed in bilayer PTAA/CuI-based devices (Figure 1b). The hysteresis in bilayer PTAA/CuI-based devices is comparable to the PTAA-based devices (Figure S2a and S2d). On the other hand, significantly smaller hysteresis in PEDOT:PSS-based devices indicate that the perovskite active layer is not the primary origin for this effect. Therefore, we speculate that diffusion of excess iodide ions from CuI leads to the doping of PTAA,³⁶ which improves the hole conductivity of both layers and optimizes the energy level alignment, enabling a highly efficient device with negligible hysteresis. The doping of conjugated polymers by iodide ions is well known.^{37,38}

In a p-i-n device architecture, the HTL influences the perovskite films' morphology and crystallinity. Therefore, to understand the improvement in device performance in bilayer HTL containing devices, we imaged the morphology and grain size

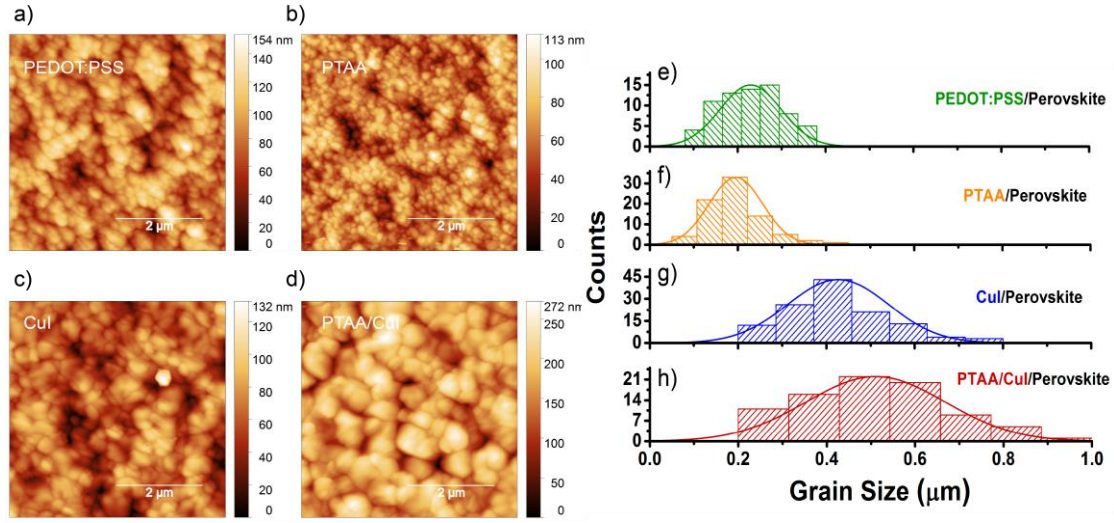


Figure 2. (a-d) Atomic force microscopy (AFM) images and (e-h) respective grain size distribution for $\text{FA}_{0.05}\text{MA}_{0.95}\text{PbI}_3$ films spin-coated on (a,e) PEDOT:PSS (b,f) PTAA (c,g) CuI and (d,h) PTAA/CuI HTL coated glass substrates.

of the as-prepared perovskite films on different HTLs using atomic force microscopy (AFM). The images showed uniform coverage of the perovskite films for all HTLs, ensuring fewer pinholes and hence, better charge transport in the solar cell devices (**Figure 2a-d**). The nucleation and growth processes that control the perovskite film's grain size are expected to be substrate-dependent. The average grain size/film thickness aspect ratio of $\text{FA}_{0.05}\text{MA}_{0.95}\text{PbI}_3$ on PEDOT:PSS (**Figure 2e**) and PTAA (**Figure 2f**) is 0.63 and 0.57, respectively. On the other hand, most grains in $\text{FA}_{0.05}\text{MA}_{0.95}\text{PbI}_3$ films on CuI were larger than the film thickness, with the average grain size/film thickness aspect ratio of 1.2 for CuI (**Figure 2g**) and 1.6 for CuI on PTAA (**Figure 2h**). The increase in average grain size of perovskite over PTAA/CuI HTL, by ~ 100 nm, compared to CuI alone could be due to the non-wetting nature of PTAA, which can increase CuI crystal size.^{23,39,40} These large CuI crystals can mitigate the nucleation of perovskite film that can drive larger grain formation. Surprisingly, unlike previous reports, we did not observe an enhancement in perovskite grain size on PTAA (**Figure 2f**), perhaps due to the differences in film fabrication methods—one-step antisolvent dripping vs. two-step interdiffusion.²³ It has been widely observed that the large grain size, *i.e.*, a smaller density of grain boundaries, can enhance the V_{OC} due to the elimination of charge trapping and non-radiative recombination. In addition to the increase in grain size, the root mean square (RMS) roughness for the perovskite film deposited on PTAA/CuI was less (8.3 nm) compared to ones on CuI (19.3 nm), PTAA (15.9 nm), and PEDOT:PSS (22.0 nm) (**Figure S4**). Moreover, using XRD, we observed an enhancement in perovskite (110) peak intensity (**Figure S5**) when deposited on PTAA/CuI, indicating improved crystallinity compared to other HTLs. As a result, low trap density in PTAA/CuI-based films benefits the device performance.

The energy levels of individual components and their alignment at various hetero-interfaces in the device structures were studied by ultraviolet photoelectron spectroscopy (UPS). **Figure 3a** shows the binding energy region that was used to determine secondary electron cut-off energies (E_{SEC}), and to calculate vacuum level (VL), Fermi level (E_F), and work function. **Figure 3b** shows the binding energy region that contains the highest occupied molecular orbital (HOMO) or valence band (VB) onsets. The work function of the ITO was 4.85 eV (**Figure**

S6). The energy levels for all HTLs and $\text{FA}_{0.05}\text{MA}_{0.95}\text{PbI}_3$ are summarized in **Figure 3c**. The work function of PEDOT:PSS was 5.1 eV. The E_F and HOMO of PTAA were 4.6 eV and 5.1 eV, respectively. Note that in contact with materials that can source I^- anions, such as CuI or $\text{FA}_{0.05}\text{MA}_{0.95}\text{PbI}_3$ in our case, iodide anion doping of PTAA could lead to the pinning of E_F as high energy as HOMO, *i.e.*, at 5.1 eV. Such doping of PTAA using copper salts has been observed.³⁶ The E_F and VB onset for CuI were determined at 5.25 eV and 5.35 eV, respectively. The proximity of E_F to VB indicates that CuI is a highly-doped p-type material (denoted $\text{p}^{++}\text{-CuI}$), *i.e.*, possesses high hole carrier density and, hence, electrical conductivity. p-type doping of CuI has been previously attributed to excess I^- anion vacancies.^{25,34} High electrical conductivity of CuI ensures a low resistive voltage loss across the interlayer and hence, an efficient charge collection.²⁵ CuI films deposited on PTAA shifted the E_F towards the VB by 50 meV compared to bare CuI on ITO (**Figure S7**). Although this difference is below the UPS instrument's resolution limit, such a shift in E_F could occur due to the p-doping of CuI in the presence of excess iodide vacancies. We believe that these excessive vacancies are a result of PTAA doping with iodide anions. The E_F and VB of $\text{FA}_{0.05}\text{MA}_{0.95}\text{PbI}_3$ are 4.9 eV and 5.55 eV, respectively. Herein, we note that the probed depth in the UPS experiment of ~ 2 nm, as determined by the mean free path for inelastic scattering of electrons, was significantly smaller than the perovskite film thickness of ~ 350 nm. As a result, the determined energy levels, in particular E_F , describe the properties of top surface of perovskite film. E_F in the bulk or at the hidden interface with substrate can be significantly different due to surface/interface band bending that is determined by the hetero-interfacial Fermi level alignment, magnitudes of interfacial dipoles, and population of surface/interface traps.⁴¹ On the other hand, flattening of surface band bending due to surface photovoltage effect⁴² in the UPS experiment under UV illumination and background illumination leads to the measurement of energy levels, which could be characteristic of bulk properties.⁴³ **Figure 3d** illustrates the interfacial energy level alignment between p-doped PTAA with a work function of 5.1 eV and $\text{FA}_{0.05}\text{MA}_{0.95}\text{PbI}_3$ with a work function of 4.9 eV. Assuming an ideal interface, *i.e.*, no interfacial dipole or surface traps, such work function difference would lead to the upward interfacial band bending of 0.2 eV. The larger work function of

a p^{++} -CuI of 5.25 eV can lead to an increased band bending of 0.35 eV, facilitating the hole extraction at this interface. Additionally, the p^{++} -CuI electrode can support a larger splitting of quasi-Fermi levels in the device active layer under illumination, leading to larger V_{OC} .

The alignment of energy levels of PEDOT:PSS, PTAA, CuI, and PTAA/CuI at HTL/perovskite interface in PSCs devices can be inferred from the impedance spectroscopy, using the Mott-Schottky analysis.^{44,45} We note that application of a depletion layer theory for metal-semiconductor interface with

The static charge distribution, which Mott-Schottky analysis implies, to PSCs should be exercised with caution due to ionic contributions and electrode polarization effects.^{46,47} **Figure 4** shows the dependence of C^{-2} on the applied bias (where C is the device capacitance) for two opposite scan directions and a scan rate of 117 mV s⁻¹ (identical to DC J - V measurements). As follows from variations in scan rates (Figure S8), the curves that were measured in the reverse direction for PTAA- and PTAA/CuI-based devices are independent of a scan rate and nearly coincide with the curve that was measured in the forward direction at the slowest scan rate, hence making Mott-Schottky analysis less prone to hysteresis. As discussed in Ref.⁴⁵, positive polarization as a starting point in C - V measurements, *i.e.*, reverse scan direction, is likely to remove the non-depletion-related contributions to the capacitance that obstruct determination of a built-in potential (V_{bi}) from Mott-Schottky analysis. Fitting the linear regions of reverse scan curves in Figures 4b and 4c by **Equation (1)**, the V_{bi} values of ~1.24 V and 1.28 V for PTAA- and PTAA/CuI-based devices, respectively, were determined. Both V_{bi} values are slightly smaller than the work function difference between the respective HTL and C₆₀-N/Ag electrode (work function = 3.65 eV, Ref. [20]). A slightly larger V_{bi} for PTAA/CuI-based devices is also in agreement with the larger work function of p^{++} -CuI (5.25 eV) compared to the work function of p -doped PTAA (5.10 eV) and correlates with the larger V_{OC} of CuI- and PTAA/CuI-based devices. The quasi-Fermi level for holes would be pinned at the larger work function of p^{++} -CuI, leading to a larger V_{OC} in these devices.

$$\frac{1}{C^2} = \frac{2}{q\epsilon_0\epsilon N_B}(V - V_{bi} - 2kT/q) \quad (1)$$

where q is the elementary charge, ϵ_0 is the vacuum permittivity, ϵ is the relative dielectric constant of perovskite material, N_B is either donor or acceptor concentration depending on the conductivity type, V_{bi} is the built-in potential, k is Boltzman's constant, and T is the absolute temperature.

The difference in V_{bi} values for opposite scan directions for PTAA- and PTAA/CuI-based devices, 0.06 V and 0.13 V, respectively, as determined from the Mott-Schottky analysis (ΔV_{bi} in Figure 4(b) and 4(c)) or 0.17 V and 0.06 V, respectively, as estimated for a broader set of scan rates from the difference in the minima of $C^{-2}(V)$ curves (ΔV_{min} in Figure S8) allows to estimate the extent of hysteresis that is associated with each HTL. Note that, while devices with PEDOT:PSS HTL operate with virtually no hysteresis (Figure S2(c), Table S2), the performance of CuI-based HTL devices is highly affected by the scan parameters (Figure 4(d), Figure S2(b), Table S2) and depends on measurement history. Identical to the behavior observed for DC J - V curves, the presence of PTAA in a bilayer PTAA/CuI-based devices significantly suppresses the hysteresis in C - V curves that devices with single CuI-based HTL demonstrate,

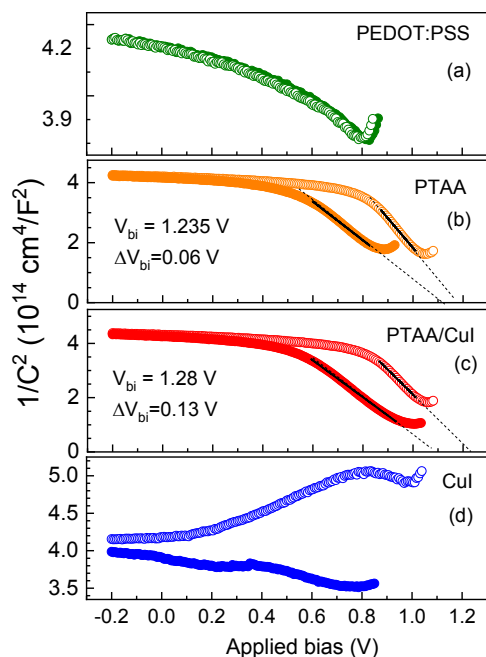
making the performance of PTAA- and PTAA/CuI-based devices comparable.

We employed the steady-state photoluminescence (PL) spectroscopy to characterize hole extraction efficiency for different HTLs. A strong emission peak was observed around ~775 nm (Figure S9), consistent with the 1.57 eV bandgap calculated from the Tauc plot (Figure S10). The PTAA/CuI and CuI underlayer quenched the FA_{0.05}MA_{0.95}PbI₃ emission, and the quenching was stronger in the case of PTAA/CuI. The smaller PL intensity is associated with less radiative losses. A reduced radiative recombination in bilayer containing devices explains a higher V_{OC} and FF .^{28,48,49}

We also employed impedance spectroscopy (IS) to investigate the recombination resistance (R_{rec}) in different HTL-based PSCs. The IS data for different HTL containing PSCs (Figure S11) was fit to an equivalent circuit model reported elsewhere.^{21,47,50} The R_{rec} for different HTLs are given in Table S3. The PTAA/CuI-containing device has a highest R_{rec} , followed by CuI and PTAA. Meanwhile, PEDOT:PSS-based device has the lowest recombination resistance. Since the perovskite/ETL interface is same for all devices, we ascribe the difference in R_{rec} to the HTL/perovskite interface. This result is consistent with the UPS data, which suggests a better HOMO alignment

Figure 4. Mott-Schottky plot of PSCs containing different HTLs under forward (solid circles) and reverse (open circles) direction. The solid lines are the fits in the respective voltage regions, and dashed lines are a guide. The C - V measurements were performed at 10 kHz with a scan rate of 117 mV/s.

of CuI with the HOMO of perovskite (Figure 3c), hence, ensures reduced recombination.



The current output stability of devices with different HTLs was tested by tracking the maximum power point (MPP), as shown in **Figure 5a**. The devices were kept at V_{MP} under AM1.5G light and the resulting current was measured over a period of 200 s. The results indicate that both PTAA ($V_{MP} = 0.857$ V) and PTAA/CuI ($V_{MP} = 0.900$ V) HTLs produce more stable output at MPP relative to other HTLs employed. The worst performance was observed in CuI-based devices ($V_{MP} = 0.887$ V) which showed $\sim 11\%$ decrease in the current within first 200 s. We expect a similar decrease in current during the reverse voltage scan, although the rate of decay could be different. This observation corroborates with the J - V scans for CuI-based devices (Figure S1b) which showed significant hysteresis

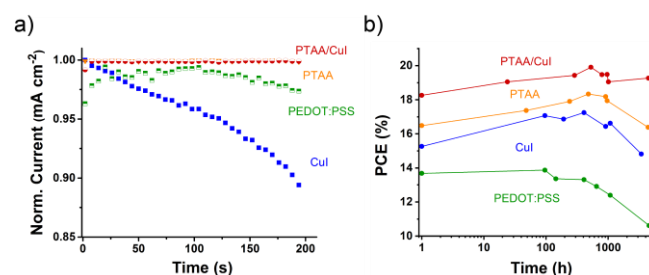


Figure 5. (a) Normalized current-density output of devices employing different HTLs as a function of time under maximum power-point (MPP) tracking test; (b) the long term (> 3000 h) stability test of CuI, PTAA/CuI, PTAA and PEDOT:PSS-based $\text{FA}_{0.05}\text{MA}_{0.95}\text{PbI}_3$ devices stored inside inert N_2 -filled glove box under white fluorescent light.

on switching the scan direction. The stable power output of different HTL containing devices at MPP is also given Figure S12.

Long-term stability of devices with CuI, PTAA, PTAA/CuI and PEDOT:PSS HTLs was tested by storing them in inert atmosphere of a glovebox (without encapsulation, under white fluorescent light) for over 3000 h. As shown in Figure 5b, CuI-, PTAA- and PTAA/CuI-based devices show superior stability

over PEDOT:PSS-based ones. While a minimal decrease in PCE was observed after 408 h, 480 h and 528 h, respectively, the PCE of PEDOT:PSS-based devices drops significantly only after 144 h of storage. The PTAA/CuI containing devices maintained their stability even after 4500 h (Figure 5b). Since, the only difference between these PSCs is the HTL, these results confirm that the PTAA/CuI bilayer is most effective for suppressing device degradation. Similarly, devices with double layer CuI/PEDOT:PSS as HTL have demonstrated an improved efficiency and stability; however, only when CuI was placed in-between ITO and PEDOT:PSS, removing a corrosive effect of PEDOT:PSS on ITO.²⁸

4. Conclusion

In this work, we employed a PTAA/CuI bilayer as an HTL in inverted PSC and showed a significant enhancement of device PCE and long-term stability compared to devices with single PEDOT:PSS, PTAA, or CuI HTLs. These improvements originate from each layer's properties as well as from their interaction in the bilayer HTL. The AFM and XRD results show that the CuI surface facilitates the growth of larger perovskite grains and improves their crystallinity. Using UPS, we determine the energy levels of each component in the device architecture. Combined with the Mott-Schottky analysis of the impedance spectra, we found that the bilayer creates an enhanced upward band bending of 0.35 eV at the CuI/perovskite interface and a large V_{bi} of ~ 1.28 V throughout the active layer, favoring the charge extraction at both sides. The higher work function of CuI is responsible for higher V_{oc} of the devices since the pinning of the quasi-Fermi level for holes under illumination can occur at a deeper level. We believe that the device architecture with bilayer HTLs is a promising route for further improvement of efficiency and stability of perovskite solar cells. Through a deeper understanding of the working mechanisms, which allows for proper design of interfaces, including the energy-level alignment, interfacial engineering could pave the way for further developments of efficient and stable PSCs.

ASSOCIATED CONTENT

Supporting Information

Structure of C_{60} -N, Device parameters, Forward and reverse scan J - V curves, Device parameters under forward and reverse scan, EQE profile of multiple PTAA/CuI containing devices, RMS roughness of the perovskite films, XRD pattern for $\text{FA}_{0.05}\text{MA}_{0.95}\text{PbI}_3$ films, UPS spectra of ITO substrates, UPS spectra of CuI film on ITO and PTAA/ITO substrates, Mott-Schottky analysis at different scan rates and directions, PL spectra of $\text{FA}_{0.05}\text{MA}_{0.95}\text{PbI}_3$ films on different HTLs, Tauc plot for $\text{FA}_{0.05}\text{MA}_{0.95}\text{PbI}_3$, Nyquist plots and equivalent circuit fits for different HTL devices, Recombination resistance for different HTL devices, stable power output of devices at MPP.

The Supporting Information is available free of charge on the ACS Publications website.

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