Fabrication and surface treatment of electron-beam evaporated niobium for lowloss coplanar waveguide resonators

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ABSTRACT

We characterize low-loss electron-beam evaporated niobium thin films deposited under ultra-high vacuum conditions. Slow deposition yields films with a high superconducting transition temperature $(9.20\pm0.06\,\mathrm{K})$ as well as a residual resistivity ratio of 4.8. We fabricate the films into coplanar waveguide resonators to extract the intrinsic loss due to the presence of two-level-system fluctuators using microwave measurements. For a coplanar waveguide resonator gap of $2\,\mu\mathrm{m}$, the films exhibit filling-factor-adjusted two-level-system loss tangents as low as 1.5×10^{-7} with single-photon regime internal quality factors in excess of one million after removing native surface oxides of the niobium.

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Recent achievements in quantum computing have shown that superconducting circuits are one of the most promising platforms to realize the long-sought challenge of building a fault-tolerant quantum computer. The performance of such devices is, however, limited by decoherence sources such as quasiparticles, and radiation effects. Recent advances in fabrication techniques and microwave engineering have significantly reduced the impacts of the above-mentioned defects, thereby leaving two-level-system (TLS) fluctuators as the most prominent source of loss in superconducting circuits. Let's the shown that TLS defects are mainly located at metal—air (MA), substrate—air (SA), and metal—substrate (MS) interfaces. The contribution of losses from these interfaces can be minimized by implementing careful surface treatments, thus enhancing the coherence of the devices.

Owing to its high superconducting transition temperature, critical field, and low microwave loss, ²² niobium (Nb) has become one of the common materials used in the fabrication of superconducting circuits. ^{23,24} Nevertheless, the known stoichiometric range of its native oxides results in a complex loss-inducing MA interface. ^{25,26} Lately, it has been shown that removing the oxides on the MA interface of Nb films

can result in highly coherent devices.^{27,28} Additionally, conventional niobium deposition techniques, such as DC magnetron sputtering, can result in a damaged MS interface due to the presence of high energy argon ions²⁹ and point defects stemming from trapped argon atoms.³⁰

In order to prevent these sources of decoherence, here we investigate a refined deposition method³¹ of Nb utilizing an ultra-high vacuum (UHV) electron-beam evaporator. We fabricate the Nb thin films into coplanar waveguide (CPW) resonators, which are well-known for their ease of fabrication as well as their sensitivity to the true intrinsic TLS defect density of the materials. We observe that surface treatment results in lower TLS densities compared to prior studies using the same CPW geometry optimized for sensitivity to TLS defects. This study further establishes Nb, with appropriate surface treatment, as an ideal material for the fabrication of highly coherent superconducting qubit processors.

Samples are fabricated on a 2-in., (100)-oriented, high resistivity (>8 k Ω · cm), single-side polished intrinsic silicon substrate cleaned in a Piranha solution (3:1 mixture of sulfuric acid and hydrogen peroxide) at 120 °C for 10 min followed by a 5-min etch in a buffered-oxide-etch (BOE) solution to remove organic contaminants as well as

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the native silicon surface oxide. 20,35 The BOE solution is a 6:1 mixture of ammonium fluoride (NH₄F) and hydrofluoric acid (HF). The substrate is then pumped down in a UHV electron-beam evaporator (AJA ATC-ORION-8E) with a base pressure lower than 5 nTorr.

After loading the wafer, a 200 nm layer of 99.95% purity Nb is evaporated onto the substrate at a rate of 1.2 nm/min, which is commensurate to the previous study.³¹ Note that the substrate is inevitably heated during this process due to the high melting point of Nb. Since pure Nb quickly adsorbs impurities,²² especially when heated, we let the sample remain under UHV conditions to cooldown for 1–2 h prior to proceeding to the next steps.

We spin and softbake the Nb samples with a high resolution photoresist (MicroChem S1805) and pattern the coated wafer with a Heidelberg DWL 66+ photolithography system. The pattern consists of eight hanger-style, quarter-wavelength CPW resonators with a gap (width) of 2 μ m (3 μ m). Devices are simulated to have frequencies ranging from 5.2 to 7 GHz with coupling quality factors $\sim\!\!6\times10^5$ (simulated using SONNET microwave software). We develop the exposed resist by using a metal-ion-free solution (MicroChem MF-319). A reactive ion etch system (Oxford Plasmalab 100) is then used to etch the samples using a fluorine chemistry (SF₆). To assist the removal of the residual resist, the samples are first ashed for 30 s using oxygen plasma (Plasma Etch PE 50, 100 W, 15 cc/min) and then soaked in N-Methyl-2-pyrrolidone (NMP) heated to 70 °C for 8 h. At last, samples are coated with the photoresist (S1805) to enhance their preservation over time 27 and protect against damage caused by dicing.

Figure 1 illustrates the surface topography of the Nb films fabricated into CPWs [Fig. 1(a)] using the above-mentioned techniques. The scanning electron microscopy (SEM) image shows the elongated Nb grains formed on the surface [Fig. 1(b)]. The dark-field scanning transmission electron microscopy (STEM) cross-sectional image of the etched films shows that our etching process is anisotropic [Fig. 1(c)]. In this study, the etching has been engineered to result in trenches shallower than 500 nm for all the fabricated devices to maintain the effective substrate dielectric constant in the devices, and thereby to avoid deviations from the desired resonance frequencies.³³ The STEM image shows grain sizes greater than 20 nm [Fig. 1(d)], exceeding previous values reported using the same deposition technique.³¹ Correlation between the grain sizes and materials loss has been recently studied,³⁶ substantiating the advantage of larger grain sizes by treating the grain boundaries as Josephson weak-links.³⁷ Finally, the films resulted in a superconducting transition temperature $T_c = 9.20 \pm 0.06 \,\mathrm{K}$ and a residual resistivity ratio (RRR) of 4.8 [Fig. 1(e)], exhibiting the high quality of the evaporated Nb in accord with recent results with sputtered Nb.36 Note that the films deposited at higher pressures (>6 nTorr) resulted in a significantly lower transition temperature $T_c \simeq 7.85 \text{ K}$ as well as a RRR of only 1.8, which can be attributed to the fact that having a low deposition rate makes the quality of the films extremely sensitive to the deposition environment. Therefore, having a UHV deposition environment is a crucial condition for achieving high quality films using this technique.

Transmission electron microscopy (TEM) images show a clear band of about 5 nm of oxide on top of the Nb, which is reduced to \sim 1.7 nm after removing the surface oxides in a BOE solution for 20 min [Figs. 2(a) and 2(b)]. Note that it takes about 20 min to transfer the sample to the characterization instruments, during which the oxide grows back following the Cabrera–Mott theory.²⁸ To identify the elements present in the

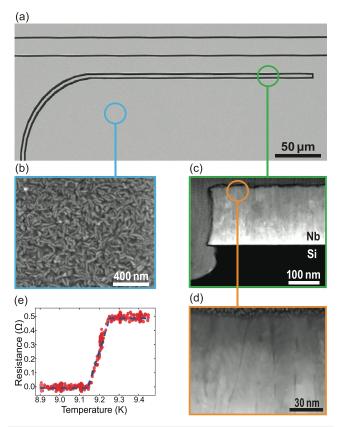


FIG. 1. Surface topography of the fabricated CPWs. (a) SEM image shows the fabricated resonator coupled to the feedline with a CPW gap of 2 μm . (b) Detailed SEM image displays the compact elongated niobium grains formed on the surface. (c) A dark-field STEM cross-sectional image illustrates the anisotropic etch. (d) A STEM cross-sectional image depicts grain sizes exceeding 20 nm. (e) The four-probe resistance measurement shows a superconducting transition temperature $T_c=9.20\pm0.06\,K$. The uncertainty in the transition temperature is taken as the full temperature range of the transition. The RRR is calculated by dividing the surface resistance at 310 K to that right before the transition at 9.258 K as $2.33\,\Omega/0.49\,\Omega\simeq4.8$ exhibiting high quality of the films.

films, we perform electron-energy loss spectroscopy (EELS) measurements. Averaged EELS spectra are displayed in Figs. 2(c) and 2(d) along the red (orange) lines for the oxide (metal) regions of the TEM images in Figs. 2(a) and 2(b). The regions indicated by red lines exhibit a clear peak located at $\sim\!535$ eV, which corresponds to the presence of oxygen. Below this NbO $_{x}$ band, there is no sign of this peak. Furthermore, EELS data revealed a clear reduction in the average oxygen content from $\sim\!50\%$ in the oxide band to less than 5% while scanning inside the metallic Nb film indicating the absence of oxygen impurities in the films.

We employ x-ray photoelectron spectroscopy (XPS) to further investigate the compounds present on the surface. Here, we utilize a PHI VersaProbe II surface analysis instrument equipped with an aluminum K-alpha x-ray source. Under the optimal neutralization settings, an overall shift of ~ 2.7 eV from the nominal binding energy values is observed due to the surface charge of the Nb films. The presented data have been adjusted to account for this shift. We examine the XPS spectrum of Nb_{3d} by curve fitting the data using the Lmfit package. The fits reveal peaks for three distinct niobium oxides

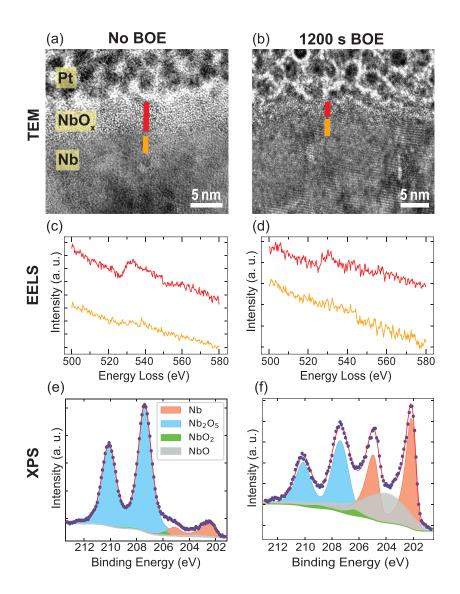


FIG. 2. Comparison of the metal-air interface characteristics with different cleaning schemes. TEM samples are prepared using a focused ion beam after the deposition of a platinum (Pt) protective layer on top. TEM images show (a) a clear band of NbOx \sim 5 nm thick on top of the Nb surface and (b) after cleaning the sample for 20 min with BOE, the oxide layer thickness was reduced to \sim 1.7 nm. Red and orange lines in (a) and (b) indicate regions where averaged EELS spectra are collected. [(c) and (d)] The averaged EELS spectra with orange and red traces corresponding to regions depicted in (a) and (b). The spectra in the oxide region (red) display a significant peak around 535 eV corresponding to oxygen. This peak is absent along the Nb metal (orange), showing that there is no trace of oxygen in the Nb film. (e) Nb3d XPS measurements of the Nb surface show different niobium-oxygen compounds, dominated mostly by Nb2O5 oxide prior to treating the surface. (f) After surface cleaning, we observe a clear increase in the Nb peak relative to the oxides

[Figs. 2(e) and 2(f)]. These peaks were fit using the "skewedVoigt" model for asymmetric metallic Nb peaks and the "pseudoVoigt" model for all the other peaks using a Shirley inelastic background with $3d_{5/2}$ binding energies located at 202.05, 203.40, 205.84, and 207.38 eV for Nb, NbO, NbO₂, and Nb₂O₅, respectively, for the untreated film. Among those, NbO is superconducting with a transition temperature of $1.38~\mathrm{K}.^{39}~\mathrm{Nb}_2\mathrm{O}_5$ is the most thermodynamically stable state of the niobium—oxygen system with the highest binding energy (\sim 207 eV) and the lowest electrical conductivity. Due to its various crystalline phases and physical properties, Nb₂O₅ has been considered as one of the main sources of defects present on the surface of Nb. NbO₂ also contributes to the loss due to oxygen vacancies, high which has the lowest participation in the deposited films reported here.

Prior to placement in the measurement cryostat, devices are cleaned for 7 min in an ultrasonic bath of acetone and isopropyl alcohol to remove particles on the samples and strip the protective photoresist. The transfer time to the fridge is kept under 90 min for the BOE

post-cleaned samples to minimize oxide regrowth on the devices. The samples are placed inside palladium-plated copper microwave launch packaging surrounded by Cryoperm shielding to protect the devices from infrared radiation and external magnetic fields. Mounted devices are cooled inside an adiabatic demagnetization refrigerator (ADR) with a base temperature of $50\,\mathrm{mK}$.

The resonator transmission, S_{21} , is measured using a vector network analyzer in an experimental setup described previously. 16,42,43 Data are collected with the ADR in the temperature regulation mode. Figure 3(a) displays the transmission near the resonance of a particular device [indicated by the arrow in Fig. 3(c)] at low power. The quality factors are extracted by employing the ϕ rotation method. Figure 3(b) displays the internal loss tangent $\delta_{\rm int}$ (inverse of the internal quality factor Q_i) as a function of the average photon number of the BOE-cleaned device mentioned above. Using the TLS model, we extract the filling-factor-adjusted TLS loss tangent (F $\delta_{\rm TLS}$), where F is defined as the fraction of the resonator's total loss stored in the TLS

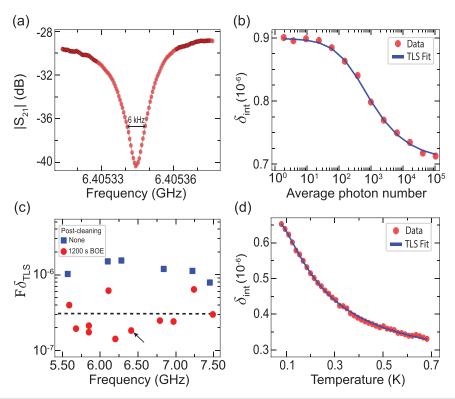


FIG. 3. Microwave measurements of the CPW resonators. (a) Transmission profile near the resonance at single-photon power shows a full width at half maximum of 6 kHz corresponding to an internal quality factor of 1.06×10^6 for the device indicated by the arrow in (c). (b) The internal loss of the same device as a function of the average photon number, showing minimal dependence on power. Data points were fit using the TLS model (blue solid line), resulting in $F\delta_{TLS} = (1.94 \pm 0.02) \times 10^{-7}$. (c) Comparison of the extracted $F\delta_{TLS}$ of the fabricated CPW resonators from the TLS fit of the power scans as a function of the resonance frequency across three chips for different post-cleaning treatments. Red circles indicate results for devices that were etched with a BOE solution for 1200 s before placement into a cryogenic vacuum environment for measurement. Blue squares correspond to results for samples that did not receive this pre-measurement etch treatment. The dashed line indicates the average $F\delta_{TLS}$ value of the treated devices. (d) TLS fit of the temperature scan at high circulating power (-93 dBm), corresponding to an average photon number of $\sim 10^5$, resulting in $F\delta_{TLS} = (4.39 \pm 0.02) \times 10^{-7}$, showing a good agreement with the power scan result in (b).

material. Figure 3(c) displays $F\delta_{TLS}$ for several devices vs resonance frequency. The $F\delta_{TLS}$ values are an order of magnitude smaller for the case of post-cleaned devices (red circles) than the untreated devices (blue squares) resulting in values as low as 0.15 ppm. The dashed line indicates the average value of $F\delta_{TLS}$ of the post-cleaned devices. The average value is 0.32 ppm, well below previous results using the same CPW geometry. The average low power Q_i of the post-cleaned devices is 1.13×10^6 . In contrast, the untreated devices (blue squares) show an average TLS loss tangent of 1.21 ppm with an average single-photon regime Q_i of 3.35×10^5 comparable with other sputtered niobium studies. 16,45

Figure 3(d) displays measurements of the internal loss of the aforementioned device vs temperature at fixed power. Based on the TLS model, the TLS-induced loss tends to saturate at high temperatures 15,32 as shown in Fig. 3(d). By fitting to the TLS model, we obtain a F δ_{TLS} of 0.44 ppm in accord with the power scan.

The above results agree with previous findings^{27,28,46} that the MA interface is one of the main sources of TLS loss. In our study, we have employed a small CPW gap, which results in a significantly higher concentration of the electric field inside the trenches of the CPW, therefore maximizing the coupling with the TLS fluctuators at the

interfaces of the material as well as resulting in a larger filling factor value. 8,34 Hence, devices with larger features 27 would reduce this filling factor and electric field density, thereby reducing $F\delta_{TLS}$ by nearly one order of magnitude as well as yielding higher internal quality factors. 8,15

We have refined a method to deposit extremely low-loss niobium films for superconducting CPW resonators using a UHV electron-beam evaporator. With post-cleaning of the Nb surface, devices demonstrated loss tangents well-below previous limits, highlighting the role of the MA interface as one of the main sources of loss in superconducting devices. Moreover, the characterization results show a significant reduction in the surface oxide thickness, verifying the efficacy of our cleaning method. Future work may explore a practical passivation scheme to bypass the MA interface-induced losses for the fabrication of highly coherent superconducting qubit processors.

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DATA AVAILABILITY

The data that support the findings of this study are available from the corresponding author upon reasonable request.

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