InAsP Quantum Dot-Embedded InP Nanowires toward Silicon Photonic Applications


ABSTRACT: Quantum dot (QD) emitters on silicon platforms have been considered as a fascinating approach to building next-generation quantum light sources toward unbreakable secure communications. However, it has been challenging to integrate position-controlled QDs operating at the telecom band, which is a crucial requirement for practical applications. Here, we report monolithically integrated InAsP QDs embedded in InP nanowires on silicon. The positions of QD nanowires are predetermined by the lithography of gold catalysts, and the 3D geometry of nanowire heterostructures is precisely controlled. The InAsP QD forms atomically sharp interfaces with surrounding InP nanowires, which is in situ passivated by InP shells. The linewidths of the excitonic (X) and biexcitonic (XX) emissions from the QD and their power-dependent peak intensities reveal that the proposed QD-in-nanowire structure could be utilized as a non-classical light source that operates at silicon-transparent wavelengths, showing a great potential for diverse quantum optical and silicon photonic applications.

KEYWORDS: quantum dot-embedded nanowire, InAsP quantum dot, VLS epitaxy, silicon photonics, exciton−biexciton transition

INTRODUCTION

With the emergence of quantum information science, single and entangled photon sources, which can serve as key components to achieve secure communications via quantum cryptography or quantum key distribution (QKD), have been intensely studied over the last few decades.1−4 Attenuated laser pulses which is a weak coherent light are widely used as an approximation to a single photon source in QKDs because such classical light sources are easy to prepare by using optical filters. However, attenuated laser pulses occasionally produce multiple photons per pulse which is vulnerable to the photon-number splitting attack without complex architectures such as the decoy state method and therefore the use of pure on-demand single photons is required to ensure high security.5,6 In addition, miniaturizing quantum devices including quantum light sources has been recognized as a major obstacle because integrated quantum devices on chips can reduce the cost and provide a pathway for various quantum communication protocols.7

Epitaxial semiconductor quantum dots (QDs) are particularly attractive in building these on-demand non-classical light sources at telecommunication wavelengths by virtue of the possibility in engineering the band alignment and providing three-dimensional (3D) quantum confinement.8 A well-known and common approach is self-assembled QDs grown in Stranski–Krastanow (SK) mode on semiconductor thin films that are capped in 3D material matrix.9,10 This method, however, is challenging to implement due to the randomness in spatial distribution of the position of QDs and the inhomogeneity of QD dimensions. This leads to spectral diffusion and insufficient photon extraction, whereas the development of practical and operable devices requires either a perfect alignment with certain optical components, such as nanophotonic cavity or waveguide, or a small vertical divergence angle for optical fiber coupling.11,12 To address these issues, a novel method of embedding QDs within bottom-up integrated nanowires is proposed, which can achieve not only self-alignment, but precise positioning by lithographically defined growth site.13,14 The controllability of the position of QDs by the quantum-dot-in-nanowire (QD-NW) approach is therefore promising for quantum light sources compatible with quantum photonic integrated circuit (QPIC). In addition, bottom-up nanowires have unique advantages in accurate formation of axial and core/shell heterostructures within the nanowires by controlling the

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diffusion lengths of adatoms and thermodynamics in their growth conditions. It is therefore an enabling technology to form 3D quantized structures, such as vertically/radially stacked QDs and quantum wells, with effective in situ passivation that can reduce the nonradiative recombination caused by surface states. Another key benefit provided by the QDs grown in vertical nanowires is that the QDs form hexagonally or circularly shaped disks. Such hexagonal or circular geometry provides high degree of structural symmetry (C_n) and can eliminate the fine-structure splitting (FSS), which is crucial in generating polarization-entangled photon pairs. In stark contrast, conventional QDs formed by the SK growth mode exhibit pyramidal shape and experience larger FSS due to the symmetry lower than D_{2h}, which is detrimental for entanglement.

The vapor–liquid–solid (VLS) method is one of the most common and mature techniques in growing bottom-up III–V nanowires incorporating QDs, wherein sharp material interfaces can be realized by precisely controlling the supersaturation of adatoms in catalysts, which is crucial for quantum confinement for exciton–biexciton generation. Within the last couple of decades, significant efforts have been made to grow III–V QD-NWs on a homogeneous substrate and the research focus has been gradually moved onto growing nanowires on silicon platform for silicon photonic applications. Despite the fact that a great leap of progress has been made, most studies to date are demonstrated in GaAs matrix with high surface recombination velocity (∼10^6 cm/s), which seriously impairs the quantum efficiency and therefore necessitates the incorporation of capping layers with small lattice mismatch to prevent dephasing. AlGaAs is commonly used as in situ capping layer of GaAs matrix, which greatly reduces the surface recombination velocity. However, this is an imperfect solution as Al oxidizes and degrades over time. Although an additional lattice-matched capping layer, such as GaAs, over the AlGaAs shell to form a core/shell/shell structure has been widely adopted, having perfect control on both axial and radial growth rate of the shell layers is a demanding requirement for efficient electrical contact. Besides, another potential limitation of the GaAs/AlGaAs QDs and GaAs/InGaAs QDs is that the emission wavelengths from QDs are typically in the range of 700–1000 nm, which is below the silicon transparency (λ > 1.1 μm), making them less favorable for on-chip integration and long-distance transmission due to attenuation and dispersion loss. In other words, for practical and functional QD-NW quantum emitters, it is necessary to form position-controlled nanowires on a silicon platform operating particularly in the silicon-transparent regime, ideally at telecommunication bands. In this regard, InP nanowires could be a promising building block because InP matrix exhibits much lower surface recombination velocity (∼10^3 cm/s) even without passivation and successful ex situ passivation with AlO_x is possible. Also, by embedding In(As)P QDs in InP nanowires, the emission wavelength can be tuned over a wide range potentially covering the telecom O-band and C-band via tailoring the As composition and QD dimensions, providing an opportunity for prospective electrical contact to InP nanowires and a pathway for deterministic photon pair creation at telecom bands.

In this study, we report on InAsP QDs embedded in InP nanowires grown on a silicon platform by the VLS method that can satisfy those requirements using metal–organic chemical vapor deposition (MOCVD). The optical properties of QDs are probed by cryogenic micro-photoluminescence (μ-PL) setup, from which excitonic (X) and biexcitonic (XX) emissions are observed and the dynamics of exciton–biexciton transitions are identified. The results suggest that the proposed method of forming QD-NWs on silicon can be universally applied to obtain ternary QDs in nanowires covering exciton–biexciton emission wavelength in the telecom regime. In this manner, by forming these nanowires on silicon-on-insulator photonic platforms and waveguides which have been recently reported, the QD-NW emitters can be utilized in silicon photonic applications such as wavelength multiplexed nanowire single photon sources.

**RESULTS AND DISCUSSION**

**Nanowire Growth.** An undoped 6-inch silicon (111) wafer is adopted as a growth substrate for VLS epitaxy, and gold (Au) nanodot arrays as a growth catalyst are patterned by electron beam (e-beam) lithography and the lift-off process. In
Figure 2. Atomic structure of InAsP QD embedded in InP nanowire. (a) STEM images of nanowire with a single QD, with 100 nm scale bar, and the high-magnification images around the (b) nanowire tip and (c) QD region, with 10 nm scale bars. (d) High-resolution STEM image showing the abrupt InP/InAsP/InP interface with an interplanar spacing of 3.53 Å/3.65 Å/3.53 Å. Scale bar, 2 nm. EDS analysis of InAsP QD in InP nanowire. (e) HAADF-STEM image of nanowire and (f–h) 2-D elemental mapping of As (red), P (cyan), and In (magenta), respectively. Scale bar, 20 nm.

In this work, the nanowires are grown by the VLS method using a low-pressure (60 Torr) Emcore D-75 MOCVD reactor, wherein ultra-high purity hydrogen (H₂) is used as a carrier gas for III–V precursors; trimethylindium (TMIn), tertiar-ybutylyphosphine (TBP), and tertiarybutylarsine (TBAs) (see Supporting Information Sections I and II for detailed sample preparation and growth processes). Figure 1 shows the schematic of step-by-step processes to control the geometry of nanowire heterostructures using the VLS method along with the scanning electron microscopy (SEM) images of nanowires corresponding to each step. First, when InP nanowires are grown at 420 °C with a V/III flow rate ratio of 760, the nanowires grown for 5 min and for 10 min show almost identical diameters with different heights (Figure 1b,c), representing growth that is purely vertical under this growth condition. Based on these results, InP–InAsP QD–InP vertical heterostructure is grown by first growing a short stem of InP nanowire, followed by QD formation by a pulsed flow of TBAs, and then vertical overgrowth of InP segment on top (Figure 1d). For QD formation, TMIn and TBP flows are shut down during the pulsed flow of TBAs, which has enabled the formation of QDs with atomically sharp interfaces by the reservoir effect of Au catalysts (see Supporting Information Section II and V for details). Nearly no radial increment is found from the short segment of InP nanowire (radius ~66.3 nm) to the growth of InAsP QD embedded in InP nanowire (radius ~67.7 nm) with the same growth temperature, as shown in Figure 1b–d. Because the nanowire aspect ratio and morphology are determined by competition between the radial and axial growth rate, this shows that the radial growth rate is suppressed by maintaining the growth temperature. According to Paiman et al. and Zhong et al., tapered nanowires are formed at high growth temperatures because the surface diffusion of adatom is kinetically favorable to incorporate in the nanowire by the vapor–solid mechanism under high growth temperature. Indeed, when the growth temperature of the upper InP segment is increased to 460 °C after forming InAsP QD, a slightly tapered InP shell can be overgrown on the InP nanowire surrounding the QD (Figure 1e), unlike the fully axial growth mode as shown in Figure 1d. The thin InP shell provides a 3D carrier confinement and passivation for the InAsP QD as schematically depicted in Figure 1f, which is employed as the structure for quantum emitters in this study. It is worth mentioning that, by controlling the growth condition, the InP shell can be grown to form a needle shape, which can be utilized in tailoring the output divergence angle to improve the collection efficiency to external optics. Here, even though we have achieved the integration of hybrid QD-NWs on a silicon platform, the growth yield in Figure 1b–e is relatively low. We envision that the growth yield on silicon can be improved by modifying the growth template and by tuning the growth condition (See Supporting Information Section IV for detailed discussion).

Atomic Structures of QD-NWs. The as-grown InP vertical nanowire with InAsP single QD is mechanically broken and transferred to a copper grid for scanning transmission electron microscopy (STEM) and energy-dispersive X-ray spectroscopy (EDS) analyses (see the Supporting Information for EDS analysis of the catalyst/nanowire interface). For gaining clear insights into InP/InAsP/InP interfaces as well as material compositions of InAsP QD, the nanowire without InP shell as depicted in Figure 1d is used in the material studies given that the shell can simultaneously generate the In and P counts during the EDS scan. Figure 2a shows the bright-field STEM image of a purely axial InP/InAsP/InP nanowire heterostructure with the diameter and length of approximately 63 nm and 1.26 μm, respectively. Several parasitic In droplets that are formed during the growth are shown together. The high-magnification STEM images of nanowire tips and single InAsP QD are shown in Figure 2b,c, in which the InP/InAsP/InP sharp material interfaces can be clearly observed. In order to confirm the interface quality, high-resolution STEM is performed to capture the InP/InAsP/InP interfaces, which are labeled with yellow arrows as shown in Figure 2d. The QD with a thickness of 6.94 nm is composed of 19 InAsP monolayers with an equal interspacing of 3.65 Å (d-spacing), and it is sandwiched by InP layers with an atomically abrupt interface.
Next, the two-dimensional elemental mapping of the InAsP QD region using the EDS technique is plotted as shown in Figure 2f–h. Figure 2e–h presents the high-angle annular dark-field scanning transmission electron microscopy (HAADF-STEM) image and the corresponding material distribution of As (red), P (cyan), and In (magenta) along the heterointerface, respectively. It can be clearly seen that In is spreading uniformly in the entire nanowire, whereas As is tightly appearing in the QD region replacing the P sites to form InAs$_{1-x}$P$_x$ with an estimated composition of $x = 0.6$, which corresponds to a bulk energy gap of $E_g \sim 0.724$ eV. It should be noted that the slight spreading of As is not caused by the material diffusion, but by an offset angle in the STEM rotational setup which is set to reduce the false signal collected from the TEM copper grid. With both results of STEM and EDS scanning, it is therefore confirmed that the InP/InAsP interfaces are atomically sharp without crystal defects, forming high-quality QDs. InP/InAsP QDs form type-I band alignment, and the emission wavelength of the InAsP QD can be tailored by both the ternary material composition and the QD size owing to the quantum confinement effect, which shows great promise in building wavelength-tunable non-classical quantum emitters.40

**Optical Properties of QD-NWs.** The optical properties of InAsP QDs in InP nanowires are probed using a cryogenic $\mu$-PL setup at 4K (see Supporting Information Section III for the detailed optical characterization setup). As seen in Figure 2a and in Supporting Information Figure S3, the silicon surface is partially contaminated by randomly formed droplets. As a consequence, it is critical to measure the spectrum of background and use it as a reference to subtract from the QD-NW emission spectra. Nevertheless, we did not observe any spectral features between 0.77 and 1.38 eV (900−1600 nm) within the scope of this study. Thus, we can assume that the optical emission is solely from the QD-NW segments. $\mu$-PL and time-resolved photoluminescence (TRPL) are measured from as-grown vertical nanowires without transferring them onto foreign substrates. In Figure 3a, a silicon-transparent emission at 1.109 eV is observed with a sharp linewidth of 344.7 $\mu$eV, indicating a discrete nature of energy states of the InAsP QDs. In addition, the emission with a larger energy splitting than the bulk energy gap of the InAsP QD can be tailoring by both the ternary material composition and the QD size owing to the quantum confinement effect, which shows great promise in building wavelength-tunable non-classical quantum emitters.40

Figure 3. (a) Emission spectra from the QD-NW at 4K. Inset: linewidth fitting of the peak. (b) TRPL measurement of the sharp emission at $E = 1.109$ eV with an extracted lifetime of 20.422 ns.

*Figure 4a* shows the power dependency of InAsP QD emission at 4K environment with the pump power increasing from 200 nW to 200 $\mu$W. Three extremely narrow peaks can be clearly seen starting at the excitation power of 20 $\mu$W. Here, we denote the two sharp peaks as an excitonic line (X) at $\sim 1.101$ eV and a biexcitonic line (XX) at $\sim 1.107$ eV, in which the X line and XX line exhibit the full width at half-maximum of 219.2 and 244.6 $\mu$eV by Lorentzian curve fitting. The measured excitonic and biexcitonic peak linewidths have similar orders compared to other reported nanowire QDs.27,39,43 However, these values are approximately 1-to-2 orders higher than that of the conventional planar self-assembled QDs.44 The linewidth broadening of the QDs in nanowires is typically caused by the fluctuation of charges in the vicinity of QDs.45 Such a phenomenon can be associated with the surface states and crystal defects such as stacking faults, creating a series of carrier trapping and detrapping process, which are often formed in bottom-up nanowires.66,47 Also, the strain induced by the native oxide shell could play a role in the spectral broadening of QDs.46 The intensity of the excitonic emission ($I_X$) and biexcitonic emission ($I_{XX}$) as a function of the excitation power ($P$) is plotted as shown in Figure 4b in double logarithmic scales. As the excitation power increases, the excitonic line starts to show intensity saturation when the biexcitonic line appears. $I_X$ has a sublinear dependency with the excitation power ($e^{P/2}$), whereas $I_{XX}$


has a superlinear dependency with the excitation power ($\propto P^{1.36}$). The exponent of $I_{XX}$ is $\sim 1.89$ times higher than that of $I_X$, which is a direct clue and signature property of biexciton and exciton transitions.

Lastly, the polarization properties of the QD emission lines are investigated at 4K. The measured polarization distributions and the fitting curves of the excitonic line (X) and the biexcitonic line (XX) are illustrated in Figure 4c. The emission from the vertical nanowire, denoted as $+z$ direction, is collected through a half waveplate without the presence of external fields. In general, the polarization property, determined by the orientation of dipole moments, contains $E_x$ and $E_y$ components in the in-plane direction of nanowire cross section. As reported elsewhere, nanowires with an elongated cross section can drastically increase the non-degeneracy of the two components. In other words, the polarization perpendicular to the nanowire axis generally exhibits very low degree of polarization. Thus, X and XX emission lines in Figure 4c from the vertical nanowire show a very weak intensity variation along the angle of linear polarization, which indicates that X and XX emission peaks are from the vertically standing and symmetric nanowire. With this regard, the vertical nanowire in this study not just provides the 3D quantum confinement, but also preserves the genuine and intrinsic optical properties of QDs without perturbing the polarizations, which would be an ideal platform for entangled photon generation as well as single photon generation.

**CONCLUSIONS**

In conclusion, we have demonstrated the monolithic integration of InAsP QDs in InP nanowires on silicon substrates using the VLS method. The atomic abruptness of the InP/InAsP interface formed by a pulsed growth mode is analyzed via the STEM and EDS technique, which is crucial in forming high-quality QDs. The signature of QDs with narrow optical transitions has been observed, and the cryogenic $\mu$-PL measurement clearly showed the exciton–biexciton transitions in the InAsP QDs. In addition, the exciton and biexciton emission wavelengths are potentially tunable by the QD size or ternary material composition in the silicon-transparent regime. This study provides a promising foundation for further developing site-controlled non-classical light sources for future quantum integrated technologies.

**EXPERIMENTAL SECTION**

**Au Nanodot Patterning for VLS Epitaxy.** Si(111) wafers are first cleaned by Piranha solution and 6:1 buffered oxide etchant solution. Next, nanohole patterns are defined on e-beam resist (PMMA 950A2) using Raith EBPG5000 ES e-beam writer, by a standard e-beam lithography process. After exposure and developing in MIBK/IPA = 1:3 solution, Au is deposited by e-beam evaporation with a nominal thickness of 15 nm. Finally, the Au nanodots are formed by the lift-off process using NMP (1-Methyl-2-pyrrolidin) and then washing with IPA.

**Nanowire Epitaxy.** The growth is carried out in a MOCVD reactor (Encore D-75) at 60 Torr using hydrogen as the carrier gas. TMIn, TBP, and TBAs are used as precursors for In, P, and As, respectively. After ramping up the temperature to the growth temperature of 420 °C without flowing any precursors, TBP and TMIn are simultaneously turned on to initiate the growth of InP segments. Then, the InAsP QDs are formed by shutting off TMIn and TBP and turning on TBAs, all at the same time, for 1 s. For the growth of upper InP segments, the growth temperature is kept at 420 °C for purely vertical growth or increased to 460 °C for sidewall passivation. After the growth, the reactor is cooled down with TBP flow to prevent the desorption of nanowires. Further details on the patterning, epitaxy, and characterization can be found in the Supporting Information.
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