

Coding for Transverse-Reads in Domain Wall Memories

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Abstract—Transverse-read is a novel technique to detect the number of ‘1’s stored in domain wall memory, also known as racetrack memory, without shifting any domains. Motivated by the technique, we propose a novel scheme to combine transverse-read and shift-operation such that we can reduce the number of shift-operations while still achieving high capacity. We also show that this scheme is helpful to correct errors in domain wall memory. A set of valid-words in this transverse-read channel is called transverse-read code. Our goal in this work is to study the transverse-read code.

We first present several properties of the transverse-read code and show that it is equivalent to a constrained code. Then, we compute maximal asymptotic rate of transverse-read codes for some certain parameters. Furthermore, we construct some codes achieving capacity with efficient encoding/decoding algorithms. Finally, we discuss their ability of correcting shift-errors in domain wall memory.

I. INTRODUCTION

Spintronic domain-wall memory (DWM), also referred as *racetrack memory*, is a promising candidate as a memory solution that can overcome the density limitations of spin-transfer torque magnetic memory (STT-MRAM), while still retaining its static energy benefits [1]–[4]. DWM is constructed from ferromagnetic nanowires, referred to as *tapes* or *racetracks*, which are separated into domains and are connected to a single or a few access transistors to create access ports. The state of the magnetic domains is accessed by *shifting* them along the nanowire and aligning the target domain to an access device. Unfortunately, due to process variation of deeply-scaled domain-wall memories [1], slight fluctuations in current combined with imperfections in the nanowires can cause faults in the shift process. These faults include over- and under-shifting of the tape, and thus for domain-wall memory to become viable, the shifting reliability must be addressed. As a result, several innovative approaches have been developed to detect and correct shift-errors in racetracks [5]–[9]. Besides that, the access latency and the energy consumption in racetrack memory depend on the average number of shift-operations. Several work have been done to reduce the number of shift operations in racetrack memory [10], [11].

Another approach to overcome the faults in the shifting process of DMW was proposed recently in [12]–[14]. In these work, a novel *transverse read* (TR) mechanism was developed in order to provide global information about the data stored within a nanowire. In particular, transverse read can detect the number of ones among the data stored in a DMW without shifting any domains, while still requiring ultra-low power. However, detecting only the number of ones in the DMW significantly reduces the information rate that can be stored within the memory. Hence, the authors of [14] also demonstrated how TR can be applied to partial segments of the nanowire, such as from an end to an access point or between two access points.

This enables a segmented TR which allows access to all of the bits of an arbitrarily long nanowire in several steps, while maintaining isolated current paths. While independently sensing several segments can increase the memory’s information rate, this increase is still far from reaching its full potential.

In this work, we propose a novel scheme that simultaneously combines the two important features of DWM. On one hand, we use transverse reads in order to sense the number of ones between two consecutive access points, and on the other hand we still shift all the domains so that we can transverse read to sense the number of ones in different segments every time. In general, we consider a message $\mathbf{x} = (x_1, \dots, x_n)$ of n information bits stored in n domains and consecutive access points such that each time we can transverse read a segment of length ℓ . That is, in the first read, the Hamming weight of the first length- ℓ segment x_1, \dots, x_ℓ is sensed. Next, we shift all domains in δ positions and sense the Hamming weight of the length- ℓ segment $(x_{\delta+1}, \dots, x_{\delta+\ell})$ in the second read. We keep shifting and sensing until the last segment $(x_{k\delta+1}, \dots, x_{k\delta+\ell})$ (for simplicity, we assume that there is an integer k such that $n = k\delta + \ell$). For example, we consider the case $n = 12$, $\delta = 2$, and $\ell = 4$. If $\mathbf{x} = (0, 0, 1, 0, 1, 0, 1, 1, 0, 0, 0, 0)$, the output in our reading scheme is $(1, 2, 3, 2, 0)$. There exist other vectors, for example $\mathbf{y} = (0, 0, 0, 1, 0, 1, 1, 1, 0, 0, 0, 0) \neq \mathbf{x}$, that have the same output $(1, 2, 3, 2, 0)$. Hence, we may not obtain the full capacity using this scheme. First, we observe that the information rate in this scheme depends on δ and ℓ . For example, when $\delta = \ell = 2$, we can compute that the information rate is about 79.25%. Then, we observe that this scheme significantly reduces the number of shift-operations by about δ times. For example, when $\delta = 2$, if we just shift normally about $n/2$ times, we can only read 50% of information bits but using this scheme, we can achieve at least 79.25%. Our first question of interest is whether we can achieve higher information rate. Hence, we are interested in finding the trade-off between the number of shift-operations and the maximal information rate in this scheme. Furthermore, we can show that this scheme is also helpful to correct shift-errors in racetrack memories. From practical point of view, this scheme captures the two features of DMW in order to significantly reduce the number of shift operations and mitigate the shift errors, while still supporting high information rates. From theoretical point of view, it pose some interesting challenges in combinatorics and algorithms.

In Section II, we present some necessary notations and define the codes formally. In Section III, we study the transverse-read codes: properties, maximal asymptotic rates and constructions. Then, in Section IV, we show that our scheme of using transverse-read codes is helpful to correct shift-errors in domain wall memories. Finally, in Section V, we summarise our contributions in this work and discuss some work in near future.

II. DEFINITIONS AND PRELIMINARIES

Let \mathbb{F}_q denote the q -ary finite field and $[n]$ denote the set $\{1, 2, \dots, n\}$. For each sequence $\mathbf{u} = (u_1, \dots, u_n) \in \mathbb{F}_q^n$, let $\mathbf{u}_{[i:k]} = (u_i, u_{i+1}, \dots, u_{i+k-1})$, $1 \leq i \leq n - k + 1$, denote a length- k substring of \mathbf{u} . The weight of vector \mathbf{u} is $w(\mathbf{u}) = \sum_{i=1}^n u_i$. When $q = 2$, the weight of a binary vector is the number of 1's in the vector. A q -ary code \mathcal{C} of length n is a set of q -ary sequences of length n , that is $\mathcal{C} \subseteq \mathbb{F}_q^n$. For each code \mathcal{C} of length n , we define the rate of the code \mathcal{C} to be $R(\mathcal{C}) = \log_q(|\mathcal{C}|)/n$, where $|\mathcal{C}|$ is the size of the code \mathcal{C} .

Definition 1. Let n, ℓ, δ, k be integers such that $n - \ell = k\delta$.

- The **(ℓ, δ) -transverse-read vector** of a length- n word $\mathbf{x} = (x_1, \dots, x_n) \in \mathbb{F}_2^n$ is the vector $TR_{\ell, \delta}(\mathbf{x}) = (w(\mathbf{x}_{[1:\ell]}), w(\mathbf{x}_{[\delta+1:\ell]}), \dots, w(\mathbf{x}_{[k\delta+1:\ell]})) \in \mathbb{F}_{\ell+1}^{k+1}$, where $w(\mathbf{x}_{[i\delta+1:\ell]})$ is the weight of the length- ℓ substring $\mathbf{x}_{[i\delta+1:\ell]}$.
- A code $\mathcal{C}(n, \ell, \delta) \subseteq \mathbb{F}_2^n$ is called a **binary (ℓ, δ) -transverse-read code** if for all distinct $\mathbf{x}, \mathbf{y} \in \mathcal{C}(n, \ell, \delta)$, it holds that $TR_{\ell, \delta}(\mathbf{x}) \neq TR_{\ell, \delta}(\mathbf{y})$.
- The largest size of a length- n binary (ℓ, δ) -transverse-read code will be denoted by $A(n; \ell, \delta)$ and the maximal asymptotic rate for fixed ℓ and δ is given by

$$\mathcal{R}(\ell, \delta) = \limsup_{n \rightarrow \infty} \frac{\log_2(A(n; \ell, \delta))}{n}.$$

Note that it is also possible to define the cyclic version of these transverse-read vectors, however for now we prefer the more practical noncyclic version. In this work, we always assume that $n - \ell = k\delta$, ℓ and δ are fixed while n and k tend to infinity.

We now observe that for each word $\mathbf{x} \in \mathbb{F}_2^n$, we always find its transverse read vector $TR_{\ell, \delta}(\mathbf{x}) \in \mathbb{F}_{\ell+1}^{k+1}$. However, given a vector $\mathbf{u} \in \mathbb{F}_{\ell+1}^{k+1}$, there may not exist any binary word $\mathbf{x} \in \mathbb{F}_2^n$ that $\mathbf{u} = TR_{\ell, \delta}(\mathbf{x})$.

Definition 2. Let n, ℓ, δ, k be integers such that $n - \ell = k\delta$.

- A vector $\mathbf{u} \in \mathbb{F}_{\ell+1}^{k+1}$ is called a **valid (ℓ, δ) -transverse-read vector** if there exists a binary word $\mathbf{x} \in \mathbb{F}_2^n$ that $\mathbf{u} = TR_{\ell, \delta}(\mathbf{x})$.
- A set of such vectors \mathbf{u} is called a **valid (ℓ, δ) -transverse-read code**.

From Definitions 1 and 2, we can easily obtain the following result.

Proposition 3.

- Let \mathcal{C} be a binary (ℓ, δ) -transverse-read code and $TR_{\ell, \delta}(\mathcal{C}) = \{TR_{\ell, \delta}(\mathbf{c}) : \mathbf{c} \in \mathcal{C}\} \subset \mathbb{F}_{\ell+1}^{k+1}$. So, $TR_{\ell, \delta}(\mathcal{C})$ is a valid (ℓ, δ) -transverse-read code and $|TR_{\ell, \delta}(\mathcal{C})| = |\mathcal{C}|$.
- Let $TR(k, \ell, \delta)$ denote the set of all valid (ℓ, δ) -transverse-read vectors of length k . Then, $|TR(k, \ell, \delta)| = A(n, \ell, \delta)$.

We now examine a model of domain wall memory of n domains stored a binary word of length n , and two consecutive access points that can transverse read to sense the weight of a segment of length ℓ . A shift operation in racetrack memory shift all domains together δ positions. So, if $\mathbf{x} = (x_1, \dots, x_n) \in \mathbb{F}_2^n$ is a stored word, the output in our reading scheme is $TR_{\ell, \delta}(\mathbf{x})$. Using the new scheme, we can reduce the number of shift-operations by about δ times. However, given δ and ℓ , the maximal information rate in racetrack memories is $\mathcal{R}(\ell, \delta)$, which

may not achieve the full capacity. Hence, in this work, we are interested in finding the maximal size $A(n, \ell, \delta)$ and the maximal asymptotic rate $\mathcal{R}(\ell, \delta)$. Given δ , we are also interested in finding the optimal ℓ such that the asymptotic rate $\mathcal{R}(\ell, \delta)$ is maximal. Furthermore, we also seek for some constructions of (ℓ, δ) -transverse-read codes with efficient encoding/decoding algorithms.

Besides that, both shift-operation and transverse-read may not work perfectly and errors may occur. It is known that the shift-errors can be modelled as synchronizations, including sticky-insertions and deletions. We also see that errors in transverse-read may cause some substitution errors. Hence, in this work, we also study some transverse-read codes which can correct shift-errors and substitutions errors.

III. TRANSVERSE-READ CODES

In this section, given ℓ, δ , we study (ℓ, δ) -transverse-read codes, their properties and aim to find the maximal asymptotic rate of these codes. We are also interested in constructing these codes with efficient encoding/decoding algorithms.

To study the values of $A(n; \ell, \delta)$ and $\mathcal{R}(\ell, \delta)$, we may consider the maximal valid (ℓ, δ) -transverse-read code $TR(k, \ell, \delta)$ since $|TR(k, \ell, \delta)| = A(n; \ell, \delta)$. We first present several basic results on $A(n; \ell, \delta)$ and $\mathcal{R}(\ell, \delta)$ in the following theorem.

Theorem 4.

- 1) For $\ell = 1$, it holds that $A(n; \ell = 1, \delta) = 2^{\frac{n-1}{\delta}+1}$ and $\mathcal{R}(\ell = 1, \delta) = 1/\delta$.
- 2) For $\ell = \delta$, it holds that $A(n; \ell, \delta = \ell) = (\ell + 1)^{n/\ell}$ and $\mathcal{R}(\ell, \delta = \ell) = \frac{\log_2(\ell+1)}{\ell}$.
- 3) For $\ell \leq \delta$, it holds that $A(n; \ell, \delta) = (\ell + 1)^{\frac{n-\ell}{\delta}+1}$ and $\mathcal{R}(\ell, \delta) = \frac{\log_2(\ell+1)}{\delta}$.
- 4) For $\delta = 1$ and some constant ℓ , it holds that $A(n; \ell, \delta = 1) \geq 2^{n-\ell}$ and $\mathcal{R}(\ell, \delta = 1) = 1$.

Proof:

- 1) To prove Claim 1 for $\ell = 1$ and $k = (n-1)/\delta$, we consider a vector $\mathbf{x} = (x_1, \dots, x_n) \in \mathbb{F}_2^n$ and its transverse-read vector $TR_{\ell, \delta}(\mathbf{x}) = (x_1, x_{\delta+1}, \dots, x_{k\delta+1}) \in \mathbb{F}_{\ell+1}^{k+1}$. We observe that any vector $\mathbf{u} \in \mathbb{F}_2^{k+1}$, \mathbf{u} is a valid (ℓ, δ) -transverse read vector. Hence, $A(n, \ell = 1, \delta) = |TR(k, \ell = 1, \delta)| = 2^{k+1}$ and thus $\mathcal{R}(\ell = 1, \delta) = \lim_{n \rightarrow \infty} \frac{k+1}{n} = \frac{1}{\delta}$.
- 2) For $\ell = \delta$, and $k = (n/\delta) - 1$, given $\mathbf{x} = (x_1, \dots, x_n) \in \mathbb{F}_2^n$, $TR_{\ell, \delta}(\mathbf{x}) = (w(\mathbf{x}_{[1:\ell]}), w(\mathbf{x}_{[\ell+1:\ell]}), \dots, w(\mathbf{x}_{[k\ell+1:\ell]})) \in \mathbb{F}_{\ell+1}^{k+1}$. Since all segments $\mathbf{x}_{[i\ell+1:\ell]}$, for $0 \leq i \leq k$, are non-overlap, any vector $\mathbf{u} \in \mathbb{F}_{\ell+1}^{k+1}$ is a valid (ℓ, ℓ) -transverse read vector. Hence $A(n, \ell, \delta = \ell) = |TR(k, \ell, \ell)| = (\ell + 1)^{k+1} = (\ell + 1)^{n/\ell}$ and thus $\mathcal{R}(\ell, \delta = \ell) = \lim_{n \rightarrow \infty} \frac{(\ell+1)(\log_2(\ell+1))}{n} = \frac{\log_2(\ell+1)}{\delta}$.
- 3) Using the same argument as in part 2), note that all segments $\mathbf{x}_{[i\delta+1:\ell]}$, for $0 \leq i \leq k$, are non-overlap, the claim is proven.
- 4) To prove Claim 4, we consider two length- n vectors $\mathbf{u} = (0, \dots, 0, u_1, \dots, u_{n-\ell}) \in \mathbb{F}_2^n$ and $\mathbf{v} = (0, \dots, 0, v_1, \dots, v_{n-\ell}) \in \mathbb{F}_2^n$ such that $\mathbf{u} \neq \mathbf{v}$. We observe that $TR_{\ell, \delta=1}(\mathbf{u}) \neq TR_{\ell, \delta=1}(\mathbf{v})$. Let $\mathcal{C}(n, \ell, \delta)$ be a set of all vectors of length n that the first ℓ entries are zeros. So,

$\mathcal{C}(n, \ell, \delta)$ is a binary $(\ell, \delta = 1)$ -transverse-read code and $|\mathcal{C}(n, \ell, \delta = 1)| = 2^{n-\ell}$. Therefore, $A(n, \ell, \delta = 1) \geq 2^{n-\ell}$ and $\mathcal{R}(\ell, \delta = 1) = \lim_{n \rightarrow \infty} \frac{n-\ell}{n} = 1$. \blacksquare

For all cases in Theorem 4, we can find the maximal asymptotic rate of (ℓ, δ) -transverse-read codes. In the rest of the paper, we focus on the more challenging cases when $1 < \delta < \ell$. Let us start with the case where $\delta = 2$ and even values of ℓ , which will be addressed in the following theorem.

Theorem 5. For $\delta = 2$ and ℓ even, it holds that

$$\mathcal{R}(\ell, \delta = 2) = \frac{\log_2 3}{2} \approx 0.7925.$$

Proof: Let $n_1 = n/2$ and $\ell_1 = \ell/2$ be two positive integers. Given a vector $\mathbf{x} = (x_1, \dots, x_n) \in \mathbb{F}_2^n$, let $f(\mathbf{x}) = (f_1, \dots, f_{n_1}) \in \mathbb{F}_3^{n_1}$ where $f_i = x_{2i-1} + x_{2i} \in \{0, 1, 2\}$ for $1 \leq i \leq n_1$. We see that $TR_{\ell, \delta=2}(\mathbf{x}) = (w(f_{[1:\ell_1]}), w(f_{[\ell_1+1:\ell_1]}), \dots, w(f_{[k\ell_1+1:\ell_1]}) \in \mathbb{F}_{\ell+1}^{k+1}$. Let $\mathcal{C}(n, \ell, \delta)$ be a binary (ℓ, δ) -transverse-read code, that is, for two different vectors $\mathbf{x}, \mathbf{y} \in \mathcal{C}(n, \ell, \delta)$, it holds that $TR_{\ell, \delta}(\mathbf{x}) \neq TR_{\ell, \delta}(\mathbf{y})$. Hence, $f(\mathbf{x}) \neq f(\mathbf{y})$. So, $|\mathcal{C}(n, \ell, \delta)| \leq |\mathbb{F}_3^{n_1}| = 3^{n_1}$, for any (ℓ, δ) -transverse-read code $\mathcal{C}(n, \ell, \delta)$. Therefore, $A(n, \ell, \delta) \leq 3^{n/2}$, and thus $\mathcal{R}(\ell, \delta = 2) \leq \frac{\log_2 3}{2} \approx 0.7925$.

On the other hand, we can construct a binary (ℓ, δ) -transverse-read code $\mathcal{C}(n, \ell, \delta)$ as follows. Let $\mathcal{F} \subseteq \mathbb{F}_3^{n_1}$ be a set of all ternary vectors of length n_1 such that their first ℓ_1 entries are all zeros. So, $|\mathcal{F}| = 3^{n_1 - \ell_1}$. For each $f = (f_1, \dots, f_{n_1}) \in \mathcal{F}$, we define $f^{-1} = \mathbf{x} = (x_1, \dots, x_n) \in \mathbb{F}_2^n$ such that $(x_{2i-1}, x_{2i}) = (0, 0)$ if $f_i = 0$, $(x_{2i-1}, x_{2i}) = (0, 1)$ if $f_i = 1$ and $(x_{2i-1}, x_{2i}) = (1, 1)$ if $f_i = 2$. Let $\mathcal{C}(n, \ell, \delta)$ be a set of all vectors $\mathbf{x} = f^{-1}$ defined above where $f \in \mathcal{F}$. So, $|\mathcal{C}(n, \ell, \delta)| = |\mathcal{F}| = 3^{n_1 - \ell_1}$. Moreover, we can see that $\mathcal{C}(n, \ell, \delta)$ is a binary (ℓ, δ) -transverse-read code. Therefore, $A(n, \ell, \delta) \geq 3^{n_1 - \ell_1}$ and thus $\mathcal{R}(\ell, \delta) \geq \frac{\log_2 3}{2}$.

In conclusion, we obtain $\mathcal{R}(\ell, \delta = 2) = \frac{\log_2 3}{2} \approx 0.7925$. The theorem is proven. \blacksquare

The results in Lemma 5 can be extended in the following theorem for arbitrary values of ℓ and δ , where ℓ is a multiple of δ .

Theorem 6. If ℓ is a multiple of δ , then

$$\mathcal{R}(\ell, \delta) = \frac{\log_2(\delta + 1)}{\delta}.$$

Proof: To prove Theorem 6, we can follow the same argument as in the proof of Theorem 5. \blacksquare

Next, we continue with $\delta = 2$ and odd values of ℓ . The result in Theorem 5 gives us a lower bound on the maximal asymptotic rate of (ℓ, δ) -transverse-read codes for $\delta = 2$. We state the result formally in the following theorem.

Theorem 7.

$$\mathcal{R}(\ell, \delta = 2) \geq \frac{\log_2 3}{2} \approx 0.7925.$$

Proof: To prove Theorem 7, we present a construction of a (ℓ, δ) -transverse-read code. Let $k = (n - \ell)/2$ and $\mathbf{u} = (u_1, \dots, u_k) \in \mathbb{F}_3^k$ be a ternary vector of length k . Let

$\mathbf{g}(\mathbf{u}) = \mathbf{c} = (c_1, \dots, c_n) \in \mathbb{F}_2^n$ such that $c_1 = \dots = c_\ell = 0$ and for $1 \leq i \leq k$, $(c_{\ell+2i-1}, c_{\ell+2i}) = (0, 0)$ if $u_i = 0$, $(c_{\ell+2i-1}, c_{\ell+2i}) = (0, 1)$ if $u_i = 1$, and $(c_{\ell+2i-1}, c_{\ell+2i}) = (1, 1)$ if $u_i = 2$. Let $\mathcal{C}(n, \ell, \delta = 2) = \{g(\mathbf{u}) : \mathbf{u} \in \mathbb{F}_3^k\}$. It is possible to show that if $\mathbf{u} \neq \mathbf{v}$ then $g(\mathbf{u}) \neq g(\mathbf{v})$ for any $\mathbf{u}, \mathbf{v} \in \mathbb{F}_3^k$. Hence, $|\mathcal{C}(n, \ell, \delta = 2)| = |\mathbb{F}_3^k| = 3^k$. Moreover, if $g(\mathbf{u}) \neq g(\mathbf{v})$ then $TR_{\ell, \delta=2}(g(\mathbf{u})) \neq TR_{\ell, \delta}(g(\mathbf{v}))$. Thus, the code $\mathcal{C}(n, \ell, \delta = 2)$ constructed above is a (ℓ, δ) -transverse-read code. Hence, $A(n, \ell, \delta = 2) \geq 3^k$ and thus $\mathcal{R}(\ell, \delta = 2) \leq \frac{\log_2 3}{2} \approx 0.7925$ for any $\ell \geq \delta = 2$. \blacksquare

From the above proof of Theorem 7, we can find a construction of a $(\ell, \delta = 2)$ -transverse-read code with efficient encoding algorithms. Similar, we can extend the result in Theorem 7 for arbitrary values of ℓ and δ that $\ell > \delta$. We first present a simple construction of a binary (ℓ, δ) -transverse-read code.

Construction 8. Let $k = \frac{n-\ell}{\delta}$ and $\mathbf{u} = (u_1, \dots, u_k) \in \mathbb{F}_{\delta+1}^k$ be a $(\delta + 1)$ -ary vector of length k . Let $g(\mathbf{u}) = \mathbf{c} = (c_1, \dots, c_n) \in \mathbb{F}_2^n$ such that $c_i = 0$ for $1 \leq i \leq \ell$ and for $1 \leq i \leq k$, $c_{[\ell+\delta(i-1)+1; \delta]} \in \mathbb{F}_{\delta+1}^{\delta}$ is a subvector of length δ such that its first $\delta - j$ entries are 0 and its last j entries are 1 if $u_i = j$. Let $\mathcal{C}(n, \ell, \delta) = \{g(\mathbf{u}) : \mathbf{u} \in \mathbb{F}_{\delta+1}^k\}$.

It is possible to show that the code $\mathcal{C}(n, \ell, \delta)$ constructed above is a binary (ℓ, δ) -transverse-read code since for any $\mathbf{u}, \mathbf{v} \in \mathcal{C}(n, \ell, \delta)$, then $g(\mathbf{u}) \neq g(\mathbf{v})$, and thus $TR_{\ell, \delta}(\mathbf{u}) \neq TR_{\ell, \delta}(\mathbf{v})$. Moreover, $|\mathcal{C}(n, \ell, \delta)| = |\mathbb{F}_{\delta+1}^k| = (\delta + 1)^k$. So, $A(n, \ell, \delta) \geq (\delta + 1)^k$. Therefore, we obtain the following result on the lower bound of the maximal asymptotic rate of (ℓ, δ) -transverse-read codes.

Theorem 9. If ℓ and δ are two integers such that $\ell > \delta > 1$ then

$$\mathcal{R}(\ell, \delta) \geq \frac{\log_2(\delta + 1)}{\delta}.$$

From Construction 8, there is a binary (ℓ, δ) -transverse-read code with an efficient encoding algorithm.

In the rest of this section, we present a technique to find the asymptotic rates of (ℓ, δ) -transverse-read codes exactly, given $\ell > \delta > 1$. To find the asymptotic rate of the above codes, we first prove that these codes are equivalent to a class of constrained codes avoiding some specific patterns and a class of regular languages. Then, we can use some known techniques in constrained codes and regular languages using finite state machines to compute the maximal asymptotic rates. We first consider the case $\ell = 3$ and $\delta = 2$. We recall that $A(n, \ell, \delta) = |\mathcal{TR}(k, \ell, \delta)|$ where $\mathcal{TR}(k, \ell, \delta)$ is the set of all valid (ℓ, δ) -transverse-read vectors of length $k + 1$. Let $\mathbf{u} = (u_1, \dots, u_k) \in \mathcal{TR}(k, \ell = 3, \delta = 2) \subseteq \mathbb{F}_4^{k+1}$ be a valid $(\ell = 3, \delta = 2)$ -transverse-read vector. So, there exists a vector $\mathbf{x} \in \mathbb{F}_2^n$ such that $TR_{\ell, \delta}(\mathbf{x}) = \mathbf{u}$. Then, for each $1 \leq i \leq k$, $u_i = x_{2i-1} + x_{2i} + x_{2i+1} \in \{0, 1, 2, 3\}$. We can view u_i as a path from x_{2i-1} to x_{2i+1} . Here, x_{2i-1} is called a starting point of u_i and x_{2i+1} is called an ending point of u_i . So, a starting point of u_i is also an ending point of u_{i-1} . We observe that $\mathcal{TR}(k, \ell = 3, \delta = 2)$ is a regular language. It is recognized by a non-deterministic state machine as in Figure 1, where node j is the state that the ending point of u_{i-1} is $x_{2i-1} = j$ for $j = 1, 2$. If $x_{2i-1} = 0$ and the ending point of u_i is $x_{2i+1} = 0$, then $u_i = 0$ or $u_i = 1$.

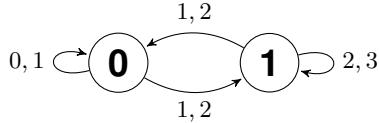


Fig. 1: Non-deterministic finite state transition diagram $\ell = 3, \delta = 2$

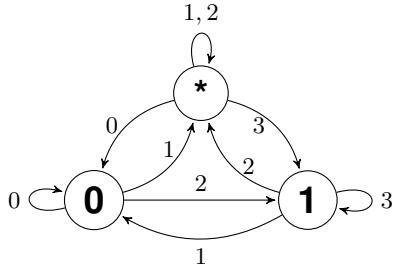


Fig. 2: Deterministic finite state transition diagram $\ell = 3, \delta = 2$

Hence, from the state 0, if we write $u_i = 0$ or $u_i = 1$ then we may still stay in the same state 0. If $x_{2i-1}=0$ and the ending point of u_i is $x_{2i+1} = 1$, then $u_i = 1$ or $u_i = 2$. Hence, from the state 0, if we write $u_i = 1$ or $u_i = 2$ then we may move to the new state 1. We also note that, from state 0, if we write $u_i = 1$, we may stay in the same state or move to the new state. Hence, the state machine in Figure 1 is a non-deterministic finite state machine. We note that, for any regular language which can be recognized by a non-deterministic finite state machine, it can be expressed by a deterministic state machine. In this case, the regular language $TR(k, \ell, \delta)$ is recognized by a deterministic finite state machine as in Figure 2. In this diagram, we have a new node “*” which is the state that x_{2i-1} can be 0 or 1. The adjacency matrix of this deterministic diagram is:

$$A_G = \begin{pmatrix} 1 & 1 & 1 \\ 1 & 2 & 1 \\ 1 & 1 & 1 \end{pmatrix}$$

So, using the well-known Perron-Frobenius theory [16], we can find exactly the maximal asymptotic rate of $(\ell = 3, \delta = 2)$ -transverse-read codes which is $(\log_2 \lambda)/2 = 0.8858$ where $\lambda = 3.4142$ is the largest real eigenvalue of A_G .

Besides that, $TR(k, \ell, \delta)$, which can be expressed by the state machine in Figure 2, is also a constrained system. We state the following result.

Theorem 10. We consider the following set

$$\mathcal{F} = \{(3, (1, 2)^i, 0), (3, (1, 2)^i, 1, 3), (0, (2, 1)^i, 3), (0, (2, 1^i), 2, 0)\}.$$

A valid $(\ell = 3, \delta = 2)$ -transverse-read code is a constrained code avoiding all patterns in \mathcal{F} .

Theorem 10 can be proven by showing that both above codes have the same finite state transition diagram as in Figure 2.

Furthermore, it is possible to extend the above results for other values of $\ell > \delta > 1$. For example, when $\ell = 5$ and $\delta = 2$, we can build a non-deterministic finite state machine of $(\ell = 5, \delta = 2)$ as in Figure 3. In this diagram, each node is a state that a length-3 substring is started by the corresponding length-3 substring. If a length-5 string starts by $(0,0,0)$ and its weight is 1, it may end by $(0,1,0)$ or $(0,0,1)$. If its weight

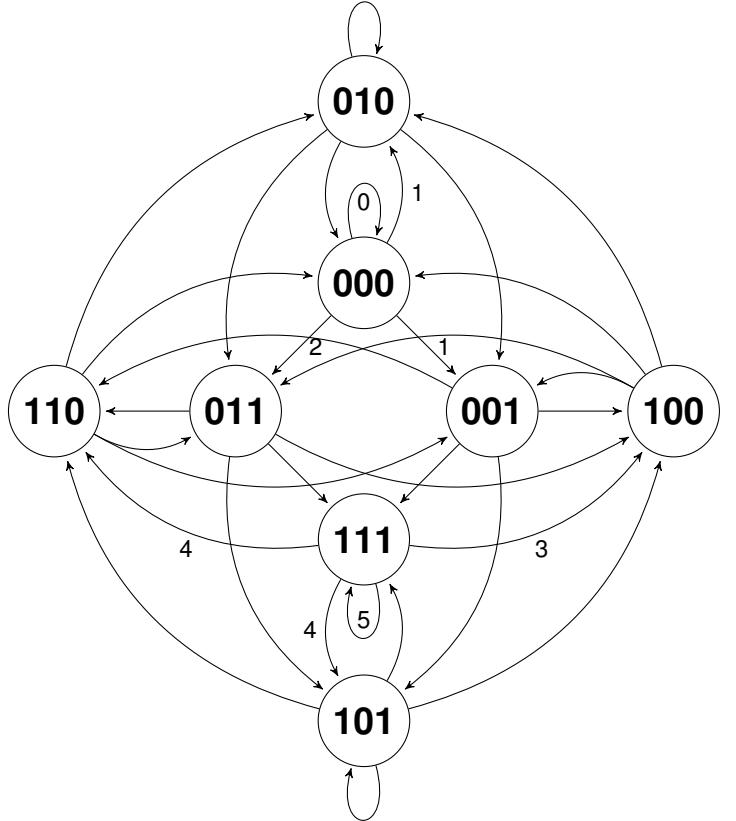


Fig. 3: Non-deterministic finite state transition diagram $\ell = 5, \delta = 2$.

is 0, it must end by $(0,0,0)$ and if its weight is 2, it must end by $(0,1,1)$. Similar, we can build a non-deterministic finite state machine. For simplicity, in Figure 3, we only label all edges go out from nodes $(0,0,0)$ and $(1,1,1)$. Once we have a non-deterministic finite state machine, we can build a deterministic finite state machine and compute the maximal asymptotic rate of transverse-read codes. Several numerical results were computed and tabulated in Table I.

TABLE I: The maximal asymptotic rates of (ℓ, δ) -transverse-read codes.

	$\ell = 3$	$\ell = 4$	$\ell = 5$	$\ell = 6$	$\ell = 7$	$\ell = 8$
$\delta = 2$	0.8857	0.7925	0.9258	0.7925	0.9361	0.7925

From the results in Table I, we see that $0.936 = TR_{\ell=7,\delta=2} > TR_{\ell=5,\delta=2} > TR_{\ell=3,\delta=2} > TR_{\ell=2,\delta=2} = 0.795$. So, using our scheme, even we reduce the number of shift-operations to 50%, we still can achieve the information rate at least 93.6%. Since the asymptotic rates of $(\ell, \delta = 2)$ are increasing when ℓ is odd and increasing, we are interested in finding the maximal asymptotic rates $\mathcal{R}(\ell, \delta = 2)$ for odd ℓ .

Furthermore, since we can build a deterministic finite state machine of (ℓ, δ) -transverse-read code, it is possible to construct this code with efficient encoding/decoding algorithms using well-known finite state splitting algorithms [16]. In the following section, we will study the ability of correcting shift-errors and substitution-errors of these codes.

IV. TRANSVERSE-READ CODES CORRECTING ERRORS

In this section, we discuss about the ability of detecting and correcting errors of these codes. There are two types of errors in this model: shift-errors and substitution errors.

Shift-errors, which may occur when all domains are shifted, can be modelled as sticky-insertions or deletions. Normally, we may need to use some classical deletion correcting codes or use multiple heads to correct these errors. In this work, we show that some transverse-read codes have special properties that are useful for correcting these shift-errors. Let us consider a vector $\mathbf{x} = (x_1, x_2, x_3, x_4, x_5) = (0, 0, 1, 1, 0)$ and its transverse-read vector $TR_{2,1}(\mathbf{x}) = (x_1 + x_2, x_2 + x_3, x_3 + x_4, x_4 + x_5) = (0, 1, 2, 1)$. Once an over-shift occurs, a symbol in $TR_{2,1}(\mathbf{x})$ is deleted and we may obtain an invalid word. For example, an over-shift occurs in the second position and symbol $x_2 + x_3 = 1$ is deleted, we obtain the word $(0, 2, 1)$. However, the word $(0, 2, 1)$ is not a valid $(2,1)$ -transverse-read vector since 0 can not be followed by 2. Hence, we can detect and locate a single deletion in this case. Based on this simple observation, we are able to design a code correcting t deletions, where there is no consecutive deletions, with at most $t \log(n) + o(\log n)$ bits of redundancies. For simplicity, we first present the result for $t = 1$.

Theorem 11. *Let $\mathcal{C}_1 \subset \mathbb{F}_2^n$ be a binary code correcting a single sticky-deletion. The code \mathcal{C}_1 can correct a single deletion in the $(2,1)$ -transverse-read code. That is, if a deletion occurs in a transverse-read vector $TR_{2,1}(\mathbf{c})$ where $\mathbf{c} \in \mathcal{C}_1$, we can recover the original word \mathbf{c} .*

Proof: Let $\mathbf{c} = (c_1, c_2, \dots, c_n) \in \mathcal{C}_1$ be a stored word. Thus $\mathbf{u} = TR_{2,1}(\mathbf{c}) = ((c_1 + c_2), (c_2 + c_3), \dots, (c_{n-1} + c_n))$, where $u_i = c_i + c_{i+1}$, is its $(2,1)$ -transverse-read vector. We observe that in a valid $(2,1)$ -transverse-read vector, the run of 1's has odd length if it is bounded by two different symbols, that is $(0, 1, \dots, 1, 2)$ or $(2, 1, \dots, 1, 0)$, and the run of 1's has even length if it is bounded by the same symbol, that is $(0, 1, \dots, 1, 0)$ or $(2, 1, \dots, 1, 2)$. Hence, if a symbol 1 is deleted in the valid $(2,1)$ -transverse-read vector, we can detect and locate the error and thus correct it. We now consider the case that a symbol 0 or 2 is deleted. Note that if $u_i = 0$ then $c_i = c_{i+1} = 0$ and if $u_i = 2$ then $c_i = c_{i+1} = 1$. Hence, if a symbol 0 or 2 is deleted in the transverse-read vector \mathbf{u} , a sticky-deletion occurs in the stored word \mathbf{c} . Since $\mathbf{c} \in \mathcal{C}_1$ which can correct a single sticky-deletion, we can correct the error and recover the original word \mathbf{c} . Hence, in any case, we can recover the stored word \mathbf{c} . Therefore, code \mathcal{C}_1 can correct a single deletion in a $(2,1)$ -transverse-read code. ■

It is known that correcting a sticky-deletion is easier than correcting a deletion. Hence, the transverse-read code is helpful in correcting a deletion (shift-error). It is interesting that we also can extend the result for code correcting multiple deletions.

Theorem 12. *Given $t > 1$, let \mathcal{C}_t be a code of length n correcting t sticky-deletions. If there are at most t deletions in a $(2,1)$ -transverse-read vector $TR_{2,1}(\mathbf{c})$ where $\mathbf{c} \in \mathcal{C}_t$ such that there does not exist two-consecutive deletions then we can recover the original word \mathbf{c} .*

Proof: Let us sketch the main idea of the proof of this theorem. To prove the theorem, we just need to follow the argument in the proof of Theorem 11. If a symbol 1 is deleted in the transverse-read vector $TR_{2,1}(\mathbf{c})$, we can detect and correct this error immediately. If a symbol 0 or 2 is deleted in the transverse-read vector, we see that a sticky-deletion occurs in the original word \mathbf{c} . Then, we use a decoder of the code \mathcal{C}_t , which can correct multiple sticky-deletion, to correct these errors. ■

So far, we showed that our scheme of using (ℓ, δ) -transverse-read code is helpful to correct shift-errors for $\ell = 2$ and $\delta = 1$. The main idea is to use codes correcting sticky-deletion to correct deletions, using some special properties of $(2,1)$ -transverse-read codes. This idea is presented in [17] for codes correcting deletions in symbol-pair read channel. We note that, the best known results on codes correcting t deletions require at least $8t \log n + o(t \log n)$ bits of redundancy while it is possible to correct t sticky-deletions using only $t \log n + o(\log n)$ bits of redundancy, given a constant t . Hence, in our scheme for $\ell = 2$ and $\delta = 1$, it is easier to correct shift-errors. The results for other values of ℓ and δ are in our interests and will be studied in the full version of this work.

Besides that, a substitution error occurs when there is a mistake in transverse read and a symbol is read wrongly. For example, $\mathbf{x} = (0, 0, 1, 1, 0)$ and $TR_{2,1}(\mathbf{x}) = (0, 1, 2, 1)$. If a third symbol in the transverse-read vector $TR_{2,1}(\mathbf{x})$ is wrong then we obtain the vector $(0, 1, 0, 1)$ which is invalid. Moreover, we can locate an error in the pattern $(0, 1, 0)$. It is helpful to correct a substitution error with large magnitude. For a substitution error with small magnitude, we propose to study a coding scheme to combine our transverse-read code with the well-known limited-magnitude error correcting code [15]. We also can show that our scheme for $\ell = 2$ and $\delta = 1$ is helpful to correct a single limited magnitude error. These schemes will be discussed in the full version of our work.

V. CONCLUSION AND DISCUSSION

In this work, we propose a new scheme of reading information in domain wall memories to reduce the number of shift-operations while still achieving the high information rate. We introduce a new family of codes, called (ℓ, δ) -transverse-read codes and study their properties, maximal asymptotic rates and constructions. Furthermore, we also show that our scheme of using these transverse-read codes are helpful to correct shift-errors in domain wall memories. In the full version of our work, we also present some encoding/decoding algorithms in details. The ability of transverse-read codes in correcting substitution-errors will be studied in near future. Furthermore, we are also interested in finding the maximal asymptotic rates of (ℓ, δ) -transverse-read codes for other values of ℓ and δ .

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REFERENCES

- [1] S.S. Parkin, M. Hayashi, and L. Thomas, "Magnetic domain-wall racetrack memory," *Science*, vol. 320, no. 5873, pp. 190–194, 2008.
- [2] Z. Sun, W. Wu, and H. Li, "Cross-layer racetrack memory design for ultra high density and low power consumption," in *Design Automation Conference (DAC)*, pp. 1–6, May 2013.
- [3] S. Parkin and S. H. Yang, "Memory on the racetrack," *Nature Nanotech.*, vol. 10, no. 3, pp. 195–198, 2015.
- [4] R. Blasing, A. Ali Khan, P. Filippou, C. Garg, F. Hameed, J. Castrillon, and S. Parkin, "Magnetic racetrack memory: From physics to the cusp of applications within a decade", *Proc. of the IEEE*, vol. 18, no. 10, pp. 1303–1321, 2020.
- [5] C. Zhang, G. Sun, X. Zhang, W. Zhang, W. Zhao, T. Wang, Y. Liang, Y. Liu, Y. Wang, and J. Shu, "Hi-fi playback: Tolerating position errors in shift operations of racetrack memory," *2015 ACM/IEEE 42nd Annual International Symposium on Computer Architecture (ISCA)*, pp. 694–706, 2015.
- [6] Y.M. Chee, H.M. Kiah, A. Vardy, V.K. Vu, and E. Yaakobi, "Coding for racetrack memories," *IEEE Trans. on Inform. Theory*, vol. 64, no. 11, pp. 7094–7112, 2018.
- [7] Y.M. Chee, H.M. Kiah, A. Vardy, V.K. Vu, and E. Yaakobi, "Codes correcting limited-shift errors in racetrack memories" *Proc. IEEE Int. Symp. on Inform. Theory*, pp. 96–100, 2018.
- [8] G. Mappouras, A. Vahid, R. Calderbank, and D. J. Sorin, "Green-Flag: Protecting 3D-Racetrack memory from shift errors", *2019 49th Annual IEEE/IFIP International Conference on Dependable Systems and Networks (DSN)*, pp. 1–12, 2019.
- [9] S. Archer, G. Mappouras, R. Calderbank, and D. J. Sorin, "Foosball coding: Correcting shift errors and bit flip errors in 3D racetrack memory", *2020 50th Annual IEEE/IFIP International Conference on Dependable Systems and Networks (DSN)*, pp. 331–342, 2020.
- [10] E. Atoofian and A. Saghir, "Shift-aware racetrack memory", *2015 33rd IEEE Int. Conf. on Computer Design (ICCD)*, pp. 427–430, 2015.
- [11] A. Ali Khan, F. Hameed, R. Blasing, S. Parkin, and J. Castrillon, "Shifts-Reduce: Minimizing shifts in racetrack memory 4.0", *ACM Trans. on Architecture and Code Optimization*, vol. 16, no. 4, pp. 56:1–56:23, 2019.
- [12] S. Olliver, S. Kline Jr., R. Kawsher, R. Melhem, S. Banja, and A.K. Jones, "Leveraging transverse reads to Correct alignment faults in domain wall memories," *49th Annual IEEE/IFIP International Conference on Dependable Systems and Networks (DSN)*, pp. 375–387, 2019.
- [13] S. Olliver, D. Jr. Kline, R. Kawsher, R. Melhem, S. Banja, and A. K. Jones, "The power of orthogonality", *2019 IEEE Annual Symp. on VLSI*, pp. 100–102, 2019.
- [14] K. Roxy, S. Olliver, A. Hoque, S. Longofono, A.K. Jones, and S. Banja, "A novel transverse read technique for domain-wall "racetrack" memories," *IEEE Transactions on Nanotechnology*, vol. 19, pp. 648–652, 2020.
- [15] Y. Cassuto, M. Schwartz, V. Bohossian, and J. Bruck, "Codes for asymmetric limited-magnitude errors with application to multilevel flash memories", *IEEE Trans. on Inform. Theory*, vol. 56, no. 4, pp. 1582–1595, 2010.
- [16] B.H. Marcus, R.M. Roth, and P.H. Siegel, *An introduction to coding for constrained system*, 5th edition, Oct. 2001.
- [17] Y. M. Chee and V. K. Vu, "Codes correcting synchronization errors for symbol-pair read channels", *Proc. IEEE Int. Symp. on Inform. Theory*, pp. 746–750, 2020.