

Fig. 3. Composite (front and back) layout of (a) three SIW resonators with $\ell = 389$, 489, and 589 μm , respectively, each resonator being sandwiched between two 578- μm -long SIW-GCPW transitions at the input and output, respectively, and (b) impedance standards "through," "line," and "reflect" fabricated on the same SiC wafer as the resonators.

II. EXPERIMENTS

Fig. 3 illustrates the layouts of three types of SIW resonators with length $\ell = 389$, 489, and 589 μm , respectively [11]. The resonators are fabricated together on 100- μm -thick c -axis 4H-SiC (Wolfspeed W4TRF0R-0200, $> 10^5 \Omega\cdot\text{cm}$ resistivity). Each SIW is bound on the top and bottom with 0.1- μm -thick Ni and 0.7- μm -thick Al, and on the left and right with rows of through-substrate vias (TSVs). The TSVs are 45 μm in diameter and spaced 100 μm center-to-center. The length of each resonator is determined by irises each formed by a pair of TSVs optimally placed in the middle of the SIW. All resonators are 520- μm wide for the D-band operation. For on-wafer measurement, the input or output of each resonator is transitioned through a 578- μm -long tapered section to a grounded coplanar waveguide (GCPW). Each transition has < 0.2 dB insertion loss with minimum impact on measurement uncertainty [13]. For impedance standards, SIW-based "through," "line," and "reflect" are also laid out and fabricated on the same SiC wafer.

On-wafer measurement is based on 2-tier calibration, using both on-wafer and off-wafer impedance standards in conjunction with an Anritsu ME7838G 220-GHz vector network analyzer and two MPI TITAN 220-GHz coaxial probes with 50- μm pitch [13]. Tier-1 calibration is based the load-reflection-match method [14] using an MPI TCS-050-100-W impedance-standard substrate. Tier-2 calibration is based on the through-reflect-line method [15] using the impedance standards shown in Fig. 3(b). Tier-1 calibration establishes the reference plane at the probe tip 15 μm inside the end of the GCPW. Tier-2 calibration moves the reference plane past the GCPW-SIW transition by 50 μm to the middle of "through." After tier-2 calibration, the de-embedded scattering parameters of the intrinsic resonators are free from the effects of ϵ_{\perp} and can be used to accurately extract ϵ_{\parallel} . In the future, with the GCPW-SIW transitions validated and repeatable, a single-tier calibration

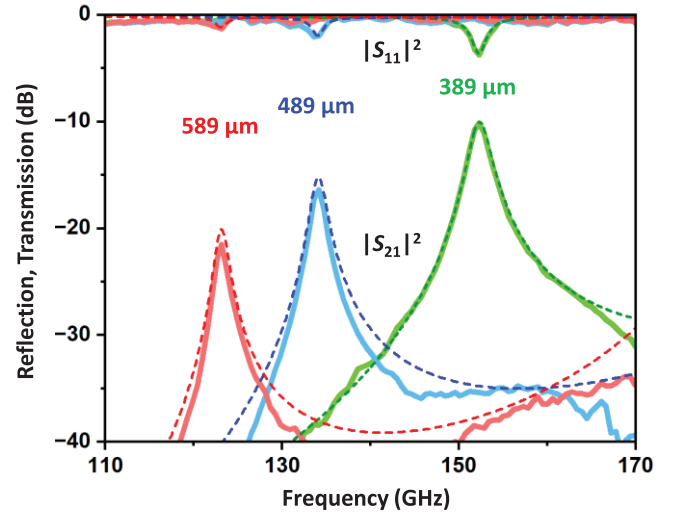


Fig. 4. Measured (solid) vs. simulated (dashed) $|S_{11}|$ and $|S_{21}|$ for intrinsic SIW resonators with $\ell = 389$, 489, and 589 μm , respectively. The simulation is performed with $\epsilon_{\parallel} = 10.27$ and $\tan\delta = 0.0001$.

using the on-wafer impedance standards of Fig. 3(b) may be sufficient to extract the intrinsic SIW characteristics.

III. RESULTS AND DISCUSSION

A. Relative Permittivity ϵ_{\parallel}

With the SIW-GCPW transitions de-embedded, Fig. 4 compares the measured and simulated magnitudes of the reflection and transmission coefficients ($|S_{11}|$ and $|S_{21}|$) of three intrinsic resonators. The simulation is performed by using the 3D finite-element full-wave simulator HFSS under the automatic adaptive meshing mode, with ϵ_{\parallel} as a tuning parameter. It can be seen that resonances occur around 123, 134, and 153 GHz and $Q = 89$, 88, and 88 for $\ell = 589$, 489, and 389 μm , respectively. Moreover, a relative $\epsilon_{\parallel} = 10.27$ and a loss tangent $\tan\delta = 0.0001$ [5], [8] can fit all three resonators. Statistically, eleven resonators are characterized. They indicate that $\epsilon_{\parallel} = 10.29 \pm 0.02$ at 123.1 ± 0.1 GHz for three 589- μm -long resonators; $\epsilon_{\parallel} = 10.25 \pm 0.03$ at 134.2 ± 0.3 GHz for three 489- μm -long resonators, and $\epsilon_{\parallel} = 10.27 \pm 0.02$ at 152.3 ± 0.1 GHz for five 389- μm -long resonators. These results are included in Fig. 2. Since they exhibit little dispersion over the D band, the average over all eleven resonators yields $\epsilon_{\parallel} = 10.27 \pm 0.03$, which differs significantly from that extrapolated from [9].

The above results confirm that ϵ_{\parallel} can be extracted with better than 1% precision. The precision can be attributed to 1) homogenous medium (without air gap or surface contamination), 2) decoupling of ϵ_{\parallel} from ϵ_{\perp} , and 3) resonance determined by lateral dimensions, which are controlled to within 0.1% by semiconductor processes.

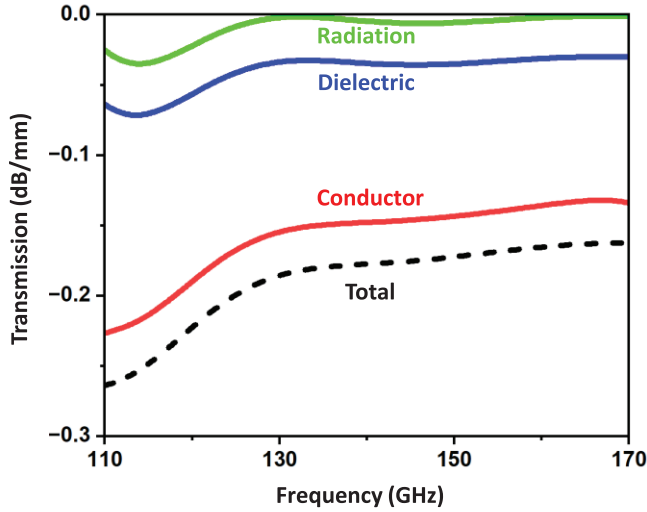


Fig. 5. Simulated SIW loss mechanisms with $\sigma = 4 \times 10^7$ S/m and $\tan\delta = 0.001$.

B. Loss Tangent $\tan\delta$

The imaginary part of ϵ_{\parallel} can be characterized by $\tan\delta$, which is critical to the resonator loss or Q . Presently, Q can only be used to determine the upper limit of $\tan\delta$ because the resonator loss is dominated by the conductor loss instead of the dielectric loss. For example, Fig. 5 shows that the HFSS-simulated loss of the SIW with typical values of $\tan\delta$ and the metal conductivity σ . It can be seen that the conductor loss dominates, consistent with other reports such as [16]. In turn, the conductor loss is dominated by σ instead of metal thickness or interface roughness because 1) at $0.8 \mu\text{m}$, the conductor is much thicker than the skin depth at the D band, and 2) the present "epi ready" 4H SiC substrate has an average roughness of $\pm 3 \text{ nm}$, which is negligible compared to the wavelength at the D band. Thus, to precisely characterize the loss of 4H SiC, σ needs to be significantly improved.

Although presently there is no effective way to separate the effects of σ and $\tan\delta$ on Q , it can be used to establish the lower limit of σ and the upper limit of $\tan\delta$. For a $389\text{-}\mu\text{m}$ -long resonator, Fig. 6 compares the measured $Q = 89$ (a horizontal solid line) with values simulated by different combinations of σ and $\tan\delta$ (dashed curves). It can be deduced that $\sigma > 6 \times 10^6$ S/cm and $\tan\delta < 0.02$, because $Q = 89$ for $\sigma = 6 \times 10^6$ S/cm and $\tan\delta = 0$, as well as for $\sigma = \infty$ and $\tan\delta = 0.02$.

IV. CONCLUSION

Enabled by the SIW, the above results demonstrate consistent and precise on-wafer characterization of the extraordinary permittivity of 4H SiC over the D band. The resulted ϵ_{\parallel} differs significantly from ϵ_{\perp} reported for the D band and ϵ_{\parallel} extrapolated from terahertz frequencies. In the future, better metallization can be used to quantify $\tan\delta$. Through on-wafer characterization of resonators and other devices fabricated on the same SiC substrate, material property can be closely correlated with

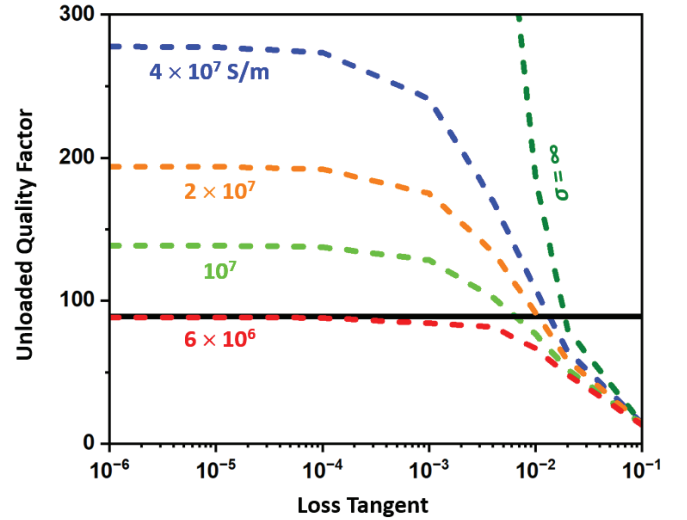


Fig. 6. Measured (solid) vs. simulated (dashed) Q as a function of $\tan\delta$ with σ as a parameter for a $389\text{-}\mu\text{m}$ -long resonator.

device performance. By fabricating SIW resonators on off-axis 4H SiC (e.g., on a axis), ϵ_{\perp} can be similarly extracted. The approach can be extended to characterize the permittivity of other materials such as high-resistivity Si, which is commonly used for monolithic microwave and millimeter-wave integrated circuits.

ACKNOWLEDGEMENT

We appreciate the technical discussion with X. Shang. We thank the technical assistance by K. Nomoto, Y. Tang, and W. Zhao. This work is supported in part by the US National Science Foundation (NSF) under Grants ECCS-2117305, ECCS-2122323, and ECCS-2132329, the US Office of Naval Research under Grant N00014-21-1-2680, as well as the Semiconductor Research Corporation and the US Defense Advanced Research Projects Agency through the Joint University Microelectronics Program. This work is performed in part at the Cornell NanoScale Facility, an NNCI member supported by NSF Grant NNCI-2025233.

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