# Vanadium-Dioxide-Based Reconfigurable Ka-Band Dual-Sense Linear-to-Circular Polarizer

Mark S. Lust<sup>10</sup>, Student Member, IEEE, David L. West<sup>10</sup>, Graduate Student Member, IEEE, Vanessa Smet, Member, IEEE, Thomas G. Williamson<sup>10</sup>, Member, IEEE, and Nima Ghalichechian<sup>10</sup>, Senior Member, IEEE

Abstract—We propose a reconfigurable dual-sense linearto-circular polarization (LCP) converter in the form of a VO<sub>2</sub>-based metasurface. VO<sub>2</sub> is a phase change material (PCM) that is useful for low-loss mmWave devices, being transparent in its insulating state and metallic when heated. The device operates in the satellite communications uplink portion of Kaband (27.5-31 GHz). It consists of a four-layer meanderline metasurface with two overlapping configurations, rotated relatively by 90° in-plane, for right-hand circular polarization (RHCP) and left-hand circular polarization (LHCP) conversion. Each configuration is interrupted by VO<sub>2</sub> patches with double spiral NiCr microheaters along the meandered traces. Under dc bias, the VO<sub>2</sub> of one trace changes from insulator to conductor, activating the configuration. The simulated design exhibits axial ratio (AR)  $\leq$  0.99 dB and insertion loss (IL)  $\leq$  2.59, for both RHCP and LHCP conversion. After verifying the dual-sense RF design in simulation, single-sense LCP conversion was measured. The measured results are for LHCP conversion with AR  $\leq$ 2.1 dB and IL  $\leq$  4.0 dB. The LHCP conversion also has 1-dB AR from 30.2-32.4 GHz and 3-dB AR from 27-38 GHz (26% circular polarization bandwidth). This study demonstrates for the first time LCP conversion using a transmission-mode reconfigurable metasurface in Ka-band using VO<sub>2</sub> PCM, and simple dc activation network.

#### I. Introduction

S COMMUNICATION networks become more and more crowded with the growing population of Internet-connected devices, new avenues of signal diversity are sought to alleviate interference. Notably, the available communication frequency bands are being expanded with the advent and

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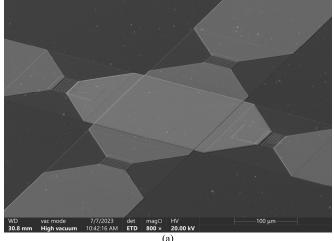
Mark S. Lust was with ElectroScience Laboratory, The Ohio State University, Columbus, OH 43212 USA. He is now with the Cyber Trust and Analytics Division, Battelle Memorial Institute, Columbus, OH 43212 USA (e-mail: Lust.50@osu.edu).

David L. West, Thomas G. Williamson, and Nima Ghalichechian are with the School of Electrical and Computer Engineering, Georgia Institute of Technology, Atlanta, GA 30308 USA (e-mail: dwest64@gatech.edu; thomas.williamson@gtri.gatech.edu; nima.1@gatech.edu).

Vanessa Smet is with the George W. Woodruff School of Mechanical Engineering, Georgia Institute of Technology, Atlanta, GA 30308 USA (e-mail: vanessa.smet@me.gatech.edu).

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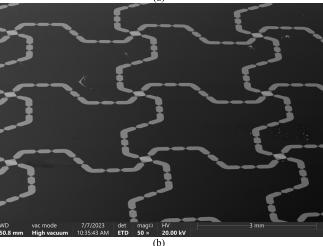


Fig. 1. Scanning electron microscope (SEM) micrographs of reconfigurable polarizer device (a) close-up of junction between configurations and (b) overview of several unit cells.

proliferation of 5G networks and the use of the Ka-band in satellite communications (satcom). These increase accessible bandwidth and number of active channels; however, there is opportunity for new dimensions of modulation and multiplexing in this paradigm. To improve robustness against signal interference and network interruptions, we can create diversity not only in the frequency domain but also in polarization.

Usage of circular polarization (CP) is desirable as opposed to linear polarization (LP), as it eliminates the risk of polarization mismatch loss due to misalignment [1]. This is especially useful in mobile networks, where handheld devices are positioned at any and every angle imaginable. Additionally, in satcom, where spacecraft are thousands or tens of thousands

of miles away, any reduction in pointing or aligning requirements is highly beneficial.

Current literature on linear to circular polarization (LCP) converters is focused on implementing dual-sense-dual-frequency LCP metasurfaces and antennas [1], [2], [3]. These provide static LCP conversion with each sense of CP assigned to its own frequency band. There are also switchable reflection-mode LCP conversion metasurfaces that produce either LP or CP with PIN-diodes [4] and varactor diodes [5], dual-sense reflection-and transmission-mode LCP converters with mechanical reconfiguration [6], liquid metal injection microfluidics [7], [8], liquid crystal [9], and transmission mode single-sense reconfigurable LCP converters using PIN-diodes [10], [11]. The creation of reconfigurable CP-sense diversity within a single band in a phase change material (PCM)-based transmission-mode metasurface is yet to be explored.

Having both right-handed circular polarization (RHCP) and left-handed circular polarization (LHCP) in the same space is attractive as they are orthogonal and will not destructively interfere with each other. A reconfigurable dual-sense LCP converter effectively doubles available bandwidth in the case that nearby networks cause interference, lowering signal strength (therefore, signal-to-noise ratio, SNR), and ultimately causing slow device connection. While the doubled bandwidth is not simultaneous per device, it allows several multi-in-multi-out (MIMO) networks to run smoothly in parallel. For example, a home 5G router with dual-sense reconfigurable LCP conversion could enhance its agility by dodging nearby interfering signals through switching polarization in addition to conventional channel hopping.

As applied to satcom, the dual-sense LCP conversion is advantageous in using a single aperture to communicate with various satellites that could have static RHCP or LHCP antennas. It is preferable to have any method of reconfiguration on the ground side, as static devices consume less power and can weigh less. Weight reduction in satellites dramatically reduces the cost of launching into space, as it directly impacts the amount of thrust required. Lowering power consumption allows the limited energy absorbed by onboard photovoltaics to be dedicated to scientific instrumentation and measurement or signal processing and computation.

In this work, we present the design, fabrication, and measurement of a reconfigurable dual-sense LCP converter metasurface based on vanadium dioxide (VO<sub>2</sub>). VO<sub>2</sub> provides a path for achieving low-loss mmWave switches. PIN and varactor diodes are widely used in the microwave band; however, they suffer from large losses at the mmWave band. MEMS switches are difficult to fabricate and integrate on the electromagnetic metasurface and require large actuation voltage. In contrast, nonlinear behavior of VO2 can be exploited to create low loss mmWave switches with millisecond response time. This response time is thermally limited; the phase transition mechanism has been shown to occur on a picosecond timescale for ultrafast laser pulses [12]. Scanning electron microscope (SEM) micrographs of a fabricated metasurface device are shown in Fig. 1. Measurement results for the metasurface device have previously been presented in recent conferences [13], [14].

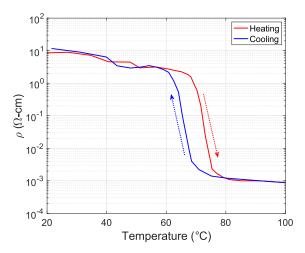


Fig. 2.  $VO_2$  phase change shown as electrical resistivity over temperature as grown on annealed  $Al_2O_3$  [32].

## A. VO<sub>2</sub> PCM

Phase change materials or PCMs are used in reconfigurable and "smart" devices, be it sensors [15], [16], [17], [18], antennas [19], [20], switches [21], [22], [23], [24], [25], [26], metasurfaces [27], [28], or power limiters [29], [30], [31]. The advantages of PCMs over typical solid-state switches include quasipassive behavior; the range of options of external stimuli such as Joule heating, voltage, current, strain, infrared light, or X-rays, and direct manipulation of device geometry. VO<sub>2</sub> is a PCM with a reversible insulator-metal transition (IMT) at 68 °C, with contrast in electrical resistivity of  $9.8 \times 10^4$  when grown on C-plane sapphire and  $1.46 \times 10^4$  when grown on Si with an annealed Al<sub>2</sub>O<sub>3</sub> buffer layer [32]. During the IMT, the crystal structure of the VO<sub>2</sub> switches from monoclinic to tetragonal, and back again when cooled. This change is illustrated in Fig. 2 [32]. VO<sub>2</sub> is especially appealing in mmWave systems for its low material losses compared to solid-state semiconductor switches. It is also highly reliable for mmWave applications, as experimentally demonstrated in our prior work, with no significant degradation after 100 million thermal cycles. Additionally, the actuation was done using a 100 Hz 10% duty cycle wave (1-ms pulses) when using Joule heaters [33]. While it is frequently deposited on sapphire substrates with the highest resistivity contrast, it is generally of better interest in mmWave systems to integrate VO2 on Si substrates. The advantages provided by using Si substrates are the ability to perform bulk micromachining to locally modify metasurface substrate thickness and potential for future packaged systems with control circuitry integrated monolithically with the metasurface. We note that while VO<sub>2</sub> exhibits low losses and high contrast ratio at the mmWave band, it can also be used as a tunable medium at other bands such as medium infrared as shown by our prior work [34].

## B. Reconfigurable LCP Converter Concept

For our device, we chose to pursue dual-sense LCP conversion with a single aperture over the same band. We chose the Ka-band, specifically the satcom uplink frequencies of 27.5–31 GHz, because it also contains the 28 GHz 5G

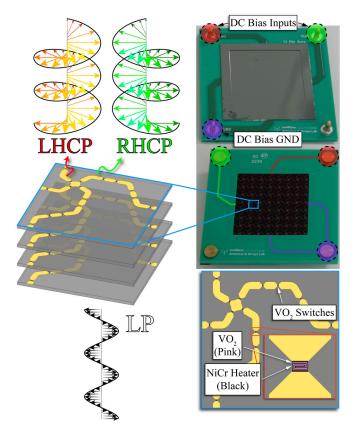


Fig. 3. Reconfigurable Ka-band polarizer operation. Each configuration is excited by a single dc voltage that separately produces LHCP or RHCP. The images in the top right are both sides of the fabricated and assembled device, and on the left is the model of the unit cell simulated in HFSS. The bottom right shows a top-down view of the unit cell.

range. The design principle for this device is based on the meanderline polarizer [35] with multiple cascaded layers [3], [36]. Original to our design is the addition of breaks in the meanderline traces connected with VO2 PCM switches. The full design is duplicated and rotated 90° about the intersection point as first reported in our preliminary work in [37]. The device operation is illustrated in Fig. 3. The goal is to construct two superimposed meanderline polarizers that are isolated in dc and each transparent when the other is activated. To enable the overlapping design, we applied the concept of the meanderline trace to a hexagonal grid so that there is less interaction between the two configurations. The shift from rectangular to hexagonal meanderlines is depicted in Fig. 4. The motivation behind having the hexagonal overlaid meanderlines was twofold: to have identical behavior between the two configurations and to maximize unit cell dimensions. A rectangular shape like the one shown in Fig. 4(a) would overlap at multiple points with 90° overlaid configurations. Square proportions for a rectangular grid meanderline would serve the device's function, but the hexagonal grid allows decreased mutual coupling between the ON and OFF configurations. Another aspect to consider was unit cell size. This is discussed in further detail in Section II-A. The decision for using the hexagonal grid over rectangular was influenced by engineering considerations, taking advantage of the fact that the underlying

physics principles of the hexagonal grid and rectangular grid meanderlines are the same, discussed below.

The operating principle of the meanderline polarizer begins with a diagonal 45° LP wave excitation. This wave is broken down into vertical and horizontal components, which encounter shunt inductance along the traces and shunt capacitance across the traces, respectively. The layout causes a 90° difference  $(\Delta \varphi)$  in phase between the two components that then reunite as a CP wave [35]. In our case, the vertical and horizontal components stay the same, but when the alternate configuration is active, the vertical component encounters shunt capacitance across the traces, and the horizontal component interacts with shunt inductance along the traces. In the alternate configuration,  $\Delta \varphi = -90^{\circ}$  (1). Equations for amplitude error,  $A_e$ , and phase error,  $\varphi_e$ , are given in (2) and (3). Amplitude error is the difference between the transmission coefficients of the vertical and horizontal modes. The phase error is the deviation in phase difference from 90°. The simple input switching scheme is enabled by using VO<sub>2</sub>. The novelty of the approach lies in the rotational-symmetric dual configuration and the VO<sub>2</sub> PCM-based switching scheme.

$$\Delta \varphi = \varphi_V - \varphi_H \tag{1}$$

$$A_e = 20\log_{10}(1 + |S_{21,V} - S_{21,H}|)$$
 (2)

$$\varphi_e = 90 - |\Delta\varphi|. \tag{3}$$

## II. DESIGN AND SIMULATION

### A. Electromagnetic Design

Modeling and simulation of the electromagnetic (EM) design was carried out using ANSYS HFSS. We performed simulations using Floquet ports and periodic boundaries for an infinite array. The unit cell, shown in Fig. 5, consists of a 100  $\mu$ m thick Si substrate, 500 nm thick Ag meanderline traces, 100 nm thick VO<sub>2</sub> at Ag gaps, and 150 nm thick NiCr double spiral heaters on top of the VO2; these are repeated as four layers, 600  $\mu$ m apart from the top of one layer to the top of the next. The vertical and horizontal traces are based on a hexagonal grid with 1.1 mm long segments such that they only intersect at one point and are identical save for the 90° rotation about their intersection point. With the need for rotational symmetry, the traces are somewhat sparse and cannot create 90° phase difference with one layer. Therefore, the initial design was made for one layer that causes  $\sim 22.5^{\circ}$ phase shift, then increased to four layers, where the pitch between the layers and the Si thickness were optimized for the 90° phase difference target. The VO<sub>2</sub> patches along the vertical traces are assigned with the properties of its metallic state, and the VO<sub>2</sub> patches along the horizontal traces are assigned the properties of its dielectric state.

In each unit cell, there are  $8 \text{ VO}_2$  switches per configuration. The number of switches was chosen based on the interplay between several factors: losses, power consumption, and resonant behavior. With too many switches, conductivity of the ON-configuration becomes too low, causing higher losses, and the power consumption increases proportionally. With too few switches, power consumption is lower. However, the solid sections of the main conductor become too large and cause

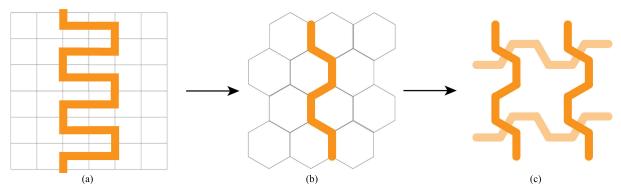


Fig. 4. Evolution of our design from (a) traditional meanderline, to (b) hexagonal grid meanderline, and to (c) rotated and overlaid hexagonal grid meanderlines.

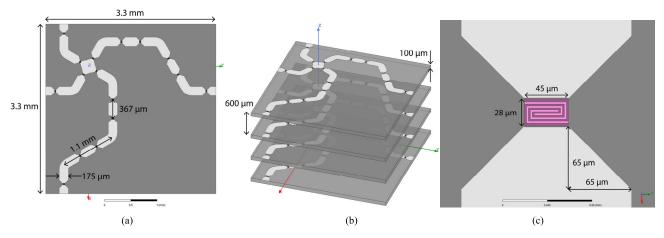


Fig. 5. HFSS model (a) top view of unit cell with dimensions, (b) trimetric view of unit cell showing four layers with dimensions, and (c) top view close-up of  $VO_2$  switch with NiCr heater and dimensions. NiCr heater and gap are each 2  $\mu$ m wide.

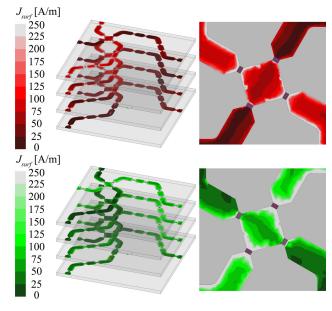


Fig. 6. RF surface current distribution in the metasurface with (top) LHCP mode active and (bottom) RHCP mode active presented (left) over the full unit cell and (right) at the junction between the traces.

the OFF-configuration to interact with the incoming wave and affect the performance of the ON-configuration. With four heaters, the main conductor sections would resonate in the OFF state within the operating band.

The implementation of a hexagonal grid accomplishes two goals: lower coupling between configurations and larger unit cell size. The hexagonal grid allows sparser layout than square, so potential field coupling to the neighboring traces is lower. Compared to a rectangular grid meanderline, the unit cell has a 50% increase in side length to 3.3 mm, or  $\lambda$ /3 at 30 GHz. The larger unit cell allows for fewer total unit cells and therefore fewer total heaters and lower power consumption for the same overall device area. The full device was made as large as possible while having all four layers fabricated on a single 100-mm wafer, discussed in Section III. Therefore, it was critical to minimize power requirements per device unit area.

The switches consist of a rectangular patch of VO<sub>2</sub> with the double spiral NiCr heaters, set in the gaps with a chamfer design in the main meanderline traces. The chamfer detail was added to reduce continuity in the OFF state caused by capacitance in the gap. This allowed further isolation between the RF configurations. The size of the VO<sub>2</sub> patches was determined iteratively in a trade-off between smaller size to decrease resistance in the activated trace and larger to increase the gap and isolation in the deactivated trace. The VO<sub>2</sub> material boundary was limited to the NiCr heater area, as the intended active area of the switch.

The heaters were designed as double spirals to add an inductive RF choke function to the OFF state of the switches, giving greater EM isolation than a capacitive gap. Once the heater excites the  $VO_2$ , the turns of the double spirals are shorted together, causing the switch to act as a simple resistor.

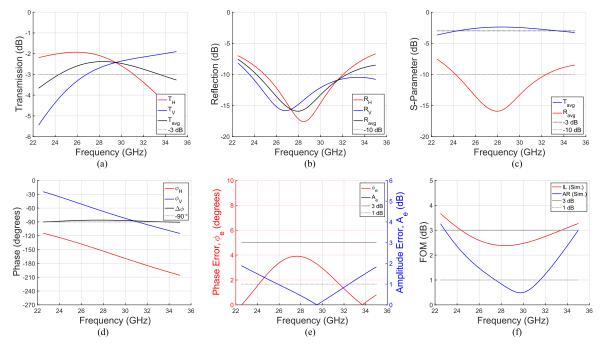


Fig. 7. Simulation results for unit cell design (a)-(c) transmission and reflection, (d) phase accumulated in each Floquet mode and difference thereof, (e) phase and amplitude error, and (f) FOM: insertion loss and axial ratio.

TABLE I RIE AND RIE-ICP RECIPES FOR VO2 AND ALUMINA

Material Etched	RIE Power (W)	ICP Power (W)	Process Pressure (mTorr)	Ar flow (sccm)	SF <sub>6</sub> flow (sccm)	BCl <sub>3</sub> flow (sccm)
$VO_2$	100	0	5	20	20	0
Al <sub>2</sub> O <sub>3</sub>	50	400	10	10	0	30

The difference in RF surface current flow between the switch states is shown in Fig. 6, with clear disruption by the double spirals and continuity over the activated VO<sub>2</sub>.

The Floquet ports are excited with two modes, defined in HFSS as  $TE_{00}$  and  $TM_{00}$ , which are plane waves with 90° rotation between the two. The port excitations are deembedded to the top surface of the top layer and the bottom surface of the bottom layer. The reflection  $(S_{11})$ , transmission  $(S_{21})$ , and phase shift are compared between the vertical and horizontal modes. To find the difference in phase shift between the two modes (hereafter referred to as the phase difference), the absolute value is taken of the difference between the respective unwrapped phase shifts. Low reflection is a good benchmark, but the chief figures of merit (FOM) of the polarizer, namely the insertion loss (IL) and axial ratio (AR), are determined from the comparative and total  $S_{21}$  and phase difference  $(\Delta \varphi)$ . IL and AR are found in (4)–(6), respectively. Subscripts V and H refer to the vertical and horizontal wave components. Phase is calculated in degrees

$$IL = -20\log_{10}\left(\frac{S_{21,V} + S_{21,H}}{2}\right) \tag{4}$$

$$IL = -20 \log_{10} \left( \frac{S_{21,V} + S_{21,H}}{2} \right)$$

$$\Delta = \sqrt{1 - \frac{4S_{21,V}^2 S_{21,H}^2 (\sin \Delta \varphi)^2}{S_{21,V}^2 + S_{21,H}^2}}$$
(5)

$$AR = 20\log_{10}\sqrt{\frac{1+\Delta}{1-\Delta}}. (6)$$

The simulated FOM results are AR  $\leq 0.99$  dB and IL < 2.59 dB throughout the 27.5-31 GHz band. AR must be at most 3 dB to be considered circular polarization, so less than 1 dB is an excellent result. IL is typically considered in a 1-dB bandwidth for static LCP converters [1], [2], [3], but reconfigurability comes with a trade-off of larger losses [38]. This trade-off of higher losses considered, we decided to compensate with a stricter AR design goal of 1 dB. Lower AR implies a lower range for potential polarization mismatch loss. Given the unit cell's rotational symmetry, the alternate configuration results are identical, only with swapped transmittance and reflectance profiles per mode and negative phase difference.

The reflection and transmission are shown for the two modes and the average in Fig. 7(a)–(c); phase shift, phase error, and amplitude error are illustrated in Fig. 7(d) and (e); and FOM results are plotted in Fig. 7(f). The cross-polarized reflection and transmission of the plane wave modes were found to be below -40 dB across the band, indicating strong isolation between the orthogonal modes.

## B. Thermo-Electric Switch Design

The switch design was largely based on the EM model. The trace width and height and double-spiral design were chosen for their effect on the EM performance. The resulting resistance of a single heater at room temperature was 1 k $\Omega$ . Due to inherent difficulties in simulating the sharp, nonlinear switching behavior of VO2 with Joule heating combined with voltage and current effects, we chose to experimentally verify the dc portion of its functionality. We fabricated and tested several experimental standalone dc switch devices on a probe station before processing the full metasurface. These included single switches and sets of switches in series.

When measured on a dc probe station, the switches were confirmed to activate with a combination of Joule heating

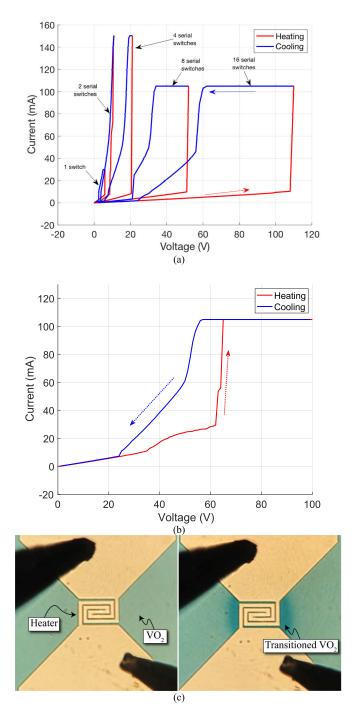


Fig. 8. Dc probe station measurements of test switches. I-V plots of (a) various sets of test switches in series and (b) one layer of the metasurface device. (c) Micrographs showing a single test switch in the OFF state (left) and in the ON state (right), where an isolated section of  $VO_2$  is transitioned, as indicated by the color change.

from the NiCr and field effect from the applied voltage. The Joule heating reduces the voltage required to activate VO<sub>2</sub>. Detailed switching behavior for a single switch, different sets of serial switches, and a full layer of the metasurface are depicted in Fig. 8 alongside a pair of micrographs of a test switch. Measurements were taken using a Keithley source meter. In the full metasurface layer, we observed that the VO<sub>2</sub> begins to transition at 35 V, then fully transitions as it hits the tool's compliance limit of 105 mA after the sharp transition from 63 to 65 V. When cooling, the current remains maxed

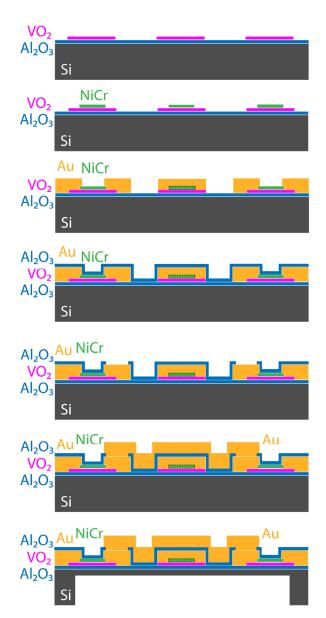


Fig. 9. Process flow for polarizer device. Starting with RIE patterning of VO<sub>2</sub>, NiCr was then evaporated and lifted off, followed by evaporation and lift-off of Au, then ALD alumina. The alumina was then etched using RIE-ICP, and the final Au layer was deposited. Last, the back side was etched by 200  $\mu$ m using DRIE.

out until it reduces to 57 V, then it begins to drop, showing a total resistance around 90  $\Omega$ , increasing around 50 V to 500  $\Omega$ . Note that the transition voltage is lower in the full metasurface layer, as it consists of eight parallel sets of 64 serial resistors. As such, it is reasonable to see this slightly different behavior from the sets of test switches. As the metasurface layers are all in parallel, this is representative of the expected voltage required for testing and 1/4 the current. Flat areas of the curves show where the source meter was at its hardware current limit for the given voltage. As shown in Fig. 8(c), when the switch is in the ON state (right), the transitioned VO<sub>2</sub> (indicated by the darker blue color) is well-confined to the heater area. The dc bias circuit within the metasurface uses the same traces as the RF circuit; this is inherent in the meanderline design

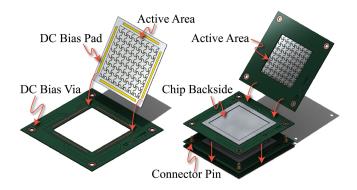


Fig. 10. Assembly procedure of the polarizer. We bond each metasurface chip to a PCB, and we layer four PCBs to complete assembly of the device. Connector pins ensure alignment between the layers and provide external dc connection points.

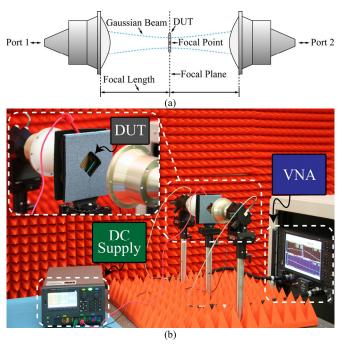


Fig. 11. (a) Schematic and (b) image of the quasioptical test setup. Lensed horn antennas focus a Gaussian beam onto the metasurface, which is placed at the focal point. The antennas are driven by a VNA, and a dc supply is used to bias the metasurface.

incorporating PCM. The two circuits work together for the simplest possible device activation.

## III. DEVICE FABRICATION

## A. Wafer-Level Processing

The fabrication process flow is shown in Fig. 9. Starting with a 300  $\mu$ m thick 100 mm diameter high-resistivity floatzone (FZ) Si wafer, we deposited and annealed ALD alumina and sputtered VO<sub>2</sub> as described in our previous work [32]. This process enables deposition of a VO<sub>2</sub> film with a high contrast ratio on Si, which was previously only available for sapphire substrates. We then patterned and etched the VO<sub>2</sub> with the RIE process detailed in Table I. Next, we performed e-beam evaporation and lift-off of 150 nm thick 80/20 NiCr alloy, followed by e-beam evaporation and lift-off of 500 nm thick Au with a 5 nm thick seed layer and a 25 nm thick

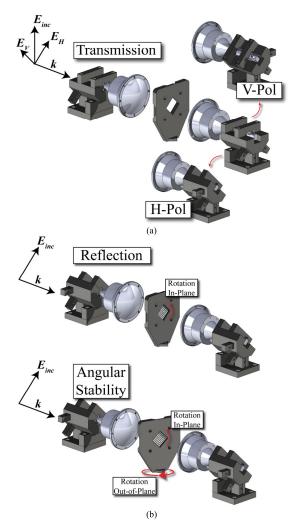


Fig. 12. Quasioptical measurement procedure. (a) Excitation of the metasurface with a diagonally polarized wave. We calibrated the system with both antennas in the vertical position and captured the V and H components of the circularly polarized output wave by rotating the receiving horn. (b) Independent measurement of the linearly polarized components to capture the reflection characteristic and angular stability. The DUT is rotated in-plane to capture each component. The DUT fixture is rotated out-of-plane to capture the response at different incident angles.

cap layer; for each of these, we used AZ nLOF 2020 negative photoresist with 2  $\mu$ m thickness. The first layer of Au connected the horizontal traces end-to-end. To connect the vertical traces, we deposited a 200 nm alumina isolation layer and etched it using the RIE-ICP recipe detailed in Table I. Then, we performed e-beam evaporation and lift-off of a 500 nm Au layer, this time using AZ nLOF 2070, a thicker photoresist at 7  $\mu$ m. Whereas the original ideal simulation used Ag as the main conductive material, we opted for Au after Ag-based prototypes exhibited dc shorts.

The full device unit cell substrate was  $100~\mu m$  thick, so the next step was to use deep reactive ion etching (DRIE), also known as Bosch process, to etch  $200~\mu m$  into the back of the substrate. First, we spin coated a film of S1813 photoresist and hard baked it to protect the front-side device layers. Afterward, we flipped the wafer and coated the back using AZ nLOF 2070 and patterned it having used the backside alignment feature of the maskless aligner (Heidelberg MLA150). We used 369 cycles of the following process: 5 s deposition using

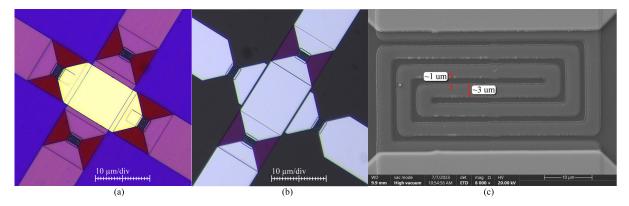


Fig. 13. Micrographs of the completed metasurface, (a) original version with two contiguous dc activation circuits, (b) ON/OFF version with both RF configurations present and one contiguous dc activation circuit and one interrupted dc circuit (i.e., with bridges and VO<sub>2</sub> excluded), and (c) SEM micrograph of NiCr heater dimensions changed due to undercutting in evaporation/lift-off.

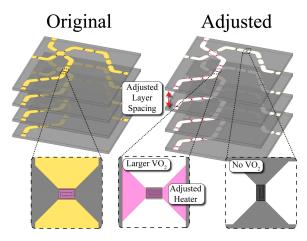


Fig. 14. Overview of the adjustments to the simulation model based on variables introduced in processing.

70 sccm  $C_4F_8$ , 1 sccm  $SF_6$ , 40 sccm Ar, 1 W RIE power, and 825 W ICP power at 15 mTorr; and 10 s etch using 5 sccm  $C_4F_8$ , 100 sccm  $SF_6$ , 40 sccm Ar, 9 W RIE power, and 825 W ICP power at 15 mTorr. To keep the substrate from overheating and burning the photoresist, 5-min rest periods were inserted every 50 cycles, and the wafer was stacked on a 100 mm  $Al_2O_3$  wafer, 500  $\mu$ m thick, to improve the seal on the chuck. The latter enhances the He cooling by increasing back pressure. We removed the photoresist using heated (80 °C) NMP (1-methyl-2-pyrrolidone). SEM micrographs of the finished device are shown in Fig. 1.

## B. PCB Design and Assembly

To provide mechanical structure and accessible dc biasing, we used a packaging scheme with printed circuit boards (PCBs). The assembly is shown in Fig. 10. We bonded each metasurface chip to a PCB layer, and four assembled layers form the complete device. Because the metasurface was designed with the silicon layers separated by air gaps, we cut an aperture into the center of the PCB to expose the active area. Large dc pads provide the electrical connection between the PCB and the chip. The pads connect to plated vias in the corners, which form the dc input ports.

The PCB thickness is required to be 300  $\mu$ m to achieve the desired layer pitch, and thicker copper is preferred to support

higher dc currents. To achieve these characteristics, four PCBs per full device are fabricated using Rogers RO4003 with a thickness of 8 mil (203  $\mu$ m) and 2-oz copper cladding. After plating the vias, the final copper thickness is 3 oz (105  $\mu$ m). We etch the backside copper on the PCB such that the chips are separated by one copper layer and one dielectric layer with a total thickness of 308  $\mu$ m.

We chose silver epoxy (MG chemicals 9410-3ML) as the bonding material, as the low thickness of the chip-side metal precluded soldering. We used a pneumatic benchtop manual dispenser to deposit a controlled volume of silver epoxy onto the PCB landing pads, then we picked-and-placed each metasurface chip onto a PCB using a Finetech FINEPLACER Matrix flip-chip bonder with a placement accuracy of  $\pm 3~\mu m$ . We then cured the samples at 150 °C for 7 min on a hot plate. To complete assembly, four samples were layered and aligned using connector pins, which were soldered by hand.

A limited amount of force had to be used when placing the metasurface chips due to the risk of shorting connections if the silver epoxy is overcompressed. As such, the silver epoxy's final thickness increases the layer spacings by  $120-230~\mu m$  (0.012–0.023 $\lambda$  at 30 GHz). We measured the layer spacings by viewing the final devices from the side using a microscope.

#### IV. QUASI-OPTICAL MEASUREMENT SETUP

We employed a quasioptical free space measurement setup to characterize the metasurface device. A diagram and an image of the test setup are shown in Fig. 11. We used a pair of Ka-band spot-focusing lensed horn antennas (Eravant SAQ-333039-28-S1). Measurement analysis begins at 27 GHz due to uncertainties near the cutoff frequency. The dielectric lenses focus the fields into a Gaussian beam with a focal length of 100 mm. The antennas are positioned two focal lengths apart with the DUT placed at the focal point, where there is a concentrated field intensity and planar phase front. The field intensity is characterized by the 3-dB beamwaist diameter, which is 12.7 mm in the E-plane. The setup emulates a plane wave incident on an infinite periodic structure, allowing for direct comparison with the Floquet port simulations. We used a PNA to perform the RF measurements, and we used a dc power supply to activate the metasurface. We applied

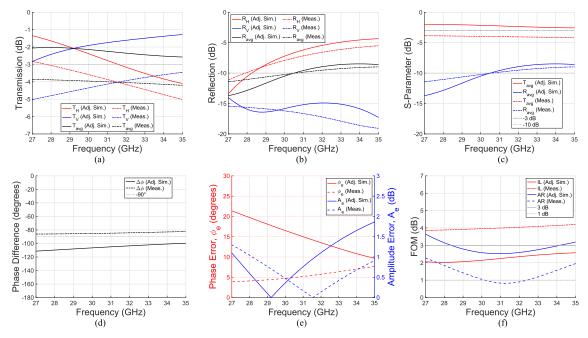


Fig. 15. Measurement results with comparisons to simulations (a)–(c) transmission and reflection, (d) phase difference between the Floquet modes, (e) phase and amplitude error, and (f) FOM: insertion loss and axial ratio.

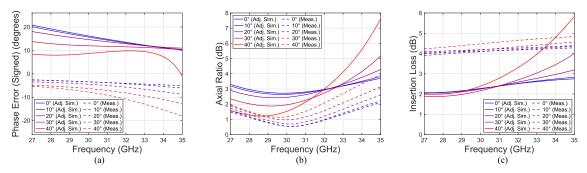


Fig. 16. Measured angular stability results with comparison to the adjusted simulation. (a) Signed phase error, (b) axial ratio, and (c) insertion loss data for incident angles ranging from  $0^{\circ}$  to  $40^{\circ}$ .

50 V dc with a current limit of 350 mA to activate the DUT. The initial current draw was 30 mA and ramped up to 50 mA as the VO<sub>2</sub> approached the transition temperature. When the VO<sub>2</sub> transitioned, the current quickly increased to the 350-mA limit with a corresponding drop in the voltage. Peak power draw was briefly 17.5 W during the transition. For four layers, 64 unit cells per layer, and eight switches per unit cell, this corresponds to a peak power of 68.4 mW per unit cell, or 8.5 mW per switch. Upon activation, the current required to sustain LCP conversion dropped to 130 mA at 50 V, corresponding to a total running power of 6.5 W, with 24.4 mW per unit cell and 3.2 mW per switch.

Each chip is a square with a side length of 33.0 mm, and the active metasurface area has a side length of 26.4 mm. Fabrication limitations prevented ideal oversizing of the metasurface relative to the Gaussian beamwaist. Ideally, the sample size would be at least four times the 3-dB beamwaist, such that the beam is truncated where the fields are >35 dB below peak [39]. In this case, the ideal sample size is 50.8 mm or larger. To mitigate beam truncation effects, as shown in Fig. 11, we placed absorber on the DUT fixture around the edges of the aperture, and the fixture was left in place during calibration.

For flexibility in the measurement setup, the test lensed horn antennas were supported by 3-D-printed fixtures with stems that allowed them to be rotated in 45° increments. The DUT was also positioned at a 45° angle in its 3-D-printed fixture. As a result, the device may be tested by creating circular polarization with a diagonally polarized incident wave, and the two components of the circularly polarized output wave may be captured by rotating the receiving horn antenna. Alternatively, both horn antennas may be placed with the same polarization to individually capture the vertical and horizontal components to synthesize circular polarization as in the simulation. To facilitate angular stability measurements, the DUT was mounted on a rotating stage. The antennas were mounted on linear translation stages for positional adjustments along the axis of propagation. To minimize reflections from the optical table, the entire setup was raised using posts. Additional absorbers were placed on the table surface below the DUT.

We used a free-space thru-reflect-line (TRL) calibration. The *thru* standard included the empty DUT fixture with the antennas placed two focal lengths apart. The *reflect* standard was a small copper plate inserted in the DUT fixture. The *line* standard comprised the empty fixture with the antennas moved

apart by  $\lambda/4$  at 30 GHz (2.5 mm) using the translation stages. We used time gating to eliminate undesired multipath effects due to repeated reflections between the antennas and DUT.

The procedure for transmission measurements at normal incidence is summarized in Fig. 12(a). We first calibrated the antennas in the vertical position. One antenna was then rotated at  $\pm 45^{\circ}$  to obtain the components of the circularly polarized output wave. The data were normalized to a thru measurement taken with the rotated receiving antenna. The normalization eliminates phase error incurred from misalignment of the antennas due to 3-D printing tolerances. In addition, the 3 dB of loss that would be expected from rotating the polarization of the receiving antenna is eliminated. As such, the normalized result can be compared to the unit cell simulations.

Reflection data for each component and measurements of angular stability required a separate procedure, shown in Fig. 12(b). We placed both horns at the +45° position with their polarizations aligned and recalibrated. In this manner, the horizontal and vertical components are captured separately. We rotated the DUT by 90° in-plane to capture both components.

To capture the angular stability, we took transmission measurements at different incident angles by rotating the DUT stage using the same calibration. Only transmission results can be measured because specular reflection at off-normal incidence cannot be captured with the measurement setup. In addition, there is blockage of the beam due to the DUT fixture, which affects the transmission, so the measurement results have been normalized to thru measurements for the corresponding rotation angle. A maximum incident angle of 40° could be measured before the transmission is drastically affected.

## V. MEASUREMENT RESULTS

We fabricated multiple versions of the metasurface. The versions of the device described in Section III were found to have thermal isolation issues between the VO<sub>2</sub> switches of the different configurations. Specifically, without the heat sink of the probe station present, the heat was no longer isolated to the area of the NiCr heaters. While we saw clear heat isolation to the switches on the probe station as shown in Fig. 8(c), the temperature distribution of the metasurface in final testing was uniform. As such, we present measurement results for an alternate version of the device, as shown in Figs. 13 and 14. VO<sub>2</sub> is only present for one set of meanderlines, and the bridges are not included. Additionally, the main conductor traces are Ag. To retain the effect of the OFF configuration on RF performance, the gaps in the Ag are narrow, and the NiCr heaters remain in place. This version is such that the presence of both RF configurations is represented, with one dc activation kept OFF to emulate thermal isolation. At room temperature, VO<sub>2</sub> is virtually transparent to mmWave radiation, so its selective absence is representative of its deactivated state. This device achieves one sense of circular polarization, which is activated when the VO2 is heated. Along with the rotational symmetry, the concept is thus proven for the dualsense LCP device in satisfaction of the mmWave design and implementation. The one remaining obstacle to dual-sense demonstration is improved thermal isolation between modes.

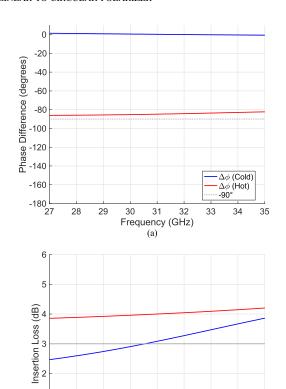


Fig. 17. Measurement results showing ON/Hot (LHCP) vs OFF/Cold (LP) performance in terms of (a) phase difference, and (b) insertion loss, demonstrating device reconfigurability.

Frequency (GHz)

32

30 31

27

28

·IL (Cold) ·IL (Hot)

34

Further iterations of thermal design are outside the scope of this project. The  $VO_2$  area is larger in the fabricated devices compared to the original simulation, which ensures good ohmic contact between the meanderline trace and the  $VO_2$  as well as eliminating alignment concerns. We also note that undercutting during the lift-off process for the heaters caused widening of the NiCr traces, as shown in Fig. 13.

In Fig. 15(a)–(c), the measured transmission and reflection results are shown for a normal incidence, with comparison to simulation. The simulation has been adjusted to account for differences in the fabricated device compared to the design, including the larger layer pitch, wider VO<sub>2</sub> area, wider NiCr traces, and the lack of bridges and VO<sub>2</sub> for one set of meanderlines. These changes are summarized in Fig. 14.

The measurement and simulation results have similar trends overall. The measured transmission magnitudes are lower than the simulation, and the difference in phase is closer to 90°. The discrepancies with the simulation can be attributed to angular misalignment of the layers in the fabricated device as well as beam truncation effects. If the layers are not parallel, the mutual coupling between them will be stronger in some areas of the array than others. Additionally, edge diffraction from beam truncation differs between the *thru* calibration standard and the device measurement due to the presence of additional silicon and PCB layers. These nonidealities are not captured in the Floquet port simulations.

Study	Device Operation	LCP AR Bandwidth (GHz)	Axial Ratio (dB)	Loss in-Band (dB)	Reconfiguration method
[4]	Reflection	11.8-24.1 [LHCP]	≤3.0	~1–3	PIN diodes
[5]	Reflection	4.9-8.2 [RHCP]	≤3.0	~1-3	Varactor diodes
[6]	Reflection	8.1–9.7; 9.8–11.2 [RHCP] 8.0–9.7; 9.8–11.1 [LHCP]	≤3.0	N/A	Mechanical
[7]	Transmission	2.49–2.55 [RHCP] 2.47–2.54 [LHCP]	≤3.0	~4	Liquid metal
[8]	Reflection	9.15–11.2 [RHCP] 9.15–11.7 [LHCP]	≤3.0	~1–3	Liquid metal
[9]	Reflection	129.5-130.5 [LHCP & RHCP]	≤6.0	~2.5–4.5	Liquid crystal
[10]	Transmission	2.50-3.64 [RHCP]	≤3.0	~2.5–3 dB	PIN diodes
This Work	Transmission	27.5-31 [LHCP]	≤2.1	~4	Phase-change material

TABLE II

COMPARISON OF RECONFIGURABLE LCP CONVERTER METASURFACES FROM THE LITERATURE

In Fig. 15(e) and (f), the measured FOM are shown. Measured AR  $\leq$  2.1 dB in the satcom uplink band, 27.5–31 GHz and AR  $\leq$  1 dB from a narrower band of 30.2–32.4 GHz. Measured IL  $\leq$  4.0 dB in the satcom uplink band and IL  $\leq$ 4.06 dB within the 1-dB AR band. It is also worth noting that CP is achieved across the entire measured frequency band, with AR  $\leq 2.3$  dB from 27–35 GHz (at least 26% CP bandwidth). Measured phase error  $(\varphi_e)$  is between 3.7° and 7.4° in the measured band, while the simulation achieves phase error between 9.7° and 21.3°. As a result, measured AR is lower than simulation, with a minimum of 0.7 dB around 30 GHz. Whereas improvement in losses from simulation to measurement would be considered somewhat unusual, error in phase difference  $(\Delta \varphi)$  can potentially be positive or negative. Therefore, this fortunate deviation from the simulation is both plausible and reasonable. Measured IL is 1.6-1.9 dB higher than simulation due to the decrease in transmission magnitude for both components.

The increased loss compared to the simulation is mainly attributed to the small size of the test vehicle. In the current setup, truncation of the Gaussian beam and potential angular misalignment of the metasurface layers, which impacts mutual coupling, contributed to the losses. The metasurface active area is  $26.4 \times 26.4 \text{ mm}^2$ , which is the maximum size that permits fabricating all four layers on a single 100-mm wafer. Fabricating a sufficiently large array to prevent Gaussian beam truncation creates an additional challenge of having four wafers with near-perfect yield. With four layers per wafer, it is possible to account for yield by matching the best chips from multiple wafers. A larger array would have been prohibitively expensive, as many high-yielding wafers would have been required to create a single four-layer device.

Additional loss (about 2 dB) inherent in the design comes as a result of the heaters being connected in both configurations at all times and from the VO<sub>2</sub> being less conductive than metal, but future iterations of the design can improve the loss figure. Solutions include increasing inductance of the heaters for improved RF choke in the switch OFF state, which would emphasize the ON state shorting of the metallic heater turns by VO<sub>2</sub>, as well as improving thermal isolation for the switches, thus decreasing the amount of NiCr needed for sufficient heating current.

Results for incident angle measurements are shown in Fig. 16 with comparison to the simulation. The simulated

data show reasonable stability of IL for higher incident angles below 31 GHz, with maximum variation of 0.2 dB. Above 31 GHz, there is a monotonic increase in IL with incident angle, with a maximum increase of 3.1 dB at 40°. The measured IL has less than 0.3 dB variation across the measured band up to 30°, but at 40°, IL is up to 0.5 dB higher compared to normal incidence. For both simulation and measurement,  $\Delta \varphi$  decreases for steeper incident angles. As a result, the simulated model's signed  $\varphi_e$  increases by up to  $2.8^{\circ}-11.6^{\circ}$ at an incident angle of 40°. Below 32 GHz, this effect leads to a decrease in AR by up to 1.7 dB for higher incident angles. In the upper part of the band, asymmetry in transmission magnitudes dominates, causing an increase in AR by up to 3.9 dB with higher incident angles. On the other hand, the measured data for normal incidence already had  $\Delta \varphi$  below 90°. Thus, the decreasing  $\Delta \varphi$  results in an increase in  $\varphi_e$  of up to 2.8°-13.0° for an incident angle of 40°. Due to the increasing  $\varphi_e$ , the AR increases up to 0.5–1.8 dB at an incident angle of 40°.

Comparing the measured ON/OFF performance for reconfigurability in Fig. 17, there is stark distinction between the two modes. When cold/deactivated, the metasurface passes LP with IL  $\leq 3.08$  dB and  $\Delta \varphi \leq 1.3^{\circ}$ , resulting in AR > 39 dB (AR not shown) in the satcom uplink band. This comparison is shown in Fig. 17. This is in contrast to the LHCP converted wave that exhibits IL  $\leq 4.0$  dB and AR  $\leq 2.1$  dB. Thus, we have experimentally confirmed Ka-band LCP conversion using a VO<sub>2</sub>-based reconfigurable metasurface.

Table II shows a comparison of reconfigurable LCP converters from the literature to illustrate the novelty of our work. We have demonstrated the first example of a transmission-mode reconfigurable LCP converter using VO₂ PCM. We also demonstrated ≤2.1 dB of AR across the uplink portion of the Ka band (27.5–31 GHz). Our metasurface targets the mmWave band unlike most other demonstrated designs, and the loss performance is comparable to others despite the higher operating frequency, due to the inherent low loss of PCM-based approaches.

# VI. CONCLUSION

In this study, we designed a reconfigurable dual-sense LCP converter metasurface at Ka-band using  $VO_2$  as the active material. The dual-sense design was based on two identical

overlapping meanderline unit cells, rotated 90° about an intersection point, where they are isolated in the dc domain and continuous in the RF domain.  $VO_2$  was chosen for its simplicity of activation and low material losses at frequencies in the mmWave spectrum, as opposed to semiconductor-based switches. In the design simulation, LHCP and RHCP modes had identical FOM of AR  $\leq$  0.99 dB and IL  $\leq$  2.59 dB in the 27.5–31 GHz uplink band. It was designed and simulated as the first transmission-mode dual-sense LCP conversion metasurface.

Measured results are presented as LP-LHCP conversion, using a device prototype retaining the structures of the RHCP configuration for their effect on RF performance, demonstrating that the only barrier to dual-sense experimental operation is improved thermal isolation. Nevertheless, this device is experimentally validated as the first PCM-based reconfigurable LCP converter and the first reconfigurable LCP conversion metasurface at Ka-band. In the OFF state, the device passes LP waves at IL  $\leq$  3.08 dB and AR  $\geq$  39 dB in the satcom uplink band, and in the ON state, it transmits LHCP waves at IL  $\leq$  4.0 and AR  $\leq$  2.1 dB in the same band. The LHCP transmission mode contains a 1-dB AR band from 30.2–32.4 GHz, where IL  $\leq$  4.1 dB. The increase in IL from simulation to measurement is attributed largely to factors such as beam truncation, diffraction effects from the edges of the device, and the multiple metasurface layers deviating slightly from parallel to each other within the device package. These factors could not be accounted for in infinite-array simulation. The measured LCP conversion performance is stable ( $\Delta AR \leq$ 1.8 dB) within the target band at incident angles up to 40°, with increase in insertion loss  $\Delta IL \leq 0.5$  dB.

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Mark S. Lust (Student Member, IEEE) received the B.S. (cum laude), M.S., and Ph.D. degrees in electrical and computer engineering from The Ohio State University, Columbus, OH, USA, in 2018, 2022, and 2023, respectively.

There, he worked at the ElectroScience Laboratory and Nanotech West Laboratory, and his research studies included antennas, phase change materials, microfabrication, frequency selective surfaces/metasurfaces, on-chip RF measurement, multiphysics simulation, and integrated optics. His

research began during his undergraduate studies in the RF Microsystems group, continuing into graduate school, and for his last year of Ph.D. studies was associated with the mmWave Antennas and Arrays Laboratory, Georgia Institute of Technology, Atlanta, GA, USA, while remaining at The Ohio State University. Currently, he is working at Battelle Memorial Institute in the Cyber Trust and Analytics division.

Dr. Lust won The Ohio State University Fellowship Award entering his doctoral studies in 2018, followed by the Defense Associated Graduate Student Innovators (DAGSI) Fellowship in 2019.



David L. West (Graduate Student Member, IEEE) received the B.S.E. degree (summa cum laude) in electrical engineering from the University of South Carolina, Columbia, SC, USA, in 2022. He is currently pursuing the Ph.D. degree in electrical engineering with Georgia Institute of Technology, Atlanta, GA, USA.

His research interests include antenna arrays, metasurfaces, vanadium dioxide phase change material, and wireless power transfer.

Mr. West has served as a reviewer for IEEE

TRANSACTIONS ON ANTENNAS AND PROPAGATION and IEEE ANTENNAS AND WIRELESS PROPAGATION LETTERS, since 2022. He was awarded the IEEE Microwave Theory and Techniques Society Undergraduate Scholarship in 2020, and received the Georgia Tech President's Fellowship in 2022.

Vanessa Smet, photograph and biography not available at the time of publication.



Thomas G. Williamson (Member, IEEE) received the B.S.E.E. (summa cum laude) and M.S.E.E. degrees from North Carolina State University, Raleigh, NC, USA, in 2018 and 2019, respectively. He is currently pursuing the Ph.D. degree with Georgia Institute of Technology, Atlanta, GA, USA.

Currently, he is a Research Engineer at Georgia Tech Research Institute (GTRI), Atlanta. He works in the Antenna Systems Division of the Sensors and Electromagnetic Applications Laboratory. He is a member of the mmWave Antennas and Arrays

Laboratory, Georgia Institute of Technology. His research areas include radar systems, antennas, arrays, filters, RFICs, phase change materials, power amplifiers, low noise amplifiers, other RF frontend electronics, digital arrays, and digital array radars. He is a U.S. Marine Corps veteran. During his service (2009–2014), he completed three deployments, attained the rank of Sergeant, and held multiple small unit leadership roles. He has led multiple internal research and development projects and sponsored projects at GTRI.

Mr. Williamson received various commendations for excellence and meritorious service, including the Navy Achievement Medal. He received the Dean's Fellowship award at NC State University in 2018. He won the Department of Defense National Defense Science and Engineering Graduate Fellowship (NDSEG) in 2019, which he declined to work at GTRI. He received the GTRI Star Performer Award in 2021.



Nima Ghalichechian (Senior Member, IEEE) received the B.S. degree in electrical engineering from Amirkabir University of Technology, Tehran, Iran, in 2001, and the M.S. and Ph.D. degrees in electrical engineering from University of Maryland, College Park, MD, USA, in 2005 and 2007, respectively.

He is an Assistant Professor at the School of Electrical and Computer Engineering, Georgia Institute of Technology, Atlanta, GA, USA. His research area is in electromagnetics, specifically, mmWave phased

arrays, reflectarrays, transmitarrays, meta-surfaces, on-chip antennas, ultrawideband antennas, novel reconfiguration techniques, phase-change materials, 3-D-printed lenses and arrays, and robotic measurements of antennas. He held a Research Scientist and a Research Assistant Professor positions at OSU from 2012 to 2017. From 2007 to 2012, he was with the Research Department of FormFactor Inc., Livermore, CA, USA, as a Senior Principal Engineer.

Prof. Ghalichechian is the Director of the mmWave Antennas and Arrays Laboratory, Georgia Tech. He is currently an Associate Director of Georgia Electronic Design Center (GEDC) and an Associate Editor of the IEEE TRANSACTIONS ON ANTENNAS AND PROPAGATION. He was an Associate Editor of the IEEE ANTENNAS AND WIRELESS PROPAGATION LETTERS from 2019 to 2022. Prior to joining Georgia Tech in 2021, he was an Assistant Professor at The Ohio State University (OSU), Columbus, from 2017 to 2021. He is a recipient of the 2018 College of Engineering Lumley Research Award at OSU, 2019 NSF CAREER Award, 2019 U.S. Air Force Faculty Summer Fellowship Award, and 2020 ECE Excellence in Teaching Award at OSU.

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