



Robust control of maximum photolithography overlay error in a pattern layer



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ABSTRACT

This paper presents a novel method for control of overlay errors in photolithography processes in semiconductor manufacturing. It minimizes the largest overlay error across all measurement markers on a pattern layer, and this minimization is done for the worst-case scenario regarding bounded process bias and modeling noise terms. This large-scale robust optimization problem was formulated as a linear program which can be solved within seconds to generate optimal control commands. Simulations based on wafer data obtained from a major 300 mm semiconductor fab illustrate consistent and significant advantages of this approach over the benchmark control strategies.

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1. Introduction

In the field of semiconductor manufacturing, photolithography processes are used to successively transfer patterns from masks onto wafers and thus build circuits up layer by layer [1]. Adequate alignment between consecutive pattern layers is critical to the production of functioning microelectronic devices [2]. Therefore, precise control of misalignments between pattern layers on a wafer, referred to as overlay errors, is a problem of great importance to the industry [3].

Due to wave behavior of light at sub-nanometer scales relevant to overlay errors, their characterization errors on a given wafer requires measurements of overlay errors from a number of overlay measurement markers dispersed across the wafer [4]. There could be hundreds of such markers on a wafer and their locations are predetermined and known. In the foundation of all overlay control approaches are Zernike-polynomial based models that relate overlay errors in those markers to controllable parameters on the photolithography tool on which the process is performed [5]. At such small scales, significant uncertainties in the behavior of controllable parameters on the tool are inevitable and because of them, there is always a difference between what a control parameter on a lithography tool is commanded to do and what the tool actually executes. Those differences are commonly referred to as *biases* in controllable parameters of the tool and their uncertain, time-varying effects on overlay errors need to be countered through the control process.

The most commonly utilized overlay control strategy in the industry is the so-called run-to-run (R2R) strategy. R2R control methods utilize historical records of overlay errors and Zernike-polynomial based overlay models to estimate behavior of biases in tool parameters over time, based on which various prediction techniques are used to predict those biases for the next wafer. Predicted biases are then countered by pre-compensating control parameters for the next wafer in the

opposite direction of predicted biases in those control parameters. A comprehensive review of R2R control strategies and their use in semiconductor manufacturing can be found in [6] and references therein.

Regardless of the underlying estimation and prediction approaches, R2R control strategies naturally deal with overlay errors of an individual pattern layer only. Nevertheless, production of a functioning circuit requires fabrication and alignment of dozens of pattern layers. In the recent years, Stream of Variation (SoV) type models [7] were used to model this inherent multistage character of lithography processes and augment R2R control paradigm with considerations of stack-up overlay errors across non-neighboring pattern layers. In [8], an algorithm for optimal stochastic control of overlay and stack-up overlay errors was developed under Bayesian assumptions of perfectly known model parameters, with model noise and bias prediction errors assumed to be normal, independent, and identically distributed (NIID) random variables. However, the tractability and convincing nature of results from [8] are predicated on the aforementioned assumptions, which are overly restrictive, especially in the area of semiconductor photolithography, where extremely minute scale of the relevant phenomena implies that the corresponding models are rarely (never) perfect, while model noise terms are rarely (never) NIID.

This problem is recognized in [9], where the authors introduced a control approach robust to imperfections in the knowledge of the variance characteristics of modeling noise and errors in bias predictions. Results in [9] showed significant benefits, especially when the bounds on the unknown parameters become wide. These results were further expanded in [10], where SoV-form models were used to devise a generalized method for robust control of multistage manufacturing quality in the presence of bounded noise terms and uncertainties in model parameters. Applications of this control approach in simulations based on the data and models of lithography overlay processes from a real fab demonstrated consistent and significant benefits of robust, distribution-agnostic control paradigm from [10] over traditional R2R approach [6], or Bayesian multistage method from [8].

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Nevertheless, in all overlay control research thus far [6,10], characterization of overlays in any given pattern layer is approached using the standard Euclidean norm of the vector consisting of overlay errors measured in all available markers. On the other hand, performance of the resulting microelectronic device is often (usually) driven by the worst overlay in the wafer area relevant to that device. Therefore, rather than minimizing the Euclidean L^2 norm of the vector of overlay errors in a given pattern layer, as was done in [6,10], one needs to minimize the L^∞ norm (maximum element) of that vector. This is a much more difficult, less tractable problem and it will be addressed in this paper.

Specifically, this paper presents a control strategy that minimizes the maximum of the overlay errors across all measurement markers in the entire pattern layer, and we seek to do that robustly, for the worst-case scenario of bounded modeling noise terms and errors in bias predictions. The remainder of this paper is structured as follows. Section 2 describes the formulation of the underlying min-max-max optimization problem, as well as its transformation into a tractable form that can be efficiently solved. Section 3 evaluates the performance of the newly proposed control strategy and compares it with that of the conventional R2R control, as well as with the overlay control approach from [10], which robustly minimizes the L^2 norm of overlay errors. All comparisons are done using data and models from a major 300 mm semiconductor manufacturing fab. Finally, Section 4 offers conclusions of the research presented in this paper and details directions for possible future work.

2. Methods

As demonstrated in [10], the vectors \mathbf{o}_k^x and \mathbf{o}_k^y consisting of overlay errors in the x and y directions in all measurement markers across pattern layer k can be expressed as:

$$\mathbf{o}_k^x = \mathbf{D}^x \cdot (\mathbf{u}_k^x + \mathbf{c}_k^x) + \mathbf{r}_k^x; \mathbf{o}_k^y = \mathbf{D}^y \cdot (\mathbf{u}_k^y + \mathbf{c}_k^y) + \mathbf{r}_k^y \quad (1)$$

where matrices \mathbf{D}^x and \mathbf{D}^y denote regression matrices that are fully defined by locations of measurement markers on the wafer, vectors \mathbf{u} consist of control commands issued to the tool, vectors \mathbf{c} denote random vectors of process biases which inherently exist due to actuator uncertainties and minute scale of overlay errors, while vectors \mathbf{r} contain residuals that account for unmodeled effects and process noise. The goal of the control strategy presented in this paper is to minimize the largest magnitude of overlay errors present on a pattern layer, subject to the worst-case scenario regarding process bias terms and model residuals, which are unknown, but are assumed to reside within known limits.

The magnitude of overlay errors at measurement marker j of pattern layer k is naturally defined using the Euclidean L^2 norm as $\|\mathbf{o}_{k,j}\| = \sqrt{\mathbf{o}_{k,j}^x 2 + \mathbf{o}_{k,j}^y 2}$. However, minimization of the maximum of such Euclidean overlay norms across all markers j , which is also robust to bounded uncertainties in model noise terms and bias prediction errors ends up being an intractable, large-scale nonlinear optimization problem. Instead, standard inequality

$$\|\mathbf{o}_{k,j}\| = \sqrt{(\mathbf{o}_{k,j}^x)^2 + (\mathbf{o}_{k,j}^y)^2} \leq |\mathbf{o}_{k,j}^x| + |\mathbf{o}_{k,j}^y|$$

can be used to establish an upper bound on the Euclidean magnitude of overlay errors. This allows for the robust optimal control problem for pattern layer k of wafer t to be formulated as

$$\begin{aligned} (\mathbf{u}_{t,k}^{x*}, \mathbf{u}_{t,k}^{y*}) = \underset{\substack{\mathbf{u}_{t,k}^x \in \mathbb{R}^{N_x}, \\ \mathbf{u}_{t,k}^y \in \mathbb{R}^{N_y}}}{\operatorname{argmin}} \max_{\substack{\mathbf{c}_{t,k}^x, \mathbf{c}_{t,k}^y \\ \mathbf{r}_{t,k}^x, \mathbf{r}_{t,k}^y}} \max_{j \in \{1, 2, \dots, J\}} |\mathbf{o}_{t,k,j}^x| + |\mathbf{o}_{t,k,j}^y| \end{aligned}$$

$$\text{s.t. } \begin{cases} \mathbf{o}_{t,k}^x = [\mathbf{D}^{Cx} \cdot (\mathbf{u}_{t,k}^x + \mathbf{c}_{t,k}^x) + \mathbf{r}_{t,k}^x] \\ \mathbf{o}_{t,k}^y = [\mathbf{D}^{Cy} \cdot (\mathbf{u}_{t,k}^y + \mathbf{c}_{t,k}^y) + \mathbf{r}_{t,k}^y] \\ \mathbf{c}_{t,k}^{lb,x} \leq \mathbf{c}_{t,k}^x \leq \mathbf{c}_{t,k}^{ub,x}; \mathbf{c}_{t,k}^{lb,y} \leq \mathbf{c}_{t,k}^y \leq \mathbf{c}_{t,k}^{ub,y} \\ \mathbf{r}_{t,k}^{lb,x} \leq \mathbf{r}_{t,k}^x \leq \mathbf{r}_{t,k}^{ub,x}; \mathbf{r}_{t,k}^{lb,y} \leq \mathbf{r}_{t,k}^y \leq \mathbf{r}_{t,k}^{ub,y} \end{cases} \quad (2)$$

where $(\mathbf{u}_{t,k}^{x*}, \mathbf{u}_{t,k}^{y*})$ are resulting vectors of optimal control commands that are to be given to the lithography tool to fabricate pattern layer k on wafer t , limits $\mathbf{c}_{t,k}^{lb,x}$, $\mathbf{c}_{t,k}^{lb,y}$, $\mathbf{r}_{t,k}^{lb,x}$ and $\mathbf{r}_{t,k}^{lb,y}$ denote the lower bounds on the bias vectors \mathbf{c} and modeling noise terms \mathbf{r} , limits $\mathbf{c}_{t,k}^{ub,x}$, $\mathbf{c}_{t,k}^{ub,y}$, $\mathbf{r}_{t,k}^{ub,x}$ and $\mathbf{r}_{t,k}^{ub,y}$ denote the corresponding upper bounds, while integers J , N_x and N_y respectively denote the number of markers on the wafer and the numbers of controllable tool parameters in the x and y directions of the wafer.

Let us now transform formulation (2) into a tractable linear program (LP), which can be solved in times comparable to cycle-times relevant to the underlying process (seconds). To simplify notation, we will observe that optimization (2) is performed for a given pattern layer on a given wafer and hence the subscripts t and k can be neglected. Let us now define vector ξ as

$$\xi = [c_x' T \ c_y' T \ r_x' T \ r_y' T]^T$$

i.e., as the vector containing all uncertain terms in the optimization (2). Also, let us define matrices

$$\begin{aligned} \mathbf{M}_{Cx} &= [\mathbf{I}_{N_x} \ 0_{N_x \times N_y} \ 0_{N_x \times N_x} \ 0_{N_x \times N_y}] \\ \mathbf{M}_{Cy} &= [0_{N_y \times N_x} \ \mathbf{I}_{N_y} \ 0_{N_y \times N_x} \ 0_{N_y \times N_y}] \\ \mathbf{M}_{Rx} &= [0_{N_x \times N_x} \ 0_{N_x \times N_y} \ \mathbf{I}_{N_x} \ 0_{N_x \times N_y}] \\ \mathbf{M}_{Ry} &= [0_{N_y \times N_x} \ 0_{N_y \times N_y} \ 0_{N_y \times N_x} \ \mathbf{I}_{N_y}] \end{aligned}$$

where \mathbf{I}_n denotes an n -by- n identity matrix, while $0_{p,q}$ denotes a p -by- q matrix of zeros. Then, if we define matrix \mathbf{S} as

$$\mathbf{S} = [\mathbf{M}_{Cx}^T \ -\mathbf{M}_{Cx}^T \ \mathbf{M}_{Cy}^T \ -\mathbf{M}_{Cy}^T \ \mathbf{M}_{Rx}^T \ -\mathbf{M}_{Rx}^T \ \mathbf{M}_{Ry}^T \ -\mathbf{M}_{Ry}^T]^T$$

and vector \mathbf{g} as

$$\mathbf{g} = [c^{ub,xT} \ -c^{lb,xT} \ c^{ub,yT} \ -c^{lb,yT} \ r^{ub,xT} \ -r^{lb,xT} \ r^{ub,yT} \ -r^{lb,yT}]^T$$

constraints

$$\begin{aligned} c_{t,k}^{lb,x} \leq c_{t,k}^x \leq c_{t,k}^{ub,x}; \ c_{t,k}^{lb,y} \leq c_{t,k}^y \leq c_{t,k}^{ub,y} \\ r_{t,k}^{lb,x} \leq r_{t,k}^x \leq r_{t,k}^{ub,x}; \ r_{t,k}^{lb,y} \leq r_{t,k}^y \leq r_{t,k}^{ub,y} \end{aligned}$$

on the uncertain bias vectors \mathbf{c} and model residuals \mathbf{r} in (2) can be compactly expressed as $\mathbf{S} \cdot \xi \leq \mathbf{g}$, and the uncertainty set ξ can be defined as $\xi = \{\xi \in \mathbb{R}^{N_\xi} : \mathbf{S} \cdot \xi \leq \mathbf{g}\}$, where N_ξ is the number of elements in ξ .

Following [11], the two maximizations in (2) can be brought into the constraints, which allows us to reformulate (2) as

$$\begin{aligned} (\mathbf{u}_x^*, \mathbf{u}_y^*) = \underset{\substack{\mathbf{u}_x \in \mathbb{R}^{N_x}, \\ \mathbf{u}_y \in \mathbb{R}^{N_y}}}{\operatorname{argmin}} \min_{\tau \in \mathbb{R}} \tau \end{aligned} \quad (3)$$

$$\text{s.t. } \tau \geq \max_{j \in \{1, 2, \dots, J\}} \max_{\xi \in \xi} |\mathbf{o}_j^x| + |\mathbf{o}_j^y| \quad (4)$$

$$\text{s.t. } \begin{cases} \mathbf{o}^x = \mathbf{D}^{Cx} \cdot (\mathbf{u}_x + \mathbf{M}_{Cx} \cdot \xi) + \mathbf{M}_{Rx} \cdot \xi \\ \mathbf{o}^y = \mathbf{D}^{Cy} \cdot (\mathbf{u}_y + \mathbf{M}_{Cy} \cdot \xi) + \mathbf{M}_{Ry} \cdot \xi \end{cases} \quad (5)$$

Constraint (4) is equivalent to

$$\tau \geq \max_{\xi \in \xi} |\mathbf{o}_j^x| + |\mathbf{o}_j^y| \quad \forall j \in \{1, 2, \dots, J\} \quad (6)$$

which can be used to replace the nested max-max optimization in (4) with J constraints (6), each containing a single optimization within it. By using this new set of constraints and defining vectors \mathbf{e}_j as a J -dimensional vectors with a 1 in the j th position and zeros in all other positions, optimization (3)–(5) can be rewritten as

$$\begin{aligned} (\mathbf{u}_x^*, \mathbf{u}_y^*) = \underset{\substack{\mathbf{u}_x \in \mathbb{R}^{N_x}, \\ \mathbf{u}_y \in \mathbb{R}^{N_y}}}{\operatorname{argmin}} \min_{\tau \in \mathbb{R}} \tau \end{aligned}$$

$$\text{s.t. } \tau \geq \max_{\xi \in \xi} \left\{ \left| \mathbf{e}_j^T \cdot \mathbf{D}^{Cx} \cdot (\mathbf{u}_x + \mathbf{M}_{Cx} \cdot \xi) + \mathbf{M}_{Rx} \cdot \xi \right| + \left| \mathbf{e}_j^T \cdot \mathbf{D}^{Cy} \cdot (\mathbf{u}_y + \mathbf{M}_{Cy} \cdot \xi) + \mathbf{M}_{Ry} \cdot \xi \right| \right\}$$

$$+ \left| \mathbf{e}_i^T \cdot \mathbf{D}^{CY} \cdot \left(\mathbf{u}_y^+ \mathbf{M}_C \mathbf{y} \cdot \xi \right) + \mathbf{M}_R \mathbf{x} \cdot \xi \right| \}, \forall j \in \{1, 2, \dots, J\} \quad (7)$$

For overlay errors σ_j^x and σ_j^y in marker j , expression $|\sigma_j^x| + |\sigma_j^y|$ can take on the form of either $\sigma_j^x + \sigma_j^y$, $\sigma_j^x - \sigma_j^y$, $-\sigma_j^x + \sigma_j^y$, or $-\sigma_j^x - \sigma_j^y$. Thus, each absolute value in the constraints on the variable τ in (7) can be replaced with four constraints - one for each possible permutation of the signs. Then, by defining indices $s_{x,i}$ and $s_{y,i}$ as follows:

$$s_{x,i} = 1 \quad \forall i \in \{1, 2, \dots, 2J\}; \quad s_{x,i} = -1 \quad \forall i \in \{2J+1, 2J+2, \dots, 4J\}$$

$$s_{y,i} = 1 \quad \forall i \in \{1, 2, \dots, J\} \cup \{2J+1, 2J+2, \dots, 3J\}$$

$$s_{y,i} = -1 \quad \forall i \in \{J+1, J+2, \dots, 2J\} \cup \{3J+1, 3J+2, \dots, 4J\}$$

as well as vectors

$$\mathbf{a}_i^T = s_{x,i} \cdot \mathbf{e}_i^T \cdot (\mathbf{D}^{CX} \cdot \mathbf{M}_{CX} + \mathbf{M}_{RX}) + s_{y,i} \cdot \mathbf{e}_i^T \cdot (\mathbf{D}^{CY} \cdot \mathbf{M}_{CY} + \mathbf{M}_{RY})$$

and functions

$$b_i(\mathbf{u}_x, \mathbf{u}_y) = s_{x,i} \cdot \mathbf{e}_i^T \cdot \mathbf{D}^{CX} \cdot \mathbf{u}_x + s_{y,i} \cdot \mathbf{e}_i^T \cdot \mathbf{D}^{CY} \cdot \mathbf{u}_y$$

for $i \in \{1, 2, \dots, 4J\}$, optimization (7) can be rewritten as

$$\left(\mathbf{u}_x^*, \mathbf{u}_y^* \right) = \underset{\substack{\mathbf{u}_x \in \mathbb{R}^{N_x}, \\ \mathbf{u}_y \in \mathbb{R}^{N_y}}}{\operatorname{argmin}} \min_{\tau \in \mathbb{R}} \tau$$

$$\text{s.t.} \cdot \tau \geq \max_{\xi \in \xi} \{ \mathbf{a}_i^T \cdot \xi + b_i(\mathbf{u}_x, \mathbf{u}_y) \} \cdot \forall i \in \{1, 2, \dots, 4J\} \quad (8)$$

Functions b_i do not depend on ξ and hence, (8) can be rewritten as

$$\left(\mathbf{u}_x^*, \mathbf{u}_y^* \right) = \underset{\substack{\mathbf{u}_x \in \mathbb{R}^{N_x}, \\ \mathbf{u}_y \in \mathbb{R}^{N_y}}}{\operatorname{argmin}} \min_{\tau \in \mathbb{R}} \tau \quad (9)$$

$$\text{s.t.} \cdot \tau - b_i(\mathbf{u}_x, \mathbf{u}_y) \geq \max_{\xi \in \xi} \{ \mathbf{a}_i^T \cdot \xi \} \quad \forall i \in \{1, 2, \dots, 4J\} \quad (10)$$

Following [12], one can dualize optimization problems (10), transforming them into

$$\tau - b_i(\mathbf{u}_x, \mathbf{u}_y) \geq \min_{\theta_i \in \mathbb{R}_+^{N_x}} \{ \theta_i^T \cdot \mathbf{g} \} \quad \forall i \in \{1, 2, \dots, 4J\} \quad (11)$$

$$\text{s.t.} \cdot \mathbf{a}_i^T - \theta_i^T \cdot \mathbf{S} = 0. \quad (12)$$

where θ_i is the vector of dual variables corresponding to the constraint $\xi \in \xi$ in the i^{th} optimization problem described by (10).

Let us observe that (11) is always satisfied as long as we have

$$\tau - b_i(\mathbf{u}_x, \mathbf{u}_y) \geq \theta_i^T \cdot \mathbf{g}$$

This more stringent constraint can now be used to eliminate the minimization in (11) the final formulation of the optimization problem can be expressed as the following LP

$$\left(\mathbf{u}_x^*, \mathbf{u}_y^* \right) = \underset{\substack{\mathbf{u}_x \in \mathbb{R}^{N_x}, \\ \mathbf{u}_y \in \mathbb{R}^{N_y}}}{\operatorname{argmin}} \min_{\tau \in \mathbb{R}} \tau$$

$$\text{s.t.} \left\{ \begin{array}{l} \tau - b_i(\mathbf{u}_x, \mathbf{u}_y) \geq \theta_i^T \cdot \mathbf{g} \quad \forall i \in \{1, 2, \dots, 4J\} \\ \mathbf{a}_i^T - \theta_i^T \cdot \mathbf{S} = 0 \quad \forall i \in \{1, 2, \dots, 4J\} \end{array} \right. \quad (13)$$

This LP can be solved within seconds using standard solvers. Hence, one can use (13) to obtain control vectors \mathbf{u}_x^* and \mathbf{u}_y^* for the lithography tool which will minimize the maximum of overlay errors in the relevant pattern, under the worst-case scenario of bounded uncertainties in the bias terms and modeling residuals.

3. Results

The min-max-max robust control method described in the previous section is evaluated using lithography overlay error models and measurements of overlay errors obtained from 4 consecutive pattern layers of more than 80 wafers processed in a major 300 mm fab. Due

to its proprietary nature, the raw data was altered via random, but consistent scaling, which removed physical units from the data, but still allowed relative comparison of controller performances in terms of outgoing overlay errors.

Performance of the newly proposed control algorithm was compared to that of R2R control approach commonly utilized by industrial practitioners, as well as with performance of the overlay control method described in [10], where Euclidean L^2 norms of overlay errors rather than their worst magnitudes are controlled. Gaussian process regression (GPR) based prediction [13] was used to predict process biases and thus realize the R2R control approach. Even though other prediction methods, such as Kalman Filtering or Exponentially Weighted Moving Average based prediction, are more commonly used in the industry [6], GPR was used in this paper because it allows for the modeling of non-stationary, heteroscedastic processes typically observed in a fab. It also naturally yields predictions of process biases, along with the corresponding uncertainty bounds needed for the underlying robust optimization.

Following suggestion from fab engineers, predictions of process biases and corresponding uncertainty bounds were obtained using a moving window of 30 wafers which was sliding through the actual fab data. For each of those windows of 30 wafers, stochastic behavior of overlays for the next wafer was simulated by the following procedure. We drew 100 samples of process biases from the corresponding GPR-predicted normal distributions, and for each marker location, we drew 100 samples of residuals from actual, location-specific distributions of those residuals, modelled based on the data from the fab. Then, for each of those 100 samples of vectors of biases and residuals, we calculated the overlays at each measurement marker using Zernike polynomial models of the form (1), with control signals \mathbf{u} given by the R2R controller (labelled as *R2R control strategy*), by the control method from [10] (labelled as *Robust L^2 control strategy*) and by the novel robust control method introduced in this paper (labelled as L^∞ control strategy). This produced 3 distributions of vectors of overlay errors: one for R2R control strategy, one for the L^2 control strategy and one for the L^∞ control strategy. Those distributions were then compared and results of those comparisons are presented here. Please note that the actual values have been removed from all figures due to the proprietary nature of the data.

Fig. 1 and **Table 1** show results from 100 simulations of the 31st wafer, which correspond to simulations based on the data records from the first window of 30 wafers. These simulations produced 100 values for the mean, standard deviation, median, and maxima of overlay errors in all measurement markers across the 31st wafer. **Fig. 1** shows box-and-whisker plots of those distributions for the three controllers, while **Table 1** offers a summarized results of comparisons of several metrics of those distributions. Namely, top entries in each cell of **Table 1** show percent reductions in the average, maximum and standard deviation of the distribution of overlay error magnitudes produced by the newly proposed L^∞ -robust controller, when compared to the corresponding metrics in the distribution of overlay error magnitudes produced by the R2R controller. Similarly, bottom entries in each cell of **Table 1** show the corresponding percent reductions given by the L^2 -robust controller from [10], when compared to the distribution metrics produced by the R2R controller. It is clearly visible that on this wafer, the newly proposed L^∞ -robust control strategy outperformed the benchmark control strategies in every metric, with that improvement being even larger relative to the R2R controller.

Fig. 2 shows averages, standard deviations, medians and maxima of distributions of overlay magnitudes obtained when the above-described simulations were performed for 20 consecutive wafers (i. e. with 20 consecutive windows of 30 wafers sliding through the historical data from the fab). Visual inspection of these results shows that the novel L^∞ -robust control strategy consistently outperforms the benchmark strategies in every metric, with that improvement being quite remarkable when compared to the R2R control performance. This is confirmed by the data shown in **Table 1**, which lists statistical metrics of the distributions shown in **Fig. 1**. Furthermore, the novel control scheme significantly outperforms both benchmark controllers when it comes to the maximal overlay

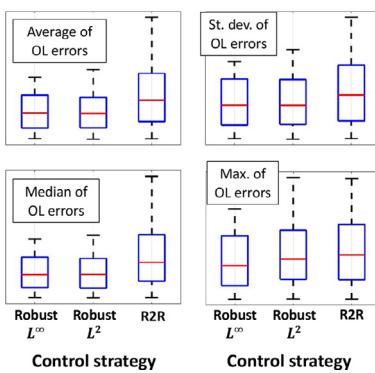


Fig. 1. Average, standard deviation, median, and maximum of overlay errors across 100 simulations of the 31st wafer under the R2R, L^2 -robust and L^∞ -robust control strategies.

Table 1

Percent reductions in metrics of the distributions of statistical measures of overlay errors across 100 simulations of the 31st wafer.

Distribution of	Metric of the distribution	% Red. of Average	% Red. of Median	% Red. of Maximum	% Red. of St. Deviation
	Average OL Errors	52.3 (L^∞) 40.6 (L^2)	47.9 (L^∞) 45.9 (L^2)	61.0 (L^∞) 44.9 (L^2)	57.0 (L^∞) 42.6 (L^2)
	Median OL Errors	56.1 (L^∞) 43.3 (L^2)	50.5 (L^∞) 47.0 (L^2)	64.0 (L^∞) 47.0 (L^2)	60.3 (L^∞) 44.9 (L^2)
	Maximum OL Errors	32.6 (L^∞) 16.6 (L^2)	30.4 (L^∞) 29.5 (L^2)	34.2 (L^∞) 8.7 (L^2)	36.2 (L^∞) 12.7 (L^2)
	St. Dev. of OL Errors	29.2 (L^∞) 24.4 (L^2)	30.8 (L^∞) 34.2 (L^2)	32.9 (L^∞) 28.3 (L^2)	36.2 (L^∞) 25.2 (L^2)

error magnitudes on each wafer, which is not surprising given that the new controller was indeed designed to robustly control the worst-case overlay magnitudes. Finally, in terms of computational speed, the novel control scheme generates control commands within a few seconds, while the iterative optimization in the foundation of the L^2 -robust strategy from [10] truncated its iterations after about 15 min per pattern layer. This demonstrates yet another clear advantage of the newly proposed control strategy and explains why it kept outperforming the L^2 -robust strategy even in terms of the average and median of overlay error magnitudes.

Finally, let us note that the R2R methods rely on the assumption that the distributions of process biases and residuals are NIID. Since simulations conducted in this paper generated process biases by sampling from a normal distribution, one can infer that the R2R controller performed better in these simulations than it would have under a more realistic distribution of process biases. Therefore, implementation of the newly proposed controller in a real system would likely lead to even more pronounced performance improvements over the traditional R2R controller.

4. Conclusions and future work

This paper presents a novel method for control of overlay errors in photolithography processes which minimizes the maximum of the overlay errors across all measurement markers in the entire pattern layer, and does that robustly, for the worst case of uncertainties in bias prediction errors and overlay modeling residuals. When compared with the industrial standard of R2R control strategy, simulations of this method produce pattern layers with significantly smaller average and maximum overlay errors, as well as smaller standard deviations of overlay errors. These improved metrics could lead to higher yields and smaller minimum feature sizes for semiconductor manufacturers.

There are several directions for future work that could build upon the results of this paper. Firstly, this paper does not consider stack-up overlay errors, i.e. errors across non-neighboring pattern layers. Stack-up overlay errors are highly impacting on the performance metrics of the resulting products and future adaptations of the approach presented in this paper are needed to account for them. However, straightforward expansion of the methods presented in this paper would not be possible. Namely, the use of absolute values in the objective function (2), which does not account for stack-up overlay errors, required $4 \cdot J$

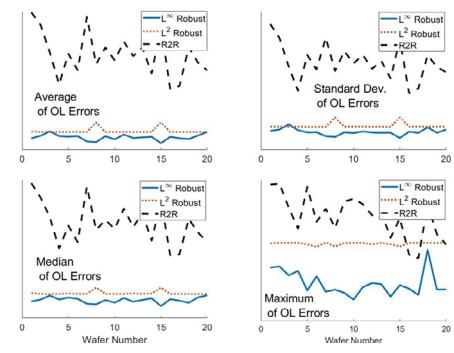


Fig. 2. Average, standard deviation, median, and maximum of overlay errors across 100 simulations of each wafer over 20 wafers under the R2R, wafer under the R2R, L^2 -robust and L^∞ -robust control strategies.

constraints in Eq. (5) to account for each permutation of the signs. Following such an approach to include an additional layer into (2) and thus account for stack-up overlay errors would require $16 \cdot J$ constraints and this number would be multiplied by 4 with each additional layer. This exponential scaling becomes unfeasible with modern semiconductor manufacturing which requires dozens of layers to be aligned properly. Future inclusion of stack-up overlay errors would require some reformulation of the problem (2) which would allow for more manageable scaling. Finally, the newly proposed control strategy is ready for implementation in an industrial setting where its true effects on overlay errors and yield rates can be observed.

Declaration of Competing Interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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References

- [1] Geng H (2018) *Semiconductor Manufacturing Handbook*, McGraw-Hill Educ. New York, NY, USA.
- [2] Ghaida RS, Gupta M, Gupta P (2013) Framework for Exploring the Interaction Between Design Rules and Overlay Control. *Journal of Micro/Nanolithography, MEMS, and MOEMS* 12(3): 033014–033014.
- [3] Mack CA (2012) *Fundamental Principles of Optical Lithography: The Science of Microfabrication*, WileyChichester, U.K.
- [4] Chien CF, Chang KH, Chen CP (2003) Design of a Sampling Strategy for Measuring and Compensating for Overlay Errors in Semiconductor Manufacturing. *Int'l. Jour. of Prod. Research* 41(11):2547–2561.
- [5] v. d. Brink MA, de Mol CGM, George RA (1988) Matching Performance for Multiple Wafer Steppers Using an Advanced Metrology Procedure. *SPIE Proc. Integrated Circuit Metrology, Inspection, and Proc. Control II* 921:180–197.
- [6] Tan F, Pan ZLT, Chen S (2015) Survey on Run-to-Run Control Algorithms in High-Mix Semiconductor Manufacturing Processes. *IEEE Trans. on Ind. Informat.* 11(6):1435–1444.
- [7] Hu SJ (1997) Stream of Variation Theory for Automotive Body Assembly. *CIRP Annals* 46(1):1–6.
- [8] Jiao Y, Djurdjanovic D (2011) Stochastic Control of Multilayer Overlay in Lithography Processes. *IEEE Trans. on Semicond. Manuf.* 24(3):404–417.
- [9] Djurdjanovic D, Jiao Y, Majstorovic V (2017) Multistage Manufacturing Process Control Robust to Inaccurate Knowledge about Process Noise. *CIRP Annals* 66(1):437–440.
- [10] Djurdjanovic D, Ul Haq A, Magnanini M, Majstorovic V (2019) Robust Model-Based Control of Multistage Manufacturing Processes. *CIRP Annals* 68(1):479–482.
- [11] Gorissen BL, Hertog D (2013) Robust Counterparts of Inequalities Containing Sums of Maxima of Linear Functions. *European Journal of Operational Research* 227(1):30–43.
- [12] Ben-Tal A, El Ghaoui L, Nemirovski A (2009) *Robust Optimization*, Princeton University PressPrinceton, NJ.
- [13] Le QV, Smola AJ, Canu S (2005) Heteroscedastic Gaussian Process Regression. In: *Proc. of the 22nd Int. Conf. on Mach. Learn. (ICML)*, 489–496.