# A 128-ch Area-Efficient Neurochemical-Sensing Front-End for FSCV Recordings of Dopamine

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Abstract—**Neurochemical recordings rely on electrochemical reactions of electroactive neurotransmitters such as dopamine, serotonin, and norepinephrine. This electrochemical technique allows for highly sensitive monitoring of neurotransmitters in the brain. Traditionally, single-channel carbon-fiber microelectrode (CFE) recordings have been considered the gold standard method. However, an alternative approach involves the use of a microelectrode array, which enables high spatiotemporal resolution imaging of electroactive neurotransmitters. To enable neurochemical imaging using a microelectrode array, the development of a high-density current-sensing microchip is necessary. Here, a neurochemical microchip is introduced, featuring a 128-channel current sensing front-end capable of supporting 128 parallel neurochemical measurements. The designed amplifier array employs a highly scalable resistive feedback transimpedance amplifier design. This design allows for a large neurochemical dynamic range of $\pm 5\ \mu A$ with a noise performance as low as 0.22 nA$_{RMS}$. The presented current sensing front end is validated by electrochemical detection of dopamine, by fast-scan cyclic voltammetry (FSCV) at 400 V/s, using an external carbon-fiber electrode in an electrochemical flow cell.**

Index Terms— **Current-sensing, dopamine, electrochemistry, fast-scan cyclic voltammetry (FSCV), microelectrode array, neural interface, neurochemical recordings, neurotransmitters.**
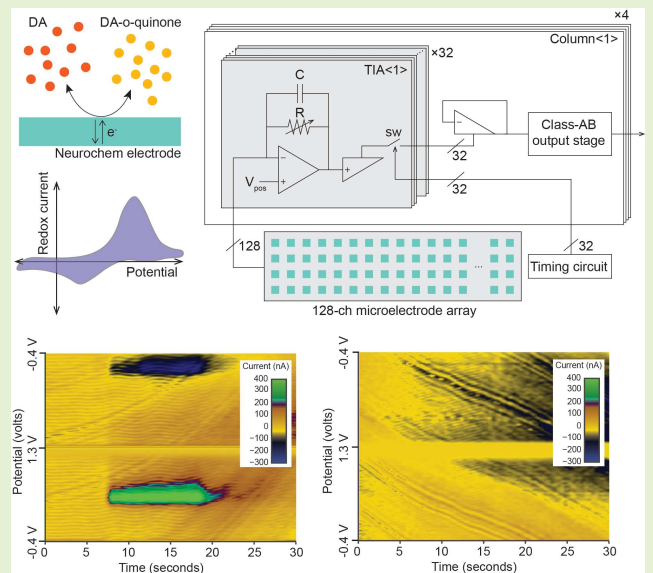
I. INTRODUCTION

HIGH spatiotemporal resolution imaging of dopamine in the brain is essential to study its complex interactions and functions in different regions and neural circuits. Dopamine (DA) is a key neurotransmitter that plays a crucial role in a wide range of neurological processes, including motivation, reward, and addiction. By imaging dopamine in real-time and with high spatial resolution, we can help elucidate the neurological function of neurochemicals in the central nervous system [2], [3], [4], [5], [6]. Moreover, high spatiotemporal resolution imaging of dopamine is critical for investigating dopamine-related disorders such as Parkinson's disease, schizophrenia, and Alzheimer's disease. Precise localization of dopamine release and uptake dynamics could aid in the identification of potential biomarkers or therapeutic targets for these disorders.
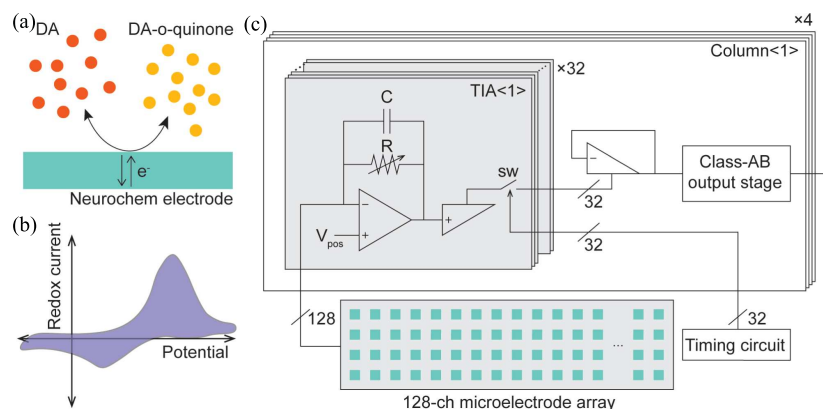
Electrochemical recordings of dopamine are widely used in neuroscience research as they provide real-time, highly sensitive measurements of dopamine release and uptake dynamics in the brain. These recordings are based on the electrochemical reactions of dopamine and other electroactive neurotransmitters at the surface of an electrode, which generates a current proportional to the amount of neurotransmitter present. Single-channel carbon-fiber microelectrode (CFE) recordings have been the gold standard for neurochemical recordings, but they are limited in their ability to capture spatial variations in neurotransmitter release. Current-sensing analog front ends are used as bioinstrumentation to apply a potential to an electrode to induce redox reactions while measuring the redox current (Fig. 1). Although a single CFE is commonly uti-

Fig. 1. Dopamine (DA) detection using 128-ch neurochemical front-end and FSCV. (a) Dopamine is detected by redox reactions at an electrode using FSCV, wherein molecules lose electrons (oxidation) or gain electrons (reduction). (b) FSCV recordings provide identification of a molecule, as well as the quantity of molecules, by the location and intensity of redox peaks versus the electrode potential. (c) Neurochemical sensing system incorporates a 128-ch neurochemical front-end for FSCV recordings using resistive-feedback TIA.

lized for in vivo monitoring of dopamine activity through fast-scan cyclic voltammetry (FSCV) [Fig. 1(b)] [2], [9], [10], [11], [12], [13], [14], a microelectrode array can offer a high spatiotemporal resolution by gathering neurochemical activity from a particular brain region to generate neurotransmitter mapping. To enable the operation and recording of a microelectrode array with a large number of electrodes (hundreds), a microchip containing an equal number of amplifiers is necessary to facilitate highly parallel electrochemical recordings [15], [16]. With the development of high-density microchips for processing and amplifying the small redox signals generated by these microelectrodes, electrochemical recordings of dopamine can provide a powerful tool for investigating the complex dynamics of dopamine signaling in the brain and its role in neurological diseases. Additionally, CMOS circuits and devices have been developed for high-density electrophysiological recordings [19], [20], [21], [22], and future multimodal CMOS bioelectronics may provide unique insight into brain functionality by providing simultaneous neurochemical and electrophysiological mapping of brain activity.

Typically, resistive feedback or integrating amplifier topologies are used to design analog front ends for electrochemical recordings. An integrating amplifier uses an operational amplifier (OPA) to regulate the electrode's potential and collects the electrode's current through a capacitor. The voltage change across the capacitor is then periodically read to monitor the current detected by the electrode. The main advantage of integrating amplifiers is their low noise characteristics [15], [23], [24], [25], [26], [27], [28], [29]; however, for large-dynamic range applications such as in vivo neurochemical recordings, the capacitor size can become too large, and integrating hundreds of amplifiers with a small and scalable silicon footprint can be challenging. For example, to achieve a $\pm 5\ \mu A$ dynamic range with a 20 kHz sampling rate, the integration capacitor must be around 150 pF. A CMOS potentiostat using a 50-fF integration capacitor was only able to record between ± 1.5 nA [23]. Another CMOS potentiostat using a 50 to 150 fF integration capacitor also has a limited dynamic range of 60 nA [30]. Although it is possible to integrate a few 150 pF capacitors using CMOS technology,

it is challenging to integrate 100 s of amplifiers. Current division or current subtraction can be used to decrease the required capacitance [1], [7], [8]; however, they come with a cost of power, area, or noise performance. An analog front-end with analog background subtraction was able to achieve high bandwidth (5 kHz) and low noise level (26.5 pA_{RMS}); however, the analog front-end size was relatively larger (0.256 mm^2) compared to other works that can achieve similar dynamic range [1]. Another subtraction-based circuit had a smaller footprint (0.06 mm^2), however, it suffered from a significant noise floor (10 pA/$\sqrt{Hz}$). Resistive feedback-based transimpedance amplifiers (TIA) can be suitable for large-dynamic range applications because the feedback resistors (around 1 MΩ and smaller) can be relatively smaller compared to equivalent capacitors (around 200 $\times$ 30 μm for 1 MΩ in a standard 0.35 μm process) [17].

In this article, we present a high-density neurochemical-sensing analog front-end microchip that integrates 128 resistive TIAs with multiplexers to support fully parallel 128-ch FSCV recordings of electrochemical activity [Fig. 1(c)]. The article is organized as follows. In Section II, the design of the neurochemical-sensing microchip and the circuit implementation are described. The characterization of gain, noise, and bandwidth performances are discussed in Section III. Section IV describes the carbon-fiber electrode fabrication and dopamine recordings using the neurochemical microchip, an external carbon fiber electrode, and an electrochemical flow cell. Finally, the conclusions and discussions are described in Section V.

II. HIGHLY SCALABLE NEUROCHEMICAL-SENSING AMPLIFIERS

To design a large array of TIAs, it is critical to develop a TIA that occupies a small footprint. While both resistive and capacitive feedback are commonly used, we consider both options to minimize the design area.

A. Highly Scalable Transimpedance Amplifier Design

In a standard 0.35 μm process, a poly capacitor has a capacitance density of 0.90 fF/μm^2. Instead, an n-well resistor is 1000 Ω/square and a poly resistor is 50/square. Given that
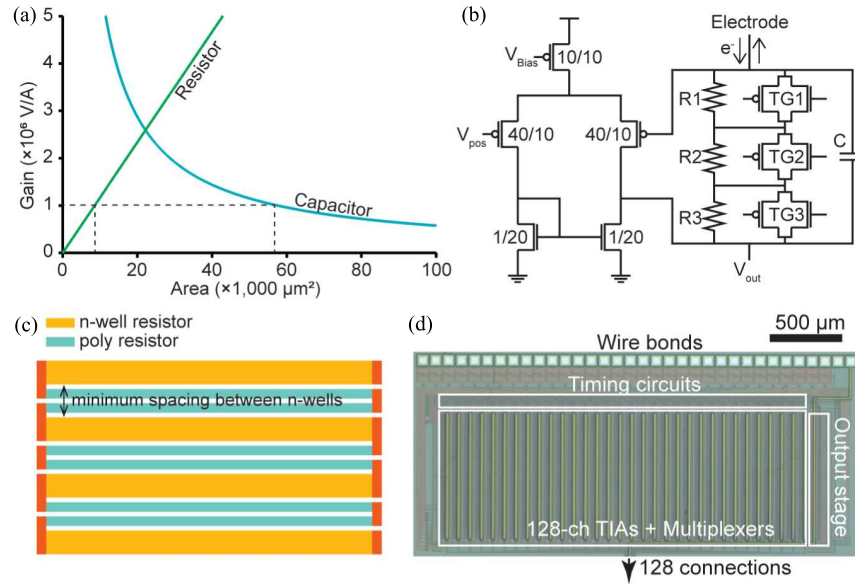
Fig. 2. Resistive feedback TIAs for 128-ch neurochemical sensing using FSCV. (a) Optimal transimpedance gain is in the range of 0.1–1 μA/V. To achieve this, a feedback resistor-based TIA can provide comparable gain to an integration capacitor-based TIA with lower area investment in layout. (b) Embedded transmission gate switches TG1–TG3 enable configurable TIA gain in tandem with the segmented feedback resistance. The feedback resistance directly determines the gain of the TIA and the gain on the scale of 100 kΩ–1 MΩ can be configured. The dimensions shown are W/L and are in μm. (c) To achieve maximum area efficiency when creating the layout, poly resistors are used in the spacing required between the n-well resistors for maximum resistance density. (d) Current sensing front-end has 128 parallel channels for neurochemical detection with the presented feedback resistor TIA arranged in a 4 $\times$ 32 array.

the minimum width of a n-well resistor is 3 μm (minimum square of 3 $\times$ 3 μm^2 according to the design rule), the resistance density can be calculated to be 110–120 Ω/μm^2. This translates to an achievable gain per unit area of 110–120 V/A/μm^2 [Fig. 2(a)]. Therefore, using resistive feedback, the gain increases linearly with the invested area.

In an integrating amplifier that uses capacitive feedback, the transimpedance gain is set by the integration time and capacitance:

$$\text{Gain [V/A]} = \frac{t_{\text{int}}}{C_{\text{int}}} \qquad (1)$$

where C_{int} is the value of the integration capacitor and t_{int} is the integration duration. Assuming a sampling rate of 20 kS/s, the highest integration time that can be allocated is 50 μs. The gain determined by the poly capacitor is 6×10^{10} V·μm^2/A. When the targeted transimpedance gain is 1×10^6 V/A, the resistive feedback requires substantially less space (9000 μm^2) compared to that of capacitive feedback (60 000 μm^2) [Fig. 2(a)]. Based on the gain-area plot, the integration capacitor becomes more area-efficient than the resistor when the desired gain is beyond 2.6×10^6 V/A. For FSCV measurements coupled with microelectrodes, the targeted transimpedance gain is typically between 0.1 and 1 μA/V, corresponding to 100 kΩ–1 MΩ. Therefore, the resistive TIA is significantly more area-efficient.

Our TIA design is based on resistive feedback in which the incoming redox current is multiplied by the resistance and directly changes the amplifier's output voltage [Fig. 2(b)]. The feedback resistance is divided into three resistors to enable programmable transimpedance gain. Each segmented resistor is linked to a parallel transmission gate (TG1–3),

which allows for adjustable transimpedance gain. The three transmission gate switches can be configured to produce 2^3 different gain settings. Because n-well resistors achieve the highest resistance density compared to other types of resistors present in the CMOS technology, n-well resistors are the preferred option for the highly scalable TIA design. However, the design rule sets a minimum spacing (typically equivalent to the minimum width of an n-well resistor) between n-wells with different potentials which adds a large overhead in integrating multiple segments of n-well resistors in series. This rule is to avoid significant current flow between n-wells. This drops the achievable gain per unit area to 55–60 V/A/μm^2. To maximize the resistance density, poly resistors can be added within the spacings [Fig. 2(c)]. Although the resistance per square for poly resistors is far smaller compared to that of n-well resistors, because the minimum width is smaller (below 1 μm^2), the resistance density is 60 Ω/μm^2, nearly half of that of n-well resistors. The three feedback resistors are made up of a combination of n-well resistors and poly resistors [as shown in Fig. 2(c)]. This configuration provides the highest resistor density per area without violating layout design rules (85–90 V/A/μm^2), which will still produce a higher gain compared to a capacitive design.

The transistor dimensions are selected to optimize the area efficiency while preserving gain, bandwidth, power efficiency, and uniformity based on Monte Carlo simulation [Fig. 2(b)]. The feedback capacitor restricts the bandwidth to a range of 10–100 kHz. The oxidation/reduction current flows through the feedback resistor, creating the transimpedance gain at the OPA's output. Each TIA occupies 220 $\times$ 80 μm (0.0176 mm^2) of space. A total of 128 TIAs occupies only 2.25 mm^2 silicon space [Fig. 2(b)].

(a)
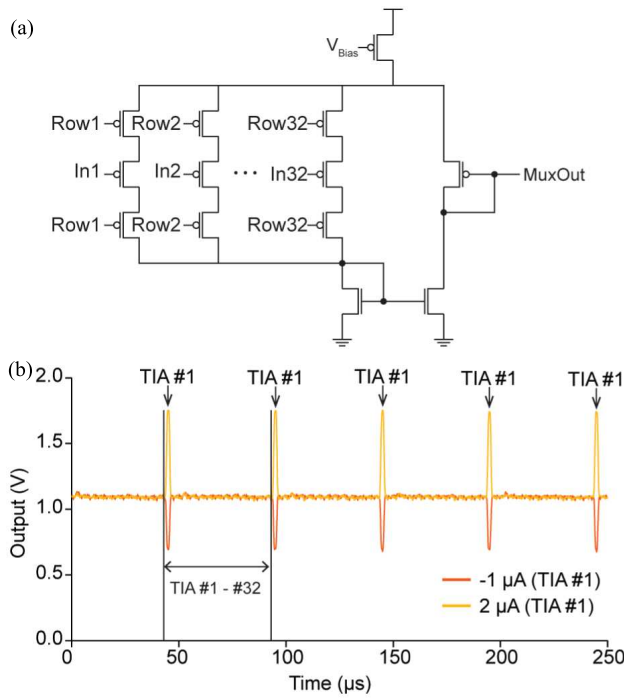
Fig. 3. To reduce the number of outputs, the amplifiers' outputs are combined in groups of 32 using time-division multiplexing. (a) Multiplexer is a half-shared structure. The non-inverting side is embedded in each amplifier and the inverting side is shared as a multiplexer output. (b) To demonstrate the time-division multiplexing output, the output of TIA #1, which is connected to In1 of the multiplexer, is measured. This TIA has an input current of either -1 or $2\ \mu$A while all other TIAs have 0 input current. At a 20 kHz sampling rate, the output of TIA #1 will be seen every 50 μs.

B. Time-Division Multiplexing

To reduce the number of outputs, the amplifiers' outputs are combined in groups of 32 as columns using time-division multiplexing. The system comprises four columns with a total of 128 fully parallel TIAs. The time-division multiplexing is based on a half-shared amplifier that connects individual TIA's outputs to the output stages using switches [15], [25], [27]. Each TIA contains an embedded non-inverting OPA (three transistors), which links to an inverting OPA in each column via a switch [Fig. 3(a)]. When Row1 is low (active low), the other row switches (Row2–Row32) are high so that only In1 is the unity gain input to the multiplexer's output, MuxOut. In a 50-μs interval, all 32 inputs (In1–In32) are multiplexed to MuxOut by staggering the switches (Row1–Row32). The output in Fig. 3(b) shows the multiplexed output where a TIA connected to In1 (TIA #1) has an input current of -1 or 2 μA. For this test, the rest of the TIAs have zero input current and therefore result in a flat output.

C. System Integration

The system consists of 128 TIAs, multiplexers, timing circuits, and output stages [Fig. 1(c)].As the outputs from this system are sampled using external analog-to-digital converters (ADC), class-AB output stages are designed within the system to drive large capacitive loads originating from the ADC's input capacitance and the line capacitance. The timing circuit produces all the sub-clocks required to run this multiplexing

scheme. The amplifier array is fabricated using a standard 2-poly 4-metal 0.35-μm process (Fig. 2). This process is chosen because of its wide voltage range. The amplifier array, timing circuit, output stage, and wire bonds occupy 3.05 $\times$ 1.37 mm.

In order to connect external electrodes to the amplifier array, such as a single CFE as discussed in Section IV, post-CMOS processing is performed. In brief, negative resist NR9-1500PY is used to create a sacrificial lift-off layer to pattern the bonding pads for external electrode connection. With the patterned sacrificial layer created, 20 nm of titanium and then 200 nm of platinum are deposited by sputtering. The negative resist is then removed by rinsing the chip with acetone. Then the chip is bonded to a custom PCB that allows the bonding pads created by post-CMOS processing to be connected to either external electrodes or an external microelectrode array.

III. Performance and Characteristics

Transimpedance gain, bandwidth, noise, and crosstalk of the TIA are evaluated in this section. For all tests, USB-6363 (National Instruments) is used for analog data acquisition. Current signals are generated using either USB-6363 DAQ for alternating current or a source/measure unit (B2901A, Keysight) for constant current. For noise measurements, the chip is powered using battery-powered voltage regulators to minimize the contribution of line frequency. For all data acquisitions, a custom-designed LabVIEW program is used. In this program, the clock source that governs the data sampling is synchronized with that of digital-to-analog converters (DACs) so that the artificial signals being injected into the amplifier have a sharply defined frequency component. This is beneficial in analyzing the frequency response of the amplifiers.

A. Measured Transimpedance Gain

To determine the gain of the current sensing front-end, an external current is introduced into the amplifier and the resulting change in output voltage is recorded [Fig. 4(a)]. Across the dynamic range shown, the amplifier exhibits a highly linear response in four gain settings, with an R^2 value of 0.999 for all gain settings. The dynamic range can vary from as high as $\pm 5\ \mu$A (Gain 4) to as low as ± 650 nA (Gain 1). Furthermore, the uniformity of the transimpedance gain across the array is evaluated. Any mismatch in the dimensions of devices in each TIA can contribute to non-uniformity in performance characteristics. The array demonstrates a consistent gain across the 128 amplifiers in the array [as Fig. 4(b)], with measured gains of 1.16 ± 0.04 MΩ, 618.93 ± 13.3 kΩ, 314 ± 9.63 kΩ, and 127 ± 2.17 kΩ (mean $\pm$ SD) for Gain 1, Gain 2, Gain 3, and Gain 4, respectively. The transimpedance gains plotted in histograms show Gaussian distributions for each gain setting [Fig. 4(b)]. Because this deviation is systematic, gains of each amplifier can be measured during a calibration step and subsequently applied individually to each amplifier's recordings to accurately represent the measured current.

B. Bandwidth, Noise, and Crosstalk

To test the bandwidth of the amplifier, sinusoidal currents (100 Hz–300 kHz) are injected into the amplifier while
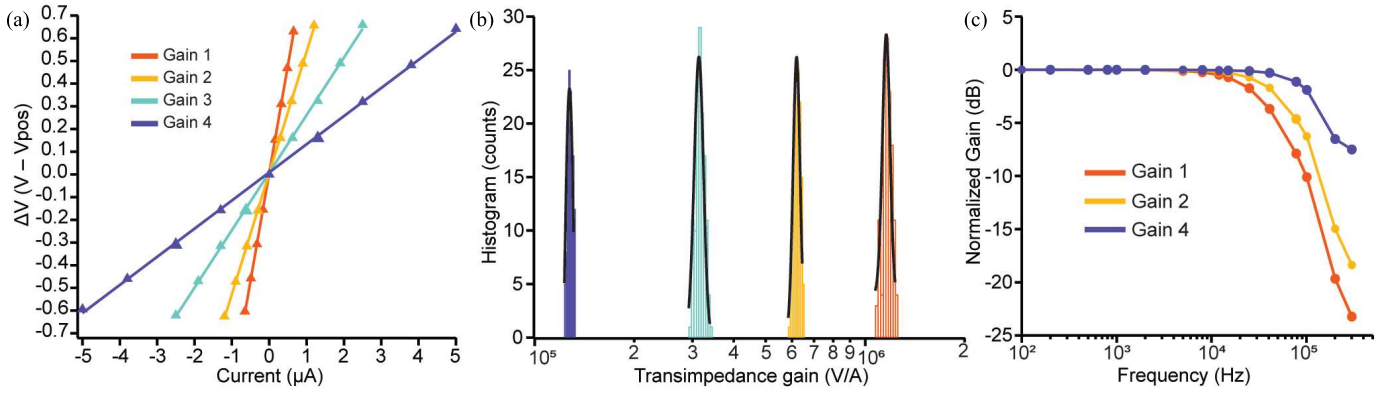
Fig. 4.   Measured transimpedance gain across the entire 128-amplifier array. (a) Gain calibration shows highly linear responses across the dynamic range with an R^2 of 0.999 for all gain settings. (b) 128-ch current sensing front-end exhibits a relatively consistent gain across the array with measured gains of 1.16 ± 0.04 MΩ, 618.93 ± 13.31 kΩ, 314.00 ± 9.63 kΩ, and 127.27 ± 2.17 kΩ (mean ± SD) for Gain 1, Gain 2, Gain 3, and Gain 4, respectively. (c) Measured bandwidth of the TIA for Gain 1, Gain 2, and Gain 4 (measured cutoff frequencies of 40, 53, and 122 kHz, respectively).

monitoring the response at the output [Fig. 4(c)]. For the three gain settings tested, the cutoff frequencies are 40, 53, and 122 kHz for Gain 1, Gain 2, and Gain 4, respectively. Because the cutoff is determined by $R_f C_f$, the lowest gain setting (Gain 4) exhibits the widest bandwidth. Because in most cases, a constant bandwidth is desirable across gain settings, a second stage behaving as a low-pass filter can be placed to eliminate the variability. However, in this application, to minimize the footprint of each TIA, an additional low-pass filter is not used.

The noise characteristics are measured as functions of gain setting and biasing level. The output of the amplifier is sampled at 640 kS/s (Fig. 5). The noise level of the amplifier is 0.22, 0.42, and 1.99 nA_{RMS} for Gain 1, Gain 2, and Gain 4, respectively. For three different bias levels between 1.1 μA to 20.1 μA (Gain 1), no appreciable difference is observable in the noise of the amplifier [Fig. 5(b)].

Crosstalk can often hinder the ability to provide independent high-density recordings. To verify the isolation between amplifiers in an array, it is important to examine crosstalk. Crosstalk is evaluated by introducing a large sinusoidal current (with a peak-to-peak amplitude of 0.8 μA and a frequency of 625 Hz) that covers almost the entire input dynamic range into one amplifier and observing the spectral response of neighboring amplifiers (as shown in Fig. 6). The highest gain setting (Gain 1) is used for this experiment to test the worst-case scenario. In the time domain, the crosstalk between the physically neighboring amplifiers is invisible [as shown in Fig. 6(a)]. In the spectral responses, the crosstalk is measured at adjacent amplifiers and found to be −82.5, −83.2, and −89.7 dBc [as depicted in Fig. 6(b)], showing nearly complete isolation between amplifiers.

IV. DOPAMINE RECORDINGS USING CARBON-FIBER ELECTRODE

To validate the neurochemical-sensing front-end for dopamine measurement, one of the amplifier's inputs is connected to a CFE. Using a flow cell setup, dopamine is introduced to the electrode while FSCV recordings are performed.

A. Carbon-Fiber Electrode Fabrication

The CFE fabrication process is based on a previously reported method [12], [14]. In brief, a 7-μm carbon fiber is threaded in through a glass capillary using a vacuum. Then, the glass capillary loaded with carbon fiber is pulled using a micropipette puller (P-87, Sutter Instrument) which creates two glass pipettes with carbon fiber. The carbon fiber extending from the tip of the glass pipette is initially trimmed using a surgical scissor to a few millimeters in length. Subsequently, the glass pipette with carbon fiber is placed under a microscope and the carbon fiber is additionally trimmed using a surgical blade to 140 μm in length. The resulting carbon-fiber electrode has a sensing area of 3100 μm^2. The carbon fiber is connected to a 22 AWG wire using silicon epoxy on the opposite side of the glass capillary.

B. Flow Cell Experiment

In order to introduce dopamine to the electrode with precision, a flow cell is typically used. For this experiment, an In vitro/FSCV microelectrode flow cell is used (NEC-FLOW-1, Pine Research). This flow cell is based on gravity feed. The fluid sample is loaded from below a flow channel (electrode channel) in which the carbon-fiber electrode is directly inserted. An Ag|AgCl reference electrode is used which is set to 1.55 V. This is to provide negative potential across the electrode-electrolyte interface without operating the CMOS chip at negative potentials. By default, phosphate-buffered saline (PBS) flows through the electrode channel with a flow rate of 1 mL/min. Dopamine solution can be added to this flow using an HPLC valve switch. This injection is governed by a syringe pump which controls the injection time, volume, and flow rate of dopamine solution (0.5–10 μM).

C. FSCV Recordings of Dopamine

All TIAs have a shared V_{pos} connection. The voltage ramp for FSCV is applied to the V_{pos} node at each TIA and sweeps from 1.15 to 2.85 V at a scan rate of 400 V/s. Against the Ag|AgCl reference electrode, the ramp range is between
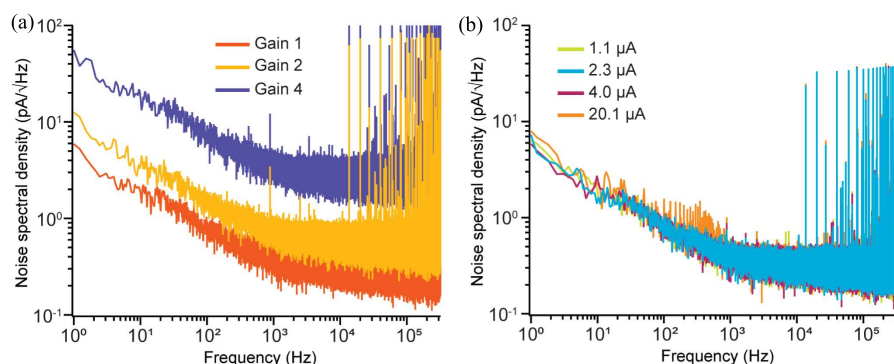
Fig. 5. Noise characteristics in various TIA gain settings and biasing current. (a) Noise level of the amplifier is 0.22 nA$_{RMS}$, 0.42 pA$_{RMS}$, and 1.99 nA$_{RMS}$ for Gain 1 (highest transimpedance gain), Gain 2, and Gain 4 (lowest transimpedance gain) respectively. (b) For bias levels between 1.1 and 20.1 μA at setting Gain 1, the noise density is consistent.
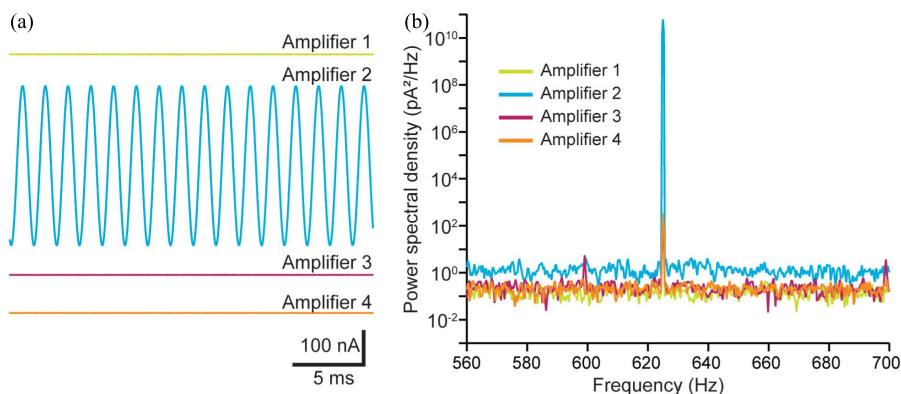
Fig. 6. Crosstalk experiment with a sinusoidal current is injected into Amplifier 2. (a) In the time domain, no crosstalk is observable. (b) Spectral analysis shows a small level of crosstalk in the adjacent amplifiers (-82.5, -83.2, and -89.7 dBc).
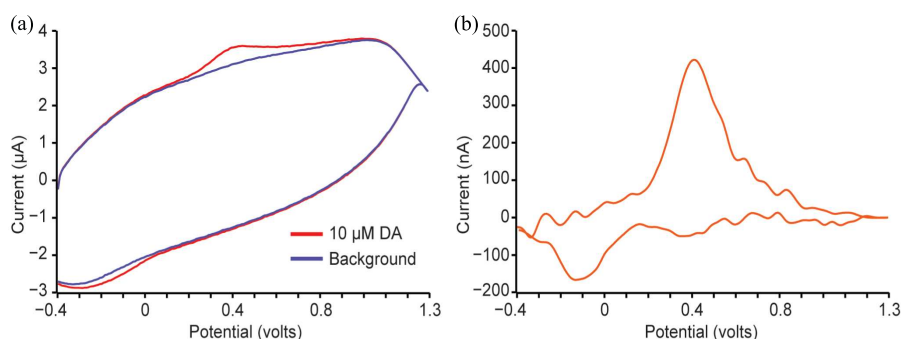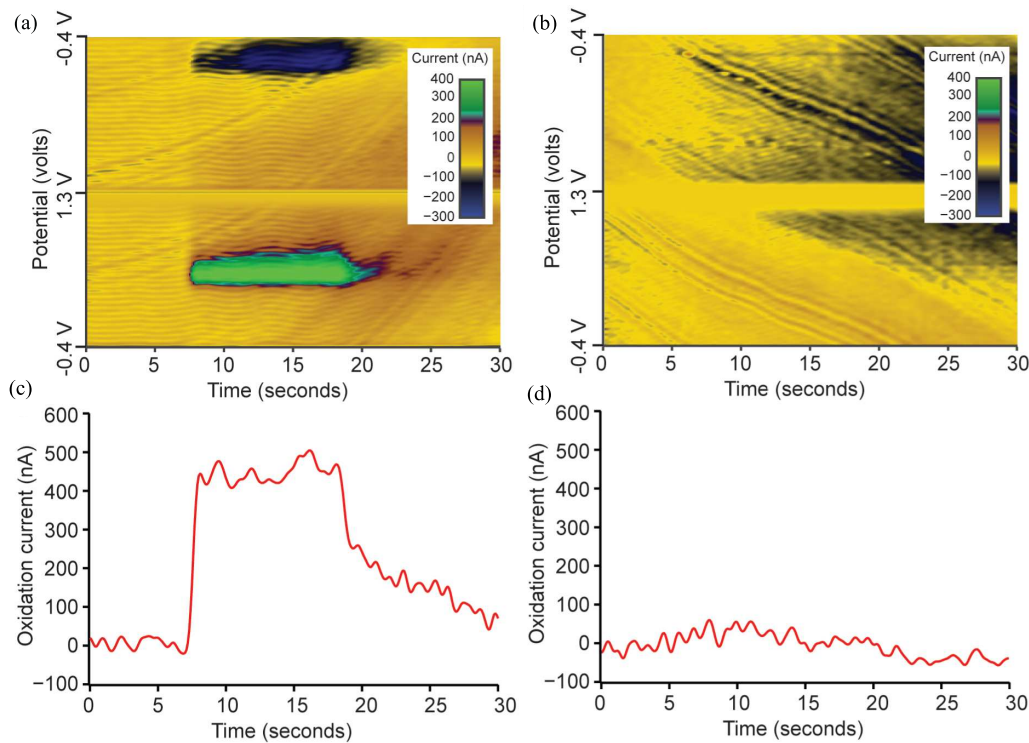

Fig. 7. Fast-scan cyclic voltammogram using the neurochemical chip and an external CFE. (a) In the background measurement, the CFE measures a current signal in PBS without dopamine (DA). This measured current is mostly from the double-layer capacitance of the electrode-electrolyte interface. The measurement is repeated with 10-μM DA in PBS. Redox peaks are visible near 0.4 and -0.2 V. (b) 10-μM DA measurement is subtracted using the background measurement to show the redox reactions of DA.

-0.4 and 1.3 V (E versus Ag|AgCl). The FSCV voltage ramp is repeated every 100 ms with an arresting potential of -0.4 V.

During the recording session, the PBS buffer is used as a default solution with a continuous flow rate of 1 mL/min. After 5–7 s of initial recording, DA solution is injected into the flow stream. For background subtraction, the initial 5-s FSCV recordings are averaged to create the background voltammogram.

For the 10-μM DA recording, the voltammogram taken after the DA injection shows distinct redox peaks compared to the background current [Fig. 7(a)]. Using the background subtraction, redox peaks are visible at 400 and -100 mV [Fig. 7(b)], consistent with previous CFE-based DA FSCV recordings [12], [31], [32]. The presented CFE-based DA recordings validate the neurochemical-sensing front end for dopamine detection. The recorded cyclic voltammograms can be plotted in a 2-D image to visualize the oxidation and reduction of DA in time [Fig. 8(a) and (b)]. The presence of dopamine can be seen between 7 and 20 s with strong oxidation and reduction currents [Fig. 8(a)]. In a control experiment in which PBS is injected without DA, redox currents are not detected as expected [Fig. 8(b)]. The oxidation

Fig. 8. Two-dimensional cyclic voltammogram using the neurochemical microchip and an external CFE. (a) and (b) CFE is mounted on a flow cell where dopamine (DA) is introduced between 5 and 7 s into the recording. For the control experiment, instead of DA, PBS is introduced. (c) and (d) Oxidation peaks of all the cycles are extracted for both DA measurement and control experiment.
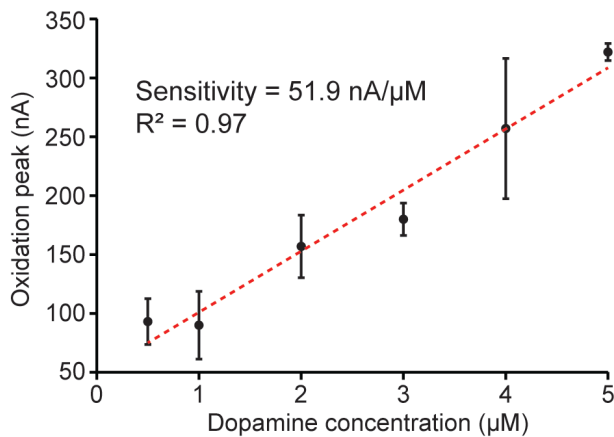
Fig. 9. DA sensitivity of CFE and neurochemical-sensing front end. The error bar is in the standard error of means.

peaks from individual cycles can be extracted to visualize the time-domain DA measurement. By extracting the oxidation currents at 400 mV, the transient response of DA shows a rapid increase in the oxidation of DA at 7 s, which continues for 13 s before tapering down [Fig. 8(c)]. No visible response is recorded in the control experiment [Fig. 8(d)], confirming that the current measured in the DA experiment is from the DA oxidation. Small ripplings in both 2-D voltammograms are originating from 60 Hz noise which is likely due to the largely unshielded connection between the CFE and the amplifier chip. In practice, the amplifier chip will be used as a headstage amplifier chip that is in close proximity to the neural probe

(<10 cm), and therefore, the 60 Hz noise will be substantially smaller.

In general, CFE should have a linear response to DA concentrations below 5 μM [31], [33], [34]. The flow cell experiment is repeated for various DA concentrations between 0.5 and 5 μM. The DA sensitivity of the CFE and the neurochemical-sensing front end is tested by plotting the oxidation currents for each DA concentration (Fig. 9). Each experiment is repeated three times. The sensitivity is 51.9 nA/μM which is consistent with previous reports of DA detection using CFE [12].

V. CONCLUSION AND DISCUSSION

This article introduces a high-density neurochemical microchip. In low-noise applications, an integration capacitor-based TIA is a common approach. However, for FSCV applications, a wide dynamic range is required, which necessitates a substantial area investment for these integration capacitors. This can be problematic when designing a large array of amplifiers. To address this, we investigated using n-well and poly resistors as feedback resistors to develop our scalable TIAs. The resistive TIAs we present have a small footprint of 220×80 μm and exhibit low-noise characteristics (0.22 nA$_{RMS}$) that are suitable for in vivo neurochemical applications. The noise floor for the highest gain setting (BW = 40 kHz) is 1.1 pA/$\sqrt{\text{Hz}}$. We achieved a wide dynamic range of ± 5 μA, which can be expanded further by opting for a smaller resistance value, albeit at the expense of noise characteristics and resolution.

TABLE I
COMPARISON TO STATE-OF-THE-ART CURRENT SENSING FRONT-ENDS

	Ying TBIOCAS '22 [1]	Zamani MWSCAS '20 [7]	Lu TBIOCAS '21 [8]	Valente EMBC '16 [17]	Nasri TBIOCAS '17 [18, 19]	**This Work**
AFE Type	Current conveyor	Current input -ΔΣ	I-to-F converter	Resistive TIA	Dual-slope integrating ADC	**Resistive TIA**
Technology Node (nm)	180	180	180	180	65	**350**
Channel Count	1	1	1	3	4	**128**
Channel Area (mm²)	0.256	0.06	-	0.21	0.075	**.0176**
Total Die Area (mm²)	-	-	3.17	9.95	-	**4.18**
On-chip ADC	Yes	Yes	Yes	Yes	Yes	**No**
Supply Voltage (V)	3.3	2.5	1.2 - 1.5	1.8	1.8	**4**
Power/Ch (μW)	3.7	14.2	19	954	3.1	**4.4**
Noise (nA$_{RMS}$)	0.027	0.71	25	0.292	0.02	**0.22**
Bandwidth (kHz)	5	5	50	100	1	**40-122**
Noise Density (pA/√Hz)	0.38	10	111	0.92	0.63	**1.1**
Input Range (μA)	0.375	± 1.5	-7 - +10	10	± 2.56	**± 5**

Compared to previously published works (Table I), our amplifier design achieves one of the lowest noise levels and smallest footprints. However, our approach uses off-chip ADCs for digitization which is beneficial in creating low power and small footprint circuits. Our future goal is to assess the amplifier's performance in FSCV applications by connecting it to a microelectrode array. This would enable high spatiotemporal resolution in vivo imaging of electroactive neurotransmitters in the brain, a feat not yet accomplished.

One of the key challenges in developing high-spatial recordings of neurochemicals is the development of robust microelectrode arrays that have a high level of specificity to faradaic current and stability without significant deterioration over time. While various materials have been tested for this purpose [35], [36], [37], [38], [39], carbon-based electrodes appear to be an appropriate candidate [39], [40], [41] for detecting electroactive neurotransmitters such as dopamine, epinephrine, norepinephrine, and serotonin. Additionally, electrode surface modifications are also being explored to enable the detection of neurotransmitters that are not electroactive, such as acetylcholine, choline, and L-glutamate [42], [43]. Our future work is aimed toward developing an on-chip microfabricated carbon-based electrode array in conjunction with this neurochemical chip to achieve high spatiotemporal resolution imaging of neurochemicals in vivo. The on-chip integration approach will bypass the bottleneck in scaling the number of electrodes.

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