

**Array-wide Uniform PEDOT:PSS Electroplating from Potentiostatic Deposition**

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## **Abstract**

Electroplating of poly(3,4-ethylenedioxythiophene) polystyrene sulfonate (PEDOT:PSS) is important in many neuroelectronic applications but is challenging to achieve uniformly on large-scale microelectrode arrays (MEA) using conventional galvanostatic methods. In this study, we address this challenge through a potentiostatic method and demonstrate highly uniform electroplating of PEDOT:PSS on MEA with more than one hundred electrodes, all at cellular sizes. The validation of this approach involves comparisons with galvanostatic deposition methods, showcasing unparalleled deposition yield and uniformity. Systematic electrochemical characterizations reveal similarities in structure and stability from potentiostatic deposited coatings. The advances developed here establish the potentiostatic method and detailed process to achieve a uniform coating of PEDOT:PSS on large-scale MEA, with broad utility in neuroelectronics.

## **Keywords**

large scale, microelectrode array, PEDOT:PSS, uniformity, potentiostatic

## **1. Introduction**

Bioelectricity plays a vital role in the brain (Levin, 2023; Hong and Lieber, 2019) , possessing rich spatiotemporal dynamic (Townsend and Gong, 2018) that necessitate the use of a large-scale MEA for advanced interfacing (Berdondini et al., 2009). These electrodes facilitate the recording of signals from multiple locations within the targeted tissues (Tian et al., 2023; Chung et al., 2019; Konerding et al., 2018; Lee et al., 2022; Viventi et al., 2011). For instance, the Neuropixel device has been recognized for its capacity to record from thousands of sites, enabling extensive single-unit recordings and brain activity mapping (Steinmetz et al., 2021). Clinical trials involving the Utah array with nearly one hundred microelectrodes have showcased its potential to empower patients in controlling prosthetic devices by neuronal signal interpretation and stimulation (Hochberg et al., 2012). Electrocorticography (ECoG) electrode arrays are instrumental in epilepsy treatment (Xie et al., 2017), and recently, ECoG arrays with hundreds of microelectrodes have enabled speech synthesis by decoding brain signals of spoken sentences (Angrick et al., 2019).

For microelectrodes, it's imperative to attain a coating of low-impedance materials to ensure high-quality electrode recording (Ludwig et al., 2006; Neto et al., 2018). Several low-impedance coatings, including Iridium Oxide (Cogan et al., 2008), Nanostructured Platinum

(Boehler et al., 2015), Platinum-Iridium (Petrossians et al., 2011), Titanium Nitride (Meijs et al., 2015), and PEDOT:PSS (Cui and Zhou, 2007) have been explored. Notably, PEDOT:PSS stands out uniquely due to its exceptional biocompatibility (Miriani et al., 2008), electrochemical property (Wang et al., 2021), and adjustable conductivity (Sanviti et al., 2022) when compared to other materials. ~~The current state-of-the-art approach for PEDOT:PSS coating involves galvanostatic electroplating (Pranti et al., 2018; Ji and Wang, 2020; Qiang et al., 2018), but often requires sequential deposition if there is a large number of microelectrodes on the array. Alternative deposition methods, such as spin coating (Cho et al., 2021) and sputtering (Hwang et al., 2006) exist; however, for large-scale, high-density devices, these techniques may yield non-uniform or non-reliable coatings, potentially compromising signal recording accuracy and stability.~~

For decades, various deposition methods have been investigated for PEDOT:PSS. For instance, spin coating (Illakkiya et al., 2018; Bessaire et al., 2017) could achieve high electrical conductivity and optical transparency with PEDOT-based polymers. However, spin coating is known to be limited to flat substrates and is prone to easier delamination of the film. Inkjet printing is also a recognized method for coating PEDOT-based polymers (Lo et al., 2021), but its effectiveness is contingent upon the physicochemical properties of the ink and requires a longer coating time. Another method is electrospinning, which recently showed PEDOT:PSS nanofiber coating with high electrical conductivity (Bessaire et al., 2017). However, achieving scalability with this methodology still poses challenges. Electroplating is extensively recognized as an effective and reliable method for the deposition of low-impedance materials on microelectrodes, typically utilizing a three-electrode system. Electroplating is further conducted under three modes, namely potentiostatic (constant potential), galvanostatic (constant current), and cyclic voltammetry (voltage sweeping). This method facilitates meticulous regulation of the coating's thickness and composition, offers a straightforward process, and requires minimal deposition time. Additionally, it is adaptable for versatile sample configurations, regardless of their physical forms.

While numerous studies have explored the electroplating of PEDOT-based polymers, the exploration of electroplating methods in existing literature often focuses on single electrodes, overlooking the complex dynamics of array-wide deposition essential for MEAs. Studies by Tran et al. (2023), Mousavi et al. (2023) and Teixeira et al. (2022) provide valuable insights into the electrodeposition kinetics and physicochemical characteristics of conducting polymers like PEDOT:PSS. Still, they are conducted on single electrodes or bulk substrates rather than on large-scale MEAs. MEA-level electroplating studies exist, but they usually lack

emphasis on electrodes with sizes at the cellular level. For instance, Castagnola et al. (2013), Ji and Wang (2018), and Pranti et al. (2018) have contributed significantly to the field by enhancing the electrochemical properties and biocompatibility of electrode surfaces; however, their investigations predominantly utilized electrodes with sizes orders of magnitude larger than cellular dimensions. This size difference underscores a critical gap, as larger electrode windows compromise spatial resolution, making them less effective for precise neuronal spike detection in highlighting the need for cellular-level electrode precision to enhance signal-to-noise ratio (SNR) (Viswam et al., 2019). Aqrawe et al. (2019) and Jones et al. (2020) have involved smaller electrodes and delved into the intricacies of electrode morphology including complex 3D structures, but a detailed investigation of scalability and uniformity across entire microelectrode arrays during electroplating processes remains missing. This gap hints at an overlooked opportunity to deepen our understanding and innovation in the realm of MEA design and fabrication.

To our knowledge, we present the first study focused on (1) array-wide uniform electroplating of PEDOT:PSS on large-throughput (>100 ch) MEAs, with electrode size down to cellular level; (2) comparison of similarities and differences between potentiostatic and galvanostatic electroplating methods in the deposition PEDOT:PSS on large-scale MEAs; (3) understanding of the electrochemical origin of the array-wide uniformity and better scalability from potentiostatic electroplating method. By addressing all the key points highlighted above, our study aims to significantly advance the understanding and application of electroplating in MEAs.

~~To address the uniformity challenges encountered during the deposition of PEDOT:PSS onto large-scale MEA, here we demonstrated a potentiostatic electroplating technique for parallel coating of this material uniformly on arrays of up to 128 microelectrodes. We conducted a comparative analysis between the potentiostatic and galvanostatic methods. Our evaluation revealed the potentiostatic method exhibited highly efficient deposition with significantly smaller deviations across microelectrodes than the conventional galvanostatic deposition. Electrochemical impedance spectroscopy (EIS) and cyclic voltammetry (CV) analyses of single electrodes revealed potentiostatic deposited PEDOT:PSS showed electrochemical properties similar to galvanostatic deposited coating. Lastly, we evaluated the chronic stability of PEDOT:PSS deposited using potentiostatic methods through accelerated aging tests. The results indicated impedance remained consistent over a 9-day period at 67 °C, equivalent to 72 days at 37 °C. This research suggests that the potentiostatic deposition of~~

~~PEDOT:PSS holds promise for neuroelectronics, particularly for large-scale device applications.~~

## **2. Materials and Methods**

### *2.1 Device fabrication*

To fabricate the substrate, 3×2 inch<sup>2</sup> glass was cleaned and blow-dried. Polydimethylsiloxane (PDMS, 10:1 ratio) was spin-coated and cured at 90 °C. Kapton (1 mil thick, Fralock) was laminated, and polyimide (PI 2545, HD Microsystems) was spin-coated (2 µm thick), cured at 110 °C for 3 minutes, 150 °C for 5 minutes, and 250 °C for 70 minutes. Metals (Cr: 5 nm, Au: 100 nm thick) were deposited via a thermal evaporator. After deposition, micropatterning was done by photolithography (M400, Midas Systems) using positive photoresist (PR, S1805 G2, MicroChem). Negative PR was spin-coated and patterned as an encapsulation layer (SU-8 2005, Kayaku) and hard baked at 180 °C for 30 min. A laser cutter (U4, LPKF) defined outline of the sample, then the device was peeled off from the PDMS layer.

### *2.2 Electrochemical deposition of PEDOT:PSS*

DI water (100 ml) was mixed with 3,4-Ethylenedioxythiophene (EDOT) monomer (0.1 % (w/v) 0.01 M, Sigma Aldrich) and poly(sodium-4-styrene sulfonate) powder (PSSNa, 0.7 % (w/v), Sigma Aldrich) to make precursors. After 3 hours of mixing with a magnetic stirrer, the beaker was left in ambient air for an hour to settle down the precipitates. Silver paste (Ted Pella) was pasted gently on the pad area and dried. The copper tape was taped on the edge to have good contact with the potentiostat. Before soaking the device into the solution, gentle O<sub>2</sub> plasma (100 W, 1 min) treated the device to have improved water wettability, then connected to a three-electrode system with a potentiostat (Reference 620, Gamry), using a platinum wire (0.5 mm diameter, Sigma Aldrich) as the counter electrode (CE) and an Ag/AgCl reference electrode (RE). Potentiostatic function was used for electroplating PEDOT:PSS with a certain amount of time. After deposition, the device was rinsed and blow-dried, removing excess silver paste and copper tape by swabbing in acetone bath.

### *2.3 Sample assembly*

In-house fabricated printed circuit boards (PCB) were used to connect the Intan head stage (RHS 32 ch, Intan technologies) to the electrodes. The device pads were aligned to the PCB solder balls and assembled in a fixed position by sandwiching with soft silicone polymers

(Ecoflex 0020, Smooth-on) and an acylate top board. Screws and nuts were assembled to secure the location on every corner of the top board. The PCB is soldered with 32 channel connectors (A79022-001, Omnetics), which could be assembled with the head stage later to measure the device impedances.

## 2.4 Surface Analysis

Digital Optical microscope (VHX-7000N, Keyence) was used to take sample images and check uniformities across the whole sample. Scanning electron miscocopy (SEM, (Thermo Fisher Scientific, Helios 5 CX DualBeam) was used to take the sample surface morphology by using secondary electron (SE) mode. To measure the array impedances, Intan RHS (Stim/Recording system, Intan technologies) was used. For single-electrode Electrochemical impedance spectroscopy (EIS) measurement, potentiostat (Reference 620, Gamry) was used with a 3-electrode system, and the result was analyzed by software (Framework, Gamry). Origin Pro software was used to generate graphs and heatmaps.

## 2.5 Accelerated aging test

PEDOT:PSS samples were prepared and soaked in phosphate-buffered saline (PBS). After positioning, the beaker containing PBS was sealed with Kapton film (0.25 mil thick, Durafilm). The beaker was put in the oven and kept at 67 °C. After soaking, the sample was taken out and cooled down to ambient temperature. After cooling down, the sample was taken out from the beaker and rinsed with DI water. The sample was then reassembled with a PCB, and impedance was measured.

# 3. Result and discussion

## 3.1. MEA preparation and PEDOT:PSS deposition

We fabricated arrays of 16, 64, and 128 microelectrodes (electrode pitch of 400  $\mu\text{m}$ ) and performed PEDOT:PSS deposition using potentiostatic electroplating. Each electrode opening size was set to  $10 \times 20 \mu\text{m}^2$  for this study. Potentiostatic deposition was conducted by applying a constant potential (with reference to a standard Ag/AgCl electrode) to all microelectrodes on a given array, where we temporarily short all contact pads by acetone-removable silver paste. We have determined the specific potential to be the optimal condition through prior experimentation. ~~Notably, potentials below 0.8 V yielded unsatisfactory deposition outcomes. At 0.8 V, only a minimal deposition was observed, suggesting that an extended duration would be required to achieve a resistance of 100 k $\Omega$  or less at 1 kHz. Conversely, at 0.9 V, the~~

deposition process exhibited an accelerated rate, proving challenging to control (Fig. S1). Within approximately 20 s, deposition was observed extending beyond the designated surface, prone to easy detachment. Consequently, the potential of 0.85 V was chosen as the optimal parameter. This potential not only facilitated ease of control but also allowed for the visual monitoring of deposition progress over a reasonable time. Furthermore, the Au oxidation potential we observed is slightly above 0.9 V and peaked at 1.2 V. Under this condition, 0.85 V for PEDOT:PSS is below the threshold for gold oxidation and exhibits optimal deposition control. 0.85 V potential was selected to finely tune the electroplating deposition rate, which is presumably crucial for the coating's morphology and adhesion. This decision was guided by our observations that at 0.8 V, marking the onset of EDOT oxidation and polymerization on the working electrode (Marzocchi et al., 2015; Teixeira et al., 2022), the deposition rate was minimal. Conversely, at 0.9 V, the deposition rate became excessively high, making it challenging to control the rate of deposition effectively. Optical microscopic images for both conditions are shown in Fig. S1.

Fig. 1 visualizes the method of potentiostatic deposition of PEDOT:PSS on a large-scale MEA and comparison between potentiostatic and galvanostatic methods, depicting the electroplating method of PEDOT:PSS via 3-electrode system, which enables comprehensive deposition across the entire set of electrodes (Fig. 1a). Fig. 1 visualizes PEDOT:PSS electroplating methods on a large-scale MEA, compares potentiostatic and galvanostatic approaches, and assesses their scalability. Fig. 1a presents a schematic representation of the electroplating process for PEDOT:PSS on microelectrode arrays (MEAs) using a three-electrode system. Here, two representative modes for electroplating were illustrated. In potentiostatic deposition, constant potential difference between WE and RE is controlled. Conversely, galvanostatic deposition pivots on the control of current, ensuring a steady current flow between the WE and CE. To highlight the advantage of the potentiostatic deposition approach, we compared the potentiostatic and galvanostatic methods with 128-ch arrays of the same design (Fig. 1b-df). In Fig. 1b, an optical microscopic image of 128-ch (32×4 matrix) is exhibited along with the images of electrodes on every 10th column. The potentiostatic deposition was carried out at 0.85 V and for 20 s, while the galvanostatic method used a 0.2 mA/cm<sup>2</sup> current density, a typical value for PEDOT:PSS coating on microelectrodes (Qiang et al., 2018). To compare these two methods fairly, we controlled the deposition charge density to be the same. Here, while potentiostatic samples showed electrodes with very similar colors of PEDOT:PSS for all electrodes, the galvanostatic

samples did not show uniform coatings. As a result, the average impedance of the microelectrodes from these two methods is similar after the deposition, achieving  $\sim 100$  k $\Omega$  and  $\sim 120$  k $\Omega$ , respectively, for the potentiostatic and galvanostatic methods. However, the galvanostatic deposited microelectrodes are much less uniform. The standard deviation values were 29.8 k $\Omega$  and 168.29 k $\Omega$ , for potentiostatic and galvanostatic methods, respectively, showing  $\sim 5.5\times$  difference. Furthermore, there are many electrodes with impedances over 600 k $\Omega$ , which is not suitable for high-performance neural interfacing. Therefore, potentiostatic methods achieve much better uniformity for large-scale MEA. To add statistical powers, we tested more samples ( $n=4$ ) for each condition (Fig. 1f). As a result, their average mean-impedances were similar (104.31 k $\Omega$  and 94.95 k $\Omega$  for potentiostatic and galvanostatic deposition, respectively), yet the standard deviation values were notably different (6.39 k $\Omega$  and 43.18 k $\Omega$ , respectively). This result implies that although the amount of deposition was similar, there was a significant difference in uniformity. Lastly, the scalability test was conducted using MEAs with electrode window sizes of  $5\times 5$ ,  $5\times 10$ , and  $10\times 10$   $\mu\text{m}^2$  (Fig. 1g-h) for both deposition methods. All samples prepared using potentiostatic deposition were processed under the conditions of 0.85 V for a 20-second deposition time. Conversely, samples created through galvanostatic deposition were ensured to get the same charge density. Potentiostatic deposition demonstrated superior uniformity, achieving a 100% deposition yield (percentage of electrodes successfully coated, cut-off impedance of electrode at 1 kHz: 500 k $\Omega$ ) and an average impedance of 132.45 k $\Omega$  with a standard deviation of 23.64 k $\Omega$  at  $5\times 5$   $\mu\text{m}^2$ . Conversely, galvanostatic deposition at the same size showed less uniformity (deposition yield: 37.5 %), with an average impedance of 172.65 k $\Omega$  and a higher standard deviation of 100.74 k $\Omega$ , indicating non-uniform coating of PEDOT:PSS. This indication was further vindicated by optical microscopic images (Fig. S2). These results highlight potentiostatic deposition's effectiveness for extremely scaled microelectrodes, essential for high-density neuroelectronic interfaces. Note that for Fig 1. b-h, all impedance values were collected at 1-kHz-frequency.

~~This significant difference in uniformity likely arises from the fact that the deposition rate of PEDOT:PSS across all microelectrodes on an array is primarily dominated by the polymerization reaction rate. From an energetic perspective, the reaction rate is related to the electron energy at the electrode, which is determined by its electrical potential. For the potentiostatic method, the electrode potential is set to be equal by the potentiostat among all microelectrodes, with negligible voltage dividing effect from parasitic resistances such as~~

from the interconnect of each microelectrode and the distance between the microelectrode to the CE. On the other hand, in the galvanostatic method, the electrode potential is not controlled among different microelectrodes and can be different from electrode to electrode due to their impedance variations, resulting in different deposition rates across electrodes.

To understand the difference between the two methods in depth, we examined the fundamental principles underlying the coating process in electroplating. To start with, we elaborate on the oxidation process of EDOT monomers and the deposition mechanism. The electroplating process initiates with the oxidation of EDOT monomers at the working electrode under a specifically applied voltage. This oxidation results in the formation of two activated monomers that subsequently react, release protons, and generate positively charged PEDOT chains. The negatively charged PSS ions crosslink with these PEDOT<sup>+</sup> chains, creating an electrostatically stable bipolymer film, as outlined by Mousavi et al. (2023). For potentiostatic deposition, in addressing the optimal conditions for voltage application during electroplating, our research leveraged the Butler-Volmer equation (1), which correlates the electrical current during electroplating with the applied voltage on the electrode.

$$j = j_0 \cdot \left\{ \exp \left[ \frac{\alpha_a z F}{RT} (V - V_{eq}) \right] - \exp \left[ -\frac{\alpha_c z F}{RT} (V - V_{eq}) \right] \right\} \quad (1)$$

Here,  $j$  is a current density,  $j_0$  is an exchange current density, which reflects the rate of an electrochemical reaction at equilibrium when the activation overpotential ( $V - V_{eq}$ ) is 0.  $V$  is an applied potential, whereas  $V_{eq}$  is an equilibrium potential.  $\alpha_c$  is a cathodic charge transfer coefficient and  $\alpha_a$  is an anodic charge transfer coefficient which is a value of  $1 - \alpha_c$ .  $z$  is the number of electrons involved in the electrode reaction,  $F$  is the Faraday constant, and  $R$  is the universal gas constant. This equation considers both anodic and cathodic reactions in one electrode, and total current density is a sum of currents from both anodic and cathodic reactions. At the low overpotential region, where the applied potential is close to the  $V_{eq}$ , the equation could be simplified to:

$$j = j_0 \cdot \frac{zF}{RT} (V - V_{eq}) \quad (2)$$

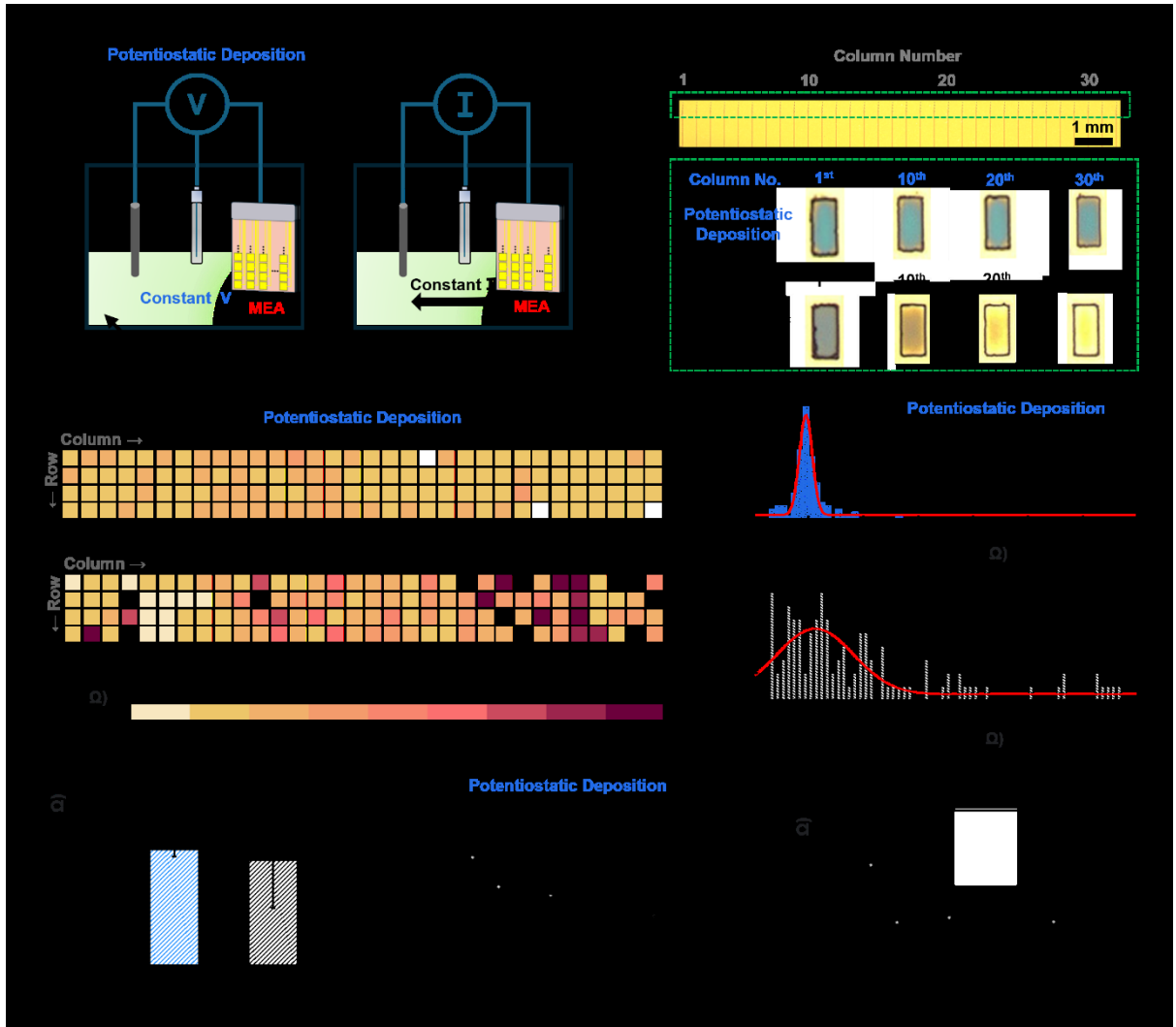
, where a linear relationship of overpotential and current density is formed. On the contrary, in high overpotential region, the Butler-Volmer equation is simplified to the Tafel equation, where the activation overpotential is substituted to:

$$V - V_{eq} = \pm A \cdot \log_{10} \left( \frac{j}{j_0} \right) \quad (3)$$

, with  $A$  as the Tafel slope (E. Katz, 2022). In this region, the current density increases at a much higher rate than that of the linear region, which is called the Tafel region. In both

regions, it is clear that the deposition current density is determined only by the electrode potential for a given deposition reaction at a given temperature. Therefore, for the potentiostatic deposition, due to the fact that all microelectrodes are applied with the same potential because of parallel connection via conductive paste on the MEA pad area, the same current density will be flowing through all microelectrodes during electroplating. Assuming all current contributes to the Faradaic reaction, this result will lead to the same deposition rate of PEDOT:PSS films across all microelectrodes (T. Niederhoffer et al., 2023). Therefore, array-wide uniform electroplating of PEDOT:PSS on all microelectrodes is achieved.

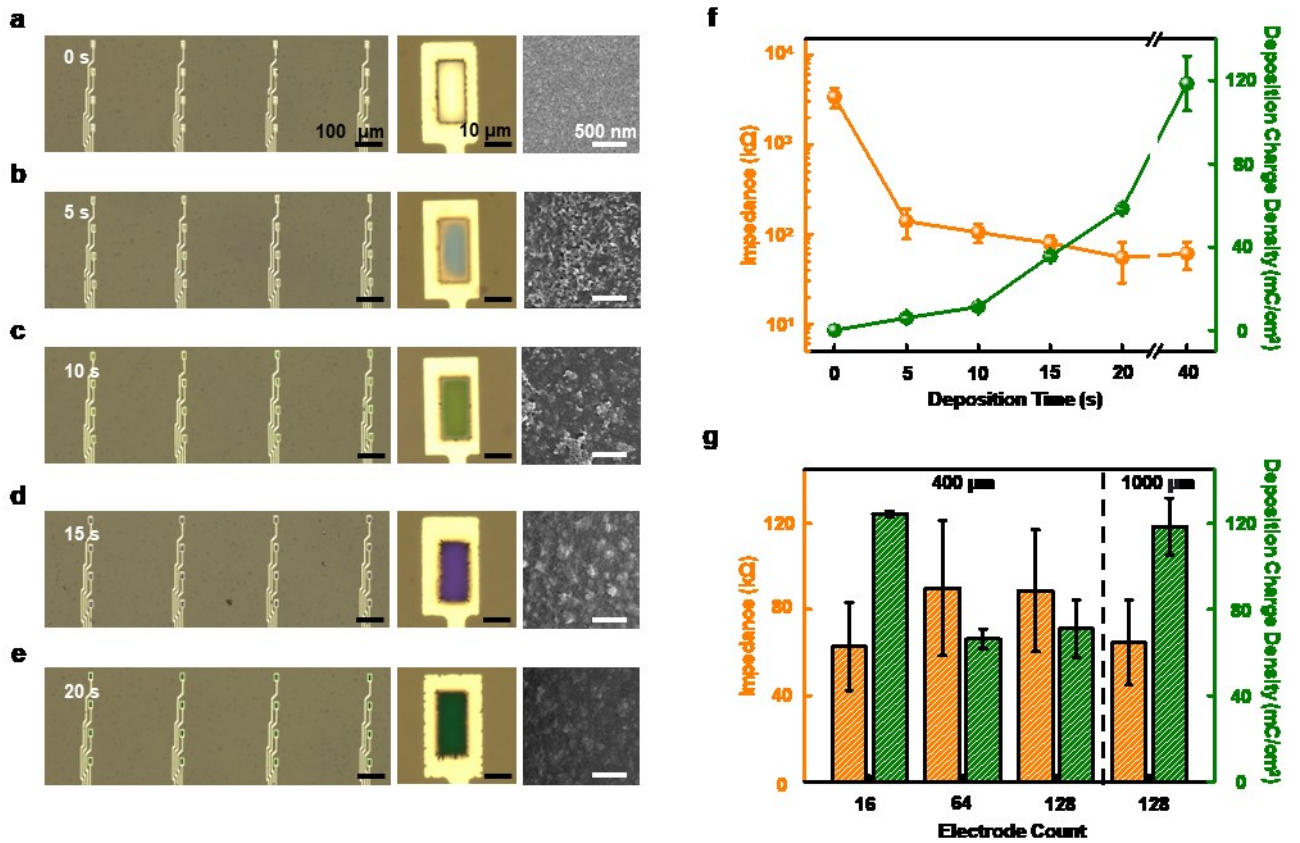
On the other hand, in galvanostatic deposition, the configuration only controls the total current from the working electrode to the counter electrode. Due to the parallel connection of all microelectrodes, the current density at individual microelectrodes can be different from each other, unlike potentiostatic (Fig. S3). This non-uniform distribution of current density is usually inevitable due to different values of microelectrode impedances across the MEA, possibly originating from electrode-to-electrode variations such as the opening area of electrodes, electrode surface roughness, and interconnect length. Electrodes with higher current density compared to others will deposit PEDOT:PSS faster on their surface. This faster deposition will, in turn, reduce the electrode impedance further and promote more current flow and, hence, deposition, thus creating positive feedback on the deposition. Therefore, in galvanostatic deposition, it would be difficult to achieve deposition uniformity of PEDOT:PSS across the large-scale MEA.



**Fig. 1. Demonstration of PEDOT:PSS electroplating on a large-scale MEA and scalability test results.** (a) Illustration of PEDOT:PSS electroplating in 3 electrode system and schematics of current density for potentiostatic and galvanostatic deposition in large-scale MEA. (b) Optical microscopic image of 128 ch MEA with magnified images ( $\times 400$ ) of electrodes after electroplating. Electrode area:  $10 \times 20 \mu\text{m}^2$ . (c) Impedance heatmap for potentiostatic deposition and galvanostatic deposition method. (d) Impedance histogram from potentiostatic deposition. (e) Impedance histogram from galvanostatic deposition. (f) Average mean-impedance of 128 ch MEA based on deposition methods. (g) Impedance and deposition yield (cut-off impedance:  $500 \text{ k}\Omega$ ) over various electrode opening areas in potentiostatic deposition, Electrode area :  $5 \times 5$ ,  $5 \times 10$ ,  $10 \times 10$ ,  $10 \times 20 \mu\text{m}^2$ . (h) Impedance and deposition yield over various electrode opening areas in galvanostatic deposition.

The electrode uniformity and surface color with respect to deposition time under the aforementioned conditions, accompanied by morphological changes were observed through scanning electron microscopy (SEM) analysis. In all instances, deposition progressed

independently, with coating evident in all conditions. Specifically, after 5 s (Fig. 2b), deposition is evident, yet full electrode coverage is not achieved, particularly at the edges. SEM analysis reveals partial exposure of the Au surface, indicating incomplete coating. With a total deposition time of 10 s (Fig. 2c), a complete electrode coating is observed, resulting in an overall color of light green. SEM further confirms uniform deposition across the entire electrode. At a 15-s deposition (Fig. 2d), the coating exhibits a shift in color to purple and further evolves into dark green after an additional 5 s deposition (Fig. 2e), with both microscopic and SEM analyses showing negligible lateral growth of PEDOT:PSS. Overall, the findings here suggest that uniform deposition of PEDOT:PSS on all electrodes is simultaneously achievable with potentiostatic deposition carried out at 0.85 V. Moreover, this deposition process is highly controllable by tuning the deposition time. At 0-s deposition (bare Au electrodes, Fig. 2a), since there was no deposition of PEDOT:PSS, we could only observe the surface of the gold film generated by E-beam evaporation. At 5-s deposition (Fig. 2b), deposition is evident, yet full coverage is not achieved. For the 10-s deposition (Fig. 2c), a complete coating is observed, resulting in an overall color of light green. From SEM, we could see that the surface of the electrode is fully covered with PEDOT:PSS. Here, cauliflower-like grains of coating were starting to form. According to V. Castagnola et al. (2014), PEDOT-rich regions are observed to be slightly brighter than other areas, enclosed in insulating PSS-rich shells. In 15-s deposition samples (Fig. 2d), the coating exhibits a shift in color to purple. From SEM, we could observe larger grains compared to 10-s deposition, with brighter images. This implies that the electrical conductivity increased with more deposition, possessing more PEDOT-rich morphologies. In the 20-s deposition (Fig. 2e), the color transitioned into dark green. From SEM, the image brightness was lower compared to 15-s deposition. This could be due to the charging effect of polymers, where the surface of polymers becomes less conductive because of excessive deposition even though it is conductive. Therefore, instead of dissipating the charge, it accumulates and shows darker contrast than the sample with previous deposition condition.



**Fig 2. Optical and electrochemical analysis of MEAs with various potentiostatic deposition times.** Optical microscopic and SEM images of MEAs with a deposition time of (a) 0 s (bare Au electrodes), scale bar: 100  $\mu\text{m}$  (left), 10  $\mu\text{m}$  (middle), 500 nm (right). (b) 5 s. (c) 10 s. (d) 15 s. (e) 20 s. (f) Impedance & total deposition charge over various deposition times for 16-ch electrode arrays (0, 5, 10, 15 and 20 s). (g) Impedance & total deposition charge over various array configurations (16, 64, 128-ch arrays with 400  $\mu\text{m}$  pitch and 128-ch arrays with 1000  $\mu\text{m}$  horizontal pitch), deposition condition: 0.85 V, 40 s.

### 3.2 Impedance analysis

From the potentiostatic deposition, it is apparent that the deposition charge increases as a function of increasing deposition time. Fig. 2f presents the impedance values at 1 kHz for a group of 16-ch samples with different amounts of PEDOT:PSS coating, together with the cumulative deposition charge. Before deposition, the bare Au microelectrode exhibits a 1-kHz impedance of approximately 2.5 M $\Omega$  for a 200  $\mu\text{m}^2$  site area. With progressing deposition from 5 to 20 s, the impedance decreases progressively from ~150 k $\Omega$  to ~60 k $\Omega$ . However, beyond 20 s, there is no tendency for the impedance to experience a significant decrease, as evidenced by remarkably similar values obtained from the 40-s deposition compared to 20 s. Meanwhile, the deposition charge density continues to increase over time. This phenomenon

1 indicates that, despite more coating, saturation is eventually observed in impedance (Dijk et  
2 al., 2020).

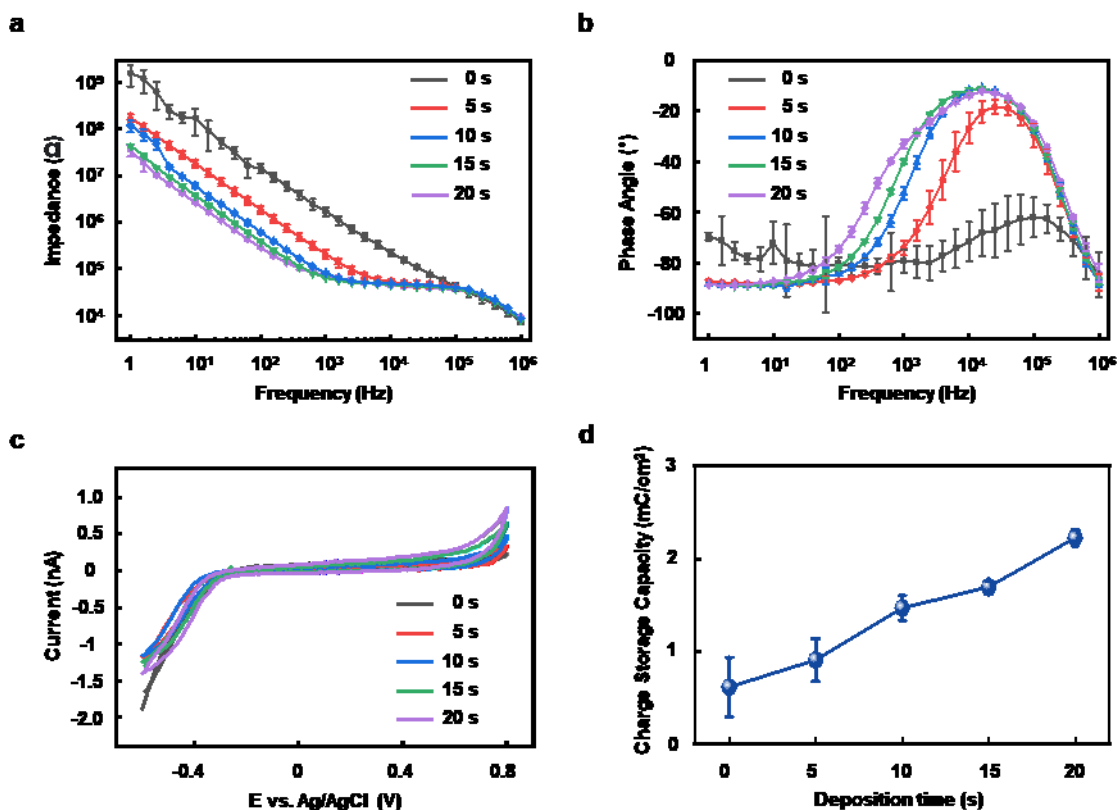
3  
4 We further investigated the effect of the total number of electrodes on the deposition  
5 (Fig. 2g). With the same potentiostatic deposition condition, we observed an electrode number  
6 effect where the deposition charge density overall decreases with the increasing electrode  
7 number, consequently leading 1-kHz-impedance to increase. This effect is presumably due to  
8 the reaction-diffusion process in the electroplating, where the diffusion process will be  
9 impacted by the electrode number and affect the overall deposition rate. This hypothesis is  
10 further validated by the fact that when comparing 128-ch arrays with a 400 and 1,000  $\mu\text{m}$   
11 horizontal pitch, the sample with a 1,000  $\mu\text{m}$  pitch had much lower impedance and higher  
12 deposition charge. As the electrode density decreases, the EDOT and PSS precursors can  
13 more quickly diffuse onto the microelectrodes, where they are consumed during the  
14 polymerization process. Although we have not tested the effects of electrode pitch less than  
15 400  $\mu\text{m}$  under the same electrode numbers in galvanostatic deposition, it is speculated that  
16 impedance also increases with a decrease in pitch. We expect the same reaction-diffusion  
17 competition to happen in galvanostatic deposition when the electrode pitch is reduced. Note in  
18 all conditions, uniformity among electrodes was ensured during visual check in the  
19 potentiostatic deposition.

### 21 3.3 Single electrode electrochemical characterization

22 To further analyze the properties of the PEDOT:PSS coating from potentiostatic  
23 electroplating, EIS and CV measurements were performed from 1 Hz to 1 MHz using a three-  
24 electrode setup with a potentiostat ( $n=4$ ). As expected, the impedance decreased significantly  
25 as a function of the PEDOT:PSS deposition time, especially between 1 Hz – 1 kHz (Fig. 3a).  
26 The trend of impedance decrease again slowed down with further deposition, which is  
27 consistent with previous observations (Fig. 3a, S42) From the phase spectra perspective, for  
28 the frequency below 1 kHz, the phase angle is  $\sim -90^\circ$  for all PEDOT:PSS coated cases (Fig.  
29 3b). This phenomenon implies that for this region, the electrode behaves mostly capacitive,  
30 and impedance is dominated by the capacitive coupling of the material (Wang et al., 2021).  
31 However, at higher frequencies, phase angle value decreases, and this effect is more  
32 prominent with longer PEDOT:PSS deposition time. This phase angle change is due to the  
33 efficient charge transfer at the PEDOT:PSS-electrolyte interface, lowering the charge transfer  
34 resistance. We also prepared and analyzed the EIS of the single electrode sample with the

galvanostatic method (Fig. S35). Overall, there is no qualitative difference in impedance or phase spectra comparing the potentiostatic with galvanostatic depositions, indicating similar PEDOT:PSS structures from these two methods. This similarity is not unexpected since the overall methodology of coating is the same electroplating for the two methods.

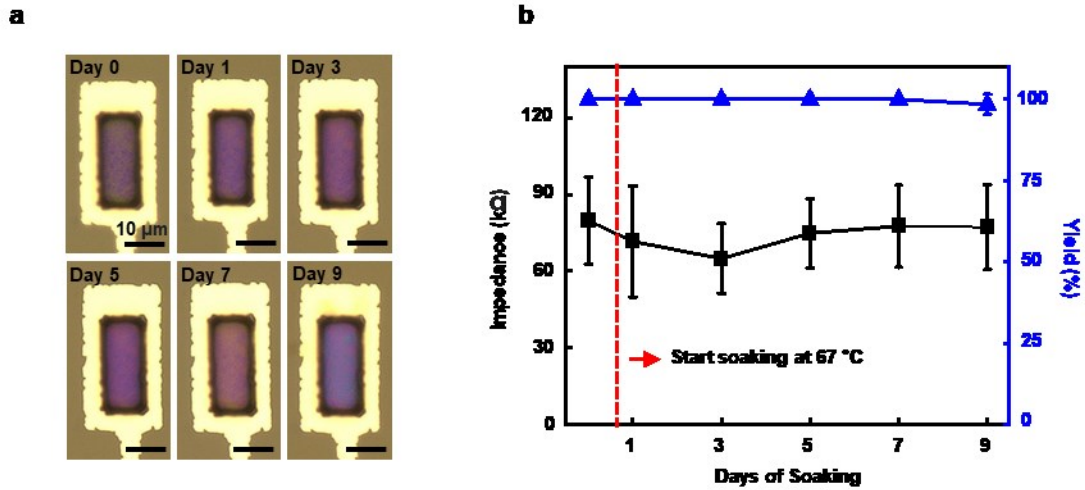
We also performed CV measurement, scanning between -0.6 V and 0.8 V, with a scan rate of 50 mV/s. As shown in Fig. 3c, PEDOT:PSS deposition onto the Au microelectrode increased its charge storage capacity (CSC), evidenced by the broader enclosed area of the CV curve. This CSC increase continued with more PEDOT:PSS was deposition, reaching  $\sim 2.2 \text{ mC/cm}^2$  at 20-s deposition (Fig. 3d). PEDOT:PSS coating tends to have micro- and nano-porous structures, increasing its effective surface area with increasing thickness. For galvanostatic method samples, the CSC value reached  $\sim 1.35 \text{ mC/cm}^2$ , which is between 5- and 10-s samples of the potentiostatic method. However, the CSC standard deviation of multiple samples from the galvanostatic method is larger ( $\sim 0.32 \text{ mC/cm}^2$ ) compared to that of from the potentiostatic method (0.23 and  $0.13 \text{ mC/cm}^2$  for 5- and 10-s samples, respectively) due to bigger sample-to-sample variation from the galvanostatic method.



**Fig. 3. Single electrode electrochemical characterization as a function of various deposition times (0, 5, 10, 15 and 20 s).** (a) Bode plot of impedance. (b) Bode plot of phase angle. (c) CV. (d) CSC.

### *3.4 Chronic stability tests*

For many microelectrode applications, chronic stability is important to ensure stable recording and stimulation performance. To this end, we prepared arrays of microelectrodes with potentiostatic PEDOT:PSS deposition and soaked them in PBS to monitor their impedance and yield changes (Fig. 4). We define yield as a percentage of the number of electrodes with measured impedance that does not exceed the cut-off values out of the total number of electrodes. We set the value of the cut-off impedance as 500 k $\Omega$ , a typical value in neuroelectronic applications (Park et al., 2014). To accelerate the aging tests, the soaking temperature was set as 67 °C, offering an acceleration factor of 8 compared to 37 °C, assuming an accelerated aging factor  $Q_{10}$  of 2 (Hukins et al., 2008). Fig. 4a shows the images of the same electrode by days of soaking, from day 0 to day 9. Here, we used 15-s deposition condition for chronic stability evaluation since this showed the most promising electrochemical performance. More accelerated aging test results are described in Fig. S46 and S57. Through the optical microscope, we observed no delamination or other damage to PEDOT:PSS coating over time, as well as no significant change in the color. For the adhesion performance, we compared our results with other research results. Qiang et al. (2018) performed a chronic soak test for 5 weeks in 37 °C PBS, while we demonstrated that we could achieve 8 days in PBS, 67 °C, which is equivalent to 72 days in 37 °C with near 100 % adhesion of the film. Furthermore, our results are similar to Luo et al. (2011) where they performed chronic soaking of the film for 3 months at 37 °C with PEDOT/CNT. Therefore, it is evident that our adhesion is comparable with studies conducted so far. These observations are consistent with the impedance and yield trends, where both the impedance and array yield remained highly stable over the entire soak duration (Fig. 4b). This stability of 9 days at 67 °C is equivalent to 72 days at 37 °C, suggesting that potentiostatic coated PEDOT:PSS is promising for chronic applications.



**Fig. 4. Chronic stability of PEDOT:PSS with potentiostatic deposition.** (a) Optical microscopic images of PEDOT:PSS with 15-s deposition time at different soaking days at 67 °C (scale bar: 10 μm). (b) 1-kHz electrode impedance and array yield (cut-off impedance: 500 kΩ) as a function of soaking days.

#### 4. Conclusion

In summary, we successfully demonstrated array-wide uniform coating of PEDOT:PSS on arrays of up to 128 microelectrodes using the potentiostatic deposition method. Time-dependent studies revealed that this deposition process is highly controllable, with rates dependent on the electrode throughput and density. In this study, we have successfully demonstrated (1) the capability of achieving array-wide uniform electroplating of PEDOT:PSS on large-throughput (>100 channels) microelectrode arrays (MEAs) with electrode sizes downscaled to the cellular level; (2) a detailed comparison that elucidates the similarities and differences between potentiostatic and galvanostatic electroplating methods in depositing PEDOT:PSS on large-scale MEAs; and (3) a deeper understanding of the electrochemical origins that underlie the array-wide uniformity and enhanced scalability offered by the potentiostatic electroplating method. Through EIS and CV analyses, we found that there was no significant difference in PEDOT:PSS electrochemical properties between the two methods, although galvanostatic deposition samples led to a larger sample-to-sample deviation. Lastly, accelerated aging tests in PBS revealed that PEDOT:PSS from the potentiostatic coating was stable for 9 days at 67 °C. Overall, our study established potentiostatic electroplating as a new coating method for large-scale MEA to achieve uniform PEDOT:PSS coating, with broad applications in bioelectronics. We envision facilitating our

novel electroplating method to enable the electroplating of large-scale MEA, surpassing a count of 1,000 channels, with the goal of expanding its application to active devices.

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